Time	Title	Speaker
	INTRODUCTION	
13:00 - 13:30	OMI Overview	Allan Cantle
	RESOURCES	
13:30 - 13:45	Overview of online Resources, Website, Specs, Github, etc	Bob Szabo
13:45 - 14:05	Overview of Transaction Layer and Data Link Layer Specifications	Bruno Mesnet
14:05 - 14:10	Smart Modular - OMI Memory Module Details	Pekon Gupta
14:10 - 14:30	OMI Development Kit	Thao Nguyen / Scott Burns
	Transitioning from DDR to OMI - Part 1	
14:30 - 15:00	Connecting your ASIC or FPGA design to OMI attached Memory	Bruno Mesnet
	BREAK	
15:00 - 15:15	Coffee Break	
	Transitioning from DDR to OMI - Part 2	
15:15 - 15:35	Getting Started with OMI: Memory Controller implementation Example	Bruno Mesnet
15:35 - 15:55	OMI Verification using SmartDV	Bipul Talukdar
	EXAMPLES	
15:55 - 16:15	IBM's P10 & Z16 Systems - OMI Lineage, Implementation, Features & Importance of Latency	Bill Starke
16:15 - 16:25	IBM's Memory Inception	Baba Arimilli
16:25 - 16:35	OMI in a Composable World - A Software Perspective	Andreas Grapentin / Lukas Wenzel / Sven Köhler
16:30 - 16:45	Top of Rack Shared Memory Pooling using OMI	Thao Nguyen / Scott Burns
	FUTURE	
16:45 - 17:00	OMI & CXL composable Memory Centric Computing Vision	Allan Cantle