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Evatronix to Demonstrate the Future of Video Streaming at the Flash Memory Summit

Combination of two high-end IP cores from Evatronix – ONFi 3.0 NAND Flash controller and the PANTA DP20 Display Processor – enables users streaming up to 3 video channels with total data throughput of 160 MB/s.

Santa Clara, USA – August 7th, 2012 - Evatronix SA, provider of robust IP for a variety of applications, announced today a super-fast video streaming application for high definition TV. Two Evatronix IP cores, the NAND flash memory controller and the PANTA DP20 PRocessor, process video data stored in ONFi 3.0 memories from Micron at an astounding speed of 160 MB/s. Thanks to advanced processing capabilities of the PANTA DP20, users can display up to 3 video- and image-layers in one composition.

"There is no better place to show what flash technology is capable of than the Flash Memory Summit," said Arkadiusz Buchalik, Memory Controllers Manager at Evatronix. "Video streaming is one of most vivid examples how to exploit the extra performance of ONFi 3.0 memories, and matching them with our PANTA DP20 processor enables users to display multiple HD video streams at once, including the ARM TrustZone compatible secure layer."

The latest iteration of the NAND Flash Controller features numerous performance enhancements, like an effective Error Correction Code, hardware support for Bad Block Management, a scatter-gather DMA, Super Sequence and Command queue. Combined with proprietary SDLL NAND Flash PHY IP, the controller supports most popular memory devices (Micron, Toshiba, Samsung) in all available technology nodes.

The PANTA DP20 Display Processor off-loads the general purpose central processing units (CPUs) from power-consuming video processing tasks, like image/video layer composition, YUV to RGB conversion, alpha-blending and gamma correction. It supports a variety of display standards and features programmable resolutions of up to 8191x8191 pixels. The PANTA DP20 supports numerous technologies from ARM – TrustZone, frame buffer data compression and AMBA AXI4 – for straightforward implementation in high-end

mobile/portable products built around ARM Cortex™ processors and MALI™ GPUs.

To see the demo live, visit Evatronix at the Flash Memory Summit at booth #915 or show up at the Evatronix hosted expert table on Tuesday, August 21st between 5.30 and 7 PM.

Other Evatronix activities at FMS include its Technical Marketing Manager to provide an overview of Error Correction Codes (Thursday, Aug 23rd, 8.30 AM, session 301-C) and an Evatronix prototyping board being on display in the ONFi booth, #918.

ABOUT EVATRONIX

Evatronix develops digital and mixed-signal Intellectual Property (IP) cores with complementary software and supporting application environments. We embrace hardware, software and design services elements to assist our customers in all SoC development stages, from concept to tape-out. Over a period of more than 20 years, Evatronix provided over 500 licenses for 8051, USB, NAND Flash, SDIO and multimedia solutions. We are headquartered in Poland, and employ more than 90 people worldwide.

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