IP-Maker to exhibit at Flash Memory Summit 2013, Booth #218

Low latency and high throughput NVMe reference design

August 6, 2013, Aix en Provence, France – IP-Maker will exhibit (Booth #218) at Flash Memory Summit, Santa Clara, CA, from August 13 to 15. IP-Maker strengthens its leader position in NVM Express technology and continues its development effort in higher performance solutions. The advanced NVMe IP core provides efficient operations, therefore leveraging the performances of the next PCIe SSD generation.

"We are excited to come back at this major event for the storage market where NVM Express is becoming one of the hottest topics", said Mickael Guyard, Product Marketing Director. "We will be happy to educate visitors on NVM Express technology thanks to our strong experience in this area."

IP-Maker's NVMe IP core is based on an automatic command processing unit and a multi-channel DMA engine. That leads to a ultra-low latency and to a very high throughput, reaching the million IOPS class. This scalable architecture has been designed to greatly reduce the power consumption thanks to the optimized gate count implementation.

The latest reference design that will be showcased at Flash Memory Summit is based on a FPGA platform. It includes the powerful NVMe IP core, integrating the automatic command processing unit and multi-channel DMA engine technologies. This reference design is available for evaluation purpose.

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About IP-Maker
IP-Maker is a leader in Intellectual Properties (IP) for high performance storage applications. IP-Maker's NVM Express (NVMe) technology provides a unique hardware accelerated solution that leverages the PCIe SSD performances, including ultra-low latency and high throughput. IP-Maker is a contributor to the NVMe specification. The ASIC & FPGA IP portfolio includes NVMe, Universal NandFlash Controller and ECC IP cores. The combination of the IP-Maker technology and its associate services leads to a reduced time-to-market.

About NVM Express
NVM Express is an optimized, high performance, scalable host controller interface with a streamlined register interface and command set designed for Enterprise and Client systems that use PCI Express* SSDs. NVM Express was developed to reduce latency and provide faster performance with support for security and end-to-end data protection. Defined by 80+ NVM Express Work Group members, the specification, published in March, 2011, provides a flexible architecture for Enterprise and Client platforms.