



Session T2A

New Approaches to Non Volatile Memory Subsystems

Session Moderator

First Half – Debasis Baral

Second Half – Ian Ferguson



AGENDA

Topic Presenter, Affiliation	Time
Introduction to Session Debasis Baral, Samsung Information Systems	2:40
NVMHCI: The Optimized Interface for Caches and SSDs Amber Huffman, Intel	2:45
ONFI 2.0 - The Compatible NAND Flash Interface Michael Abraham, Micron	3:05
New Storage Devices Utilizing Flash Memories and Hard Disk Drives Debasis Baral, Samsung Information Systems	3:25
Break	3:45
HLNAND: A New Standard for High Performance Flash Memory Peter Gillingham, MOSAID Technologies	4:00
Non-Volatile Memory Cache Enhancements: Turbo-Charging Client Platform Performance Rob Larsen, Intel	4:20
Addressing System Challenges for Next Generation SSD SoCs Ian Ferguson, ARM	4:40
Q&A	5:00