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Flash Memory Summit

SPI Flash Memory: Future Trends and Applications

August 2009

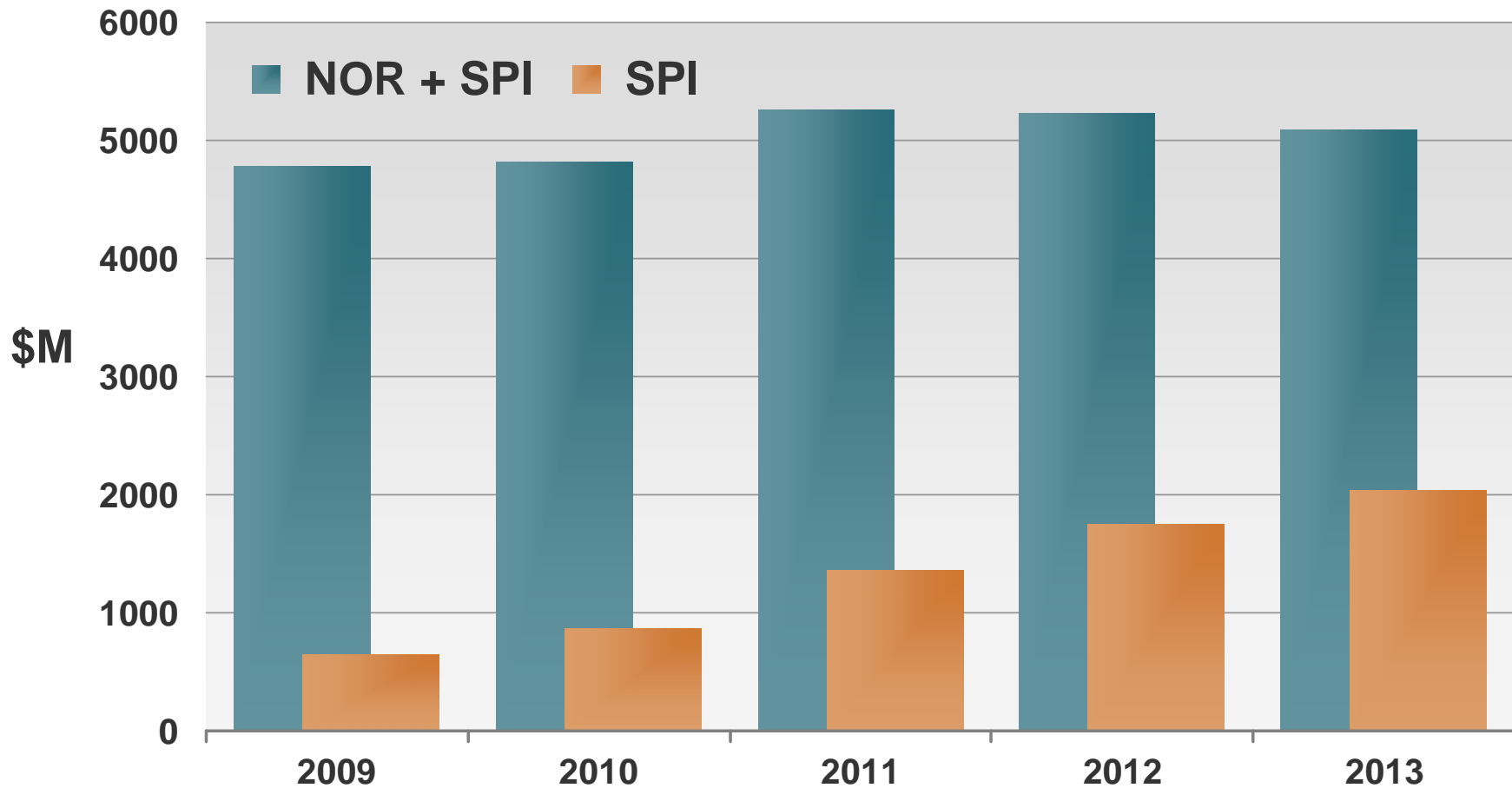


FLASH FORWARD



SPI Adoption Trend

NOR TAM

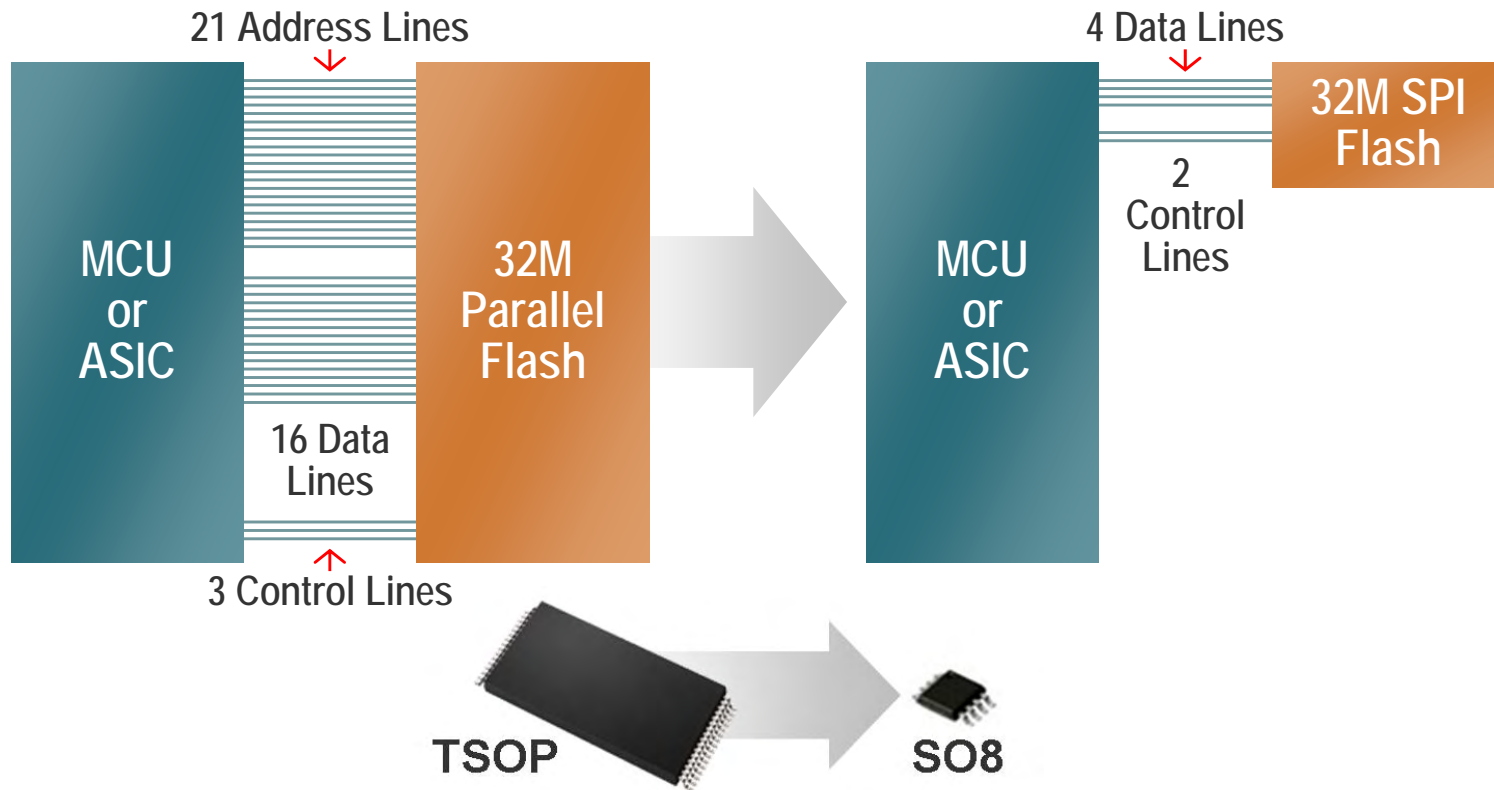


SPI at 40% of NOR TAM by 2013
33% CAGR growth driven by Mid-High density Multi I/O

Source: WebFeet August, 2009

Market Drivers

Advantage of SPI NOR Interface

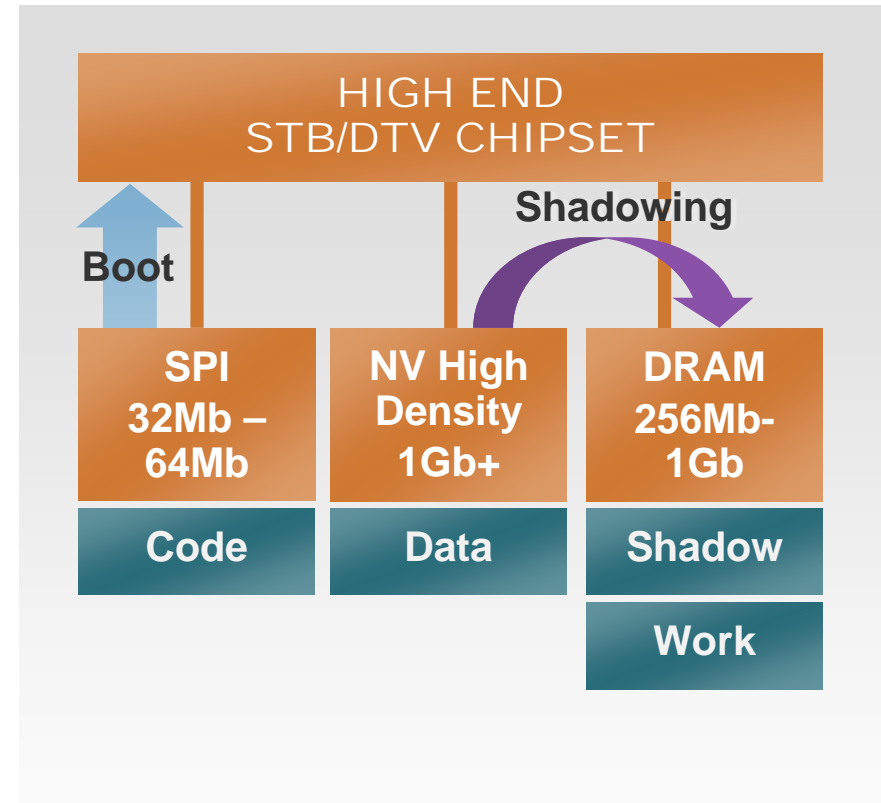
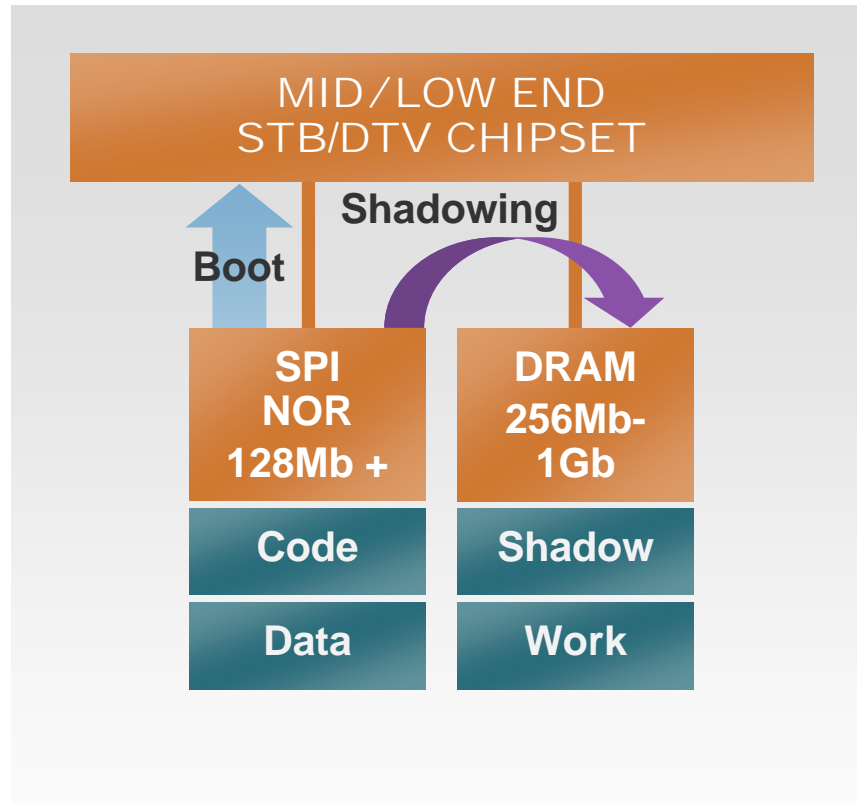


Package Type	SO8	TSOP48
Package Dimensions	30mm ² (5x6)	240mm ² (12x20)
Number of pads	8	48

Over 80% reduction in package size and pin count

Memory Subsystems

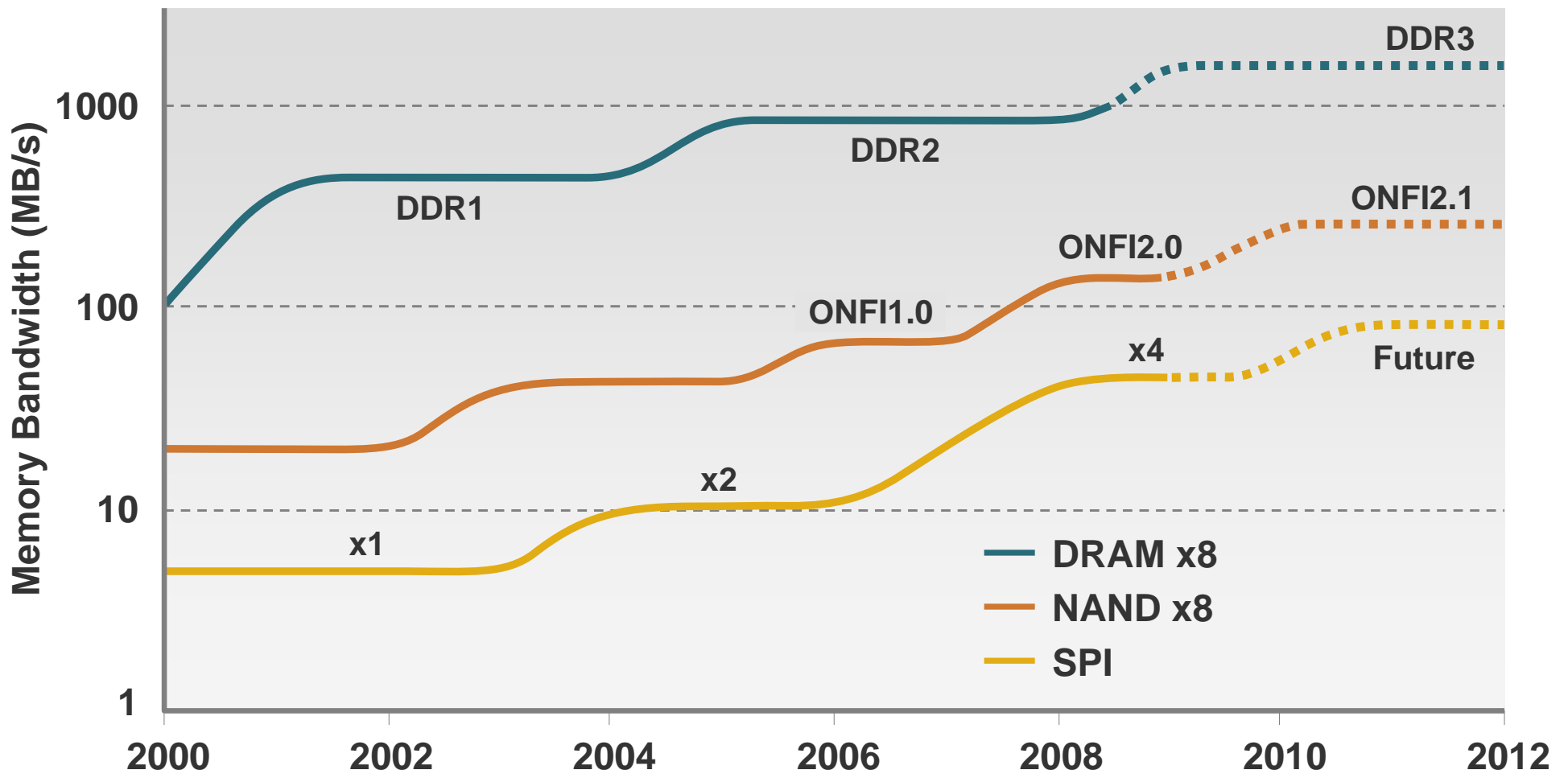
SPI NOR Adoption in High Growth Markets



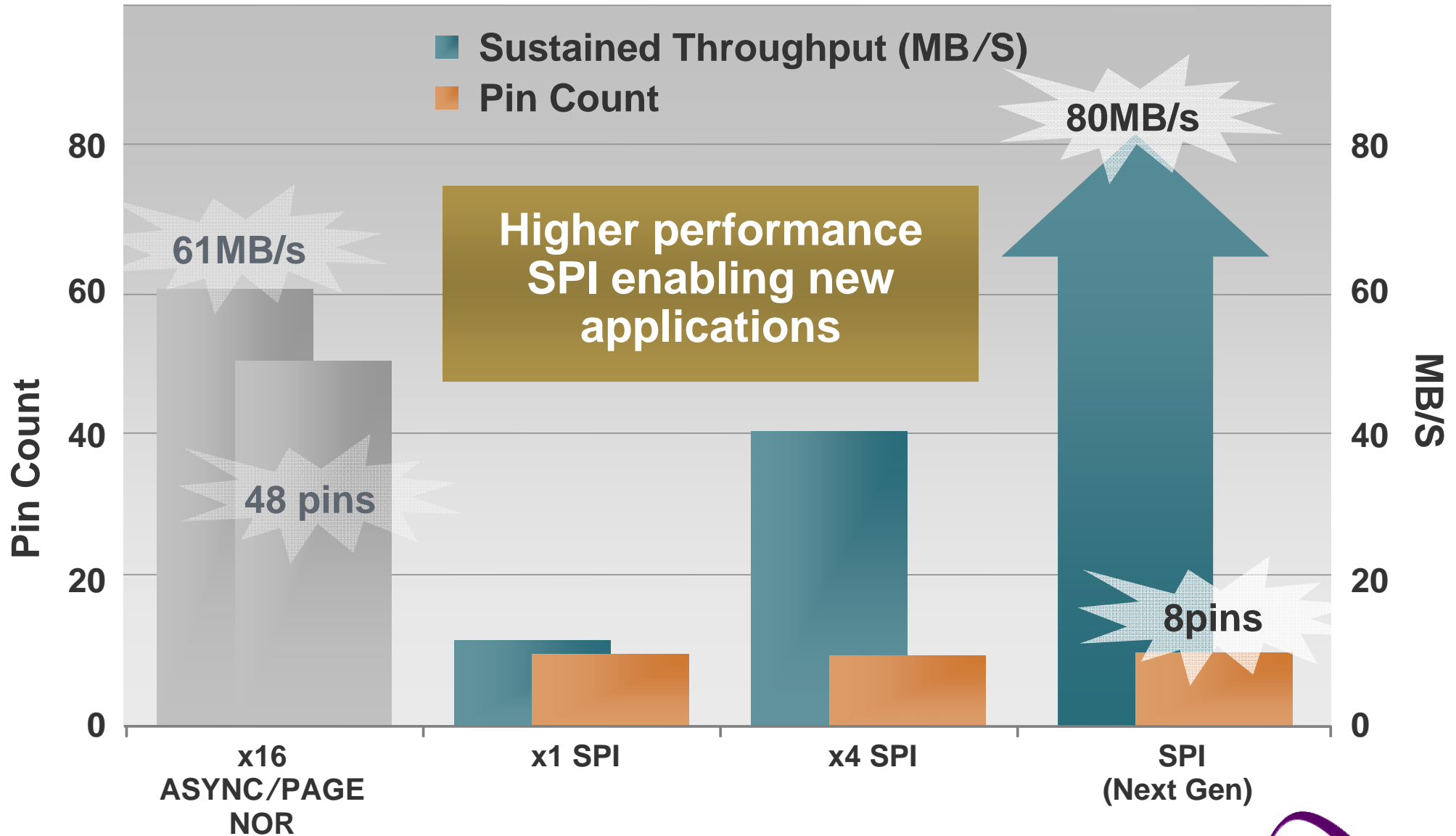
SPI Multi-IO gaining momentum in new usage models

Memory Performance Evolution

Memories Bandwidth Over Time



Performance Comparison 3V NOR Flash Memory Solutions



Application Performance Requirements





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