



# Tutorial 2A: New Approaches to Flash Memory Subsystems

Chairperson: John Geldman, Lexar Media  
jgeldman @ lexar.com

Organizer: Stephan Rosner, Spansion  
stephan.rosner @ spansion.com

Audio-Visual Sponsor

**SanDisk**<sup>®</sup>



## Agenda

- Extending the NVMeHCI Standard to the Enterprise
  - Amber Huffman, Intel
- Improving System Performance and Longevity with a New NAND Flash Architecture
  - Jin-Ki Kim, MOSAID
- 3Bit Per Cell NAND Flash
  - Terry Grunzke, Micron Technology
- Toggle-mode NAND to Fill Growing Need for Higher Performance
  - Harry Yoon, Samsung Semiconductor
- NAND Flash Architecture and Specification Trends
  - Michael Abraham, Micron Technology

Audio-Visual Sponsor

**SanDisk®**



# EXTENDING THE NVMHCI STANDARD TO THE ENTERPRISE

Amber Huffman, Intel

Audio-Visual Sponsor

**SanDisk®**



# IMPROVING SYSTEM PERFORMANCE AND LONGEVITY WITH A NEW NAND FLASH ARCHITECTURE

Jin-Ki Kim, MOSAID

Audio-Visual Sponsor

**SanDisk®**



# 3BIT PER CELL NAND FLASH

Terry Grunzke, Micron Technology

Audio-Visual Sponsor

**SanDisk®**



# TOGGLE-MODE NAND TO FILL GROWING NEED FOR HIGHER PERFORMANCE

Harry Yoon, Samsung Semiconductor

Audio-Visual Sponsor

**SanDisk**<sup>®</sup>



# NAND FLASH ARCHITECTURE AND SPECIFICATION TRENDS

Michael Abraham, Micron Technology

Audio-Visual Sponsor

**SanDisk**<sup>®</sup>