HIGH THROUGHPUT ENCRYPTION FOR FLASH MEMORIES - Session 104

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Summary
Flash memories enabled compact storage application with data rates measured in tens of gigabits per second. Modern encryption technology provides adequate means for at-speed encryption of the stored data at the cost of 30-50 thousands of ASIC gates. Presenter will discuss the choice of algorithms and silicon resources required by the high-speed cryptography.

Biography
Prior to joining IP Cores in 2005, Dmitri was a Director of Software Development at Resonext Communications and Adaptive Telecom startups. He received his PhD in computer science from the Moscow State University (Russia).

Copies of the presentation are available at www.ipcores.com/images/FlashSummit2009.pdf or by e-mailing dima@ipcores.com.

About IP Cores
IP Cores, Inc. specializes on IP cores for semiconductors, primarily in the security and cryptography area as well as DSP cores. Founded in 2004, it has offices in Palo Alto and San Diego.