Extracting Unique Fingerprints From Flash Memory Devices

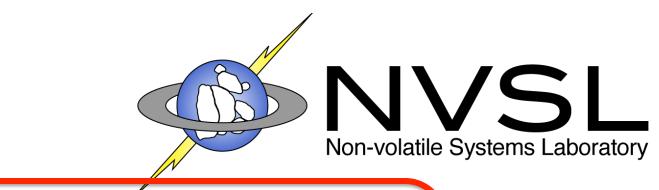
Pravin Prabhu¹, Ameen Akel¹, Laura M. Grupp¹, Wing-Kei S. Yu², G. Edward Suh², Edwin Kan², and Steven Swanson¹

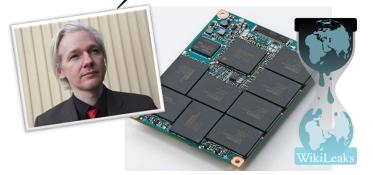
¹Non-volatile Systems Laboratory, UCSD CSE ²Cornell University







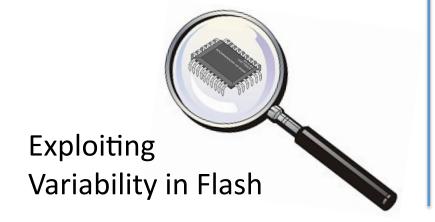


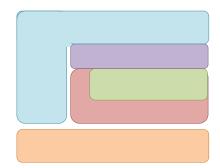


Trust and Security

Hardware/Software Prototyping







Programming interfaces

The Flash Juggernaut









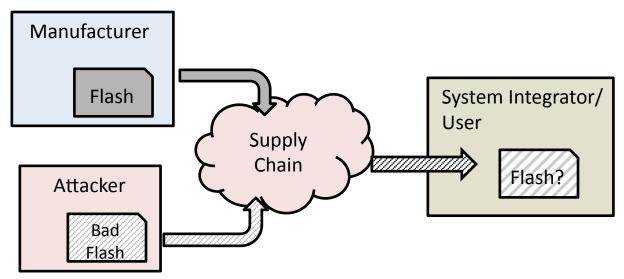






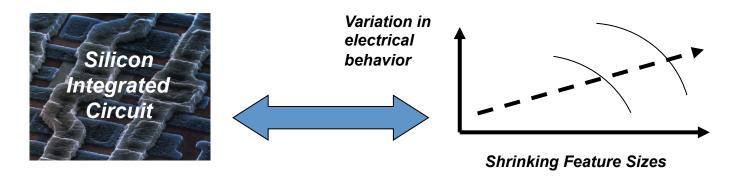
Flash Device Authentication

- Can we authenticate each flash chip?
 - Distinguish genuine flash chips from counterfeits
 - Authenticate a device with a flash chip





Physical Unclonable Functions (PUFs)



- Because of random manufacturing variations, no two Integrated Circuits are identical - even those using same mask
 - Hard to remove or predict in advance
 - Relative variation increases as feature sizes shrink
 - Variation persists, despite \$ billions spent to control it
- We can generate fingerprints from unique analog characteristics of each
 IC: Response = PUF(Challenge)
 - Inexpensive; intrinsic to each device; effectively unclonable
- This work introduces a PUF based on flash chips

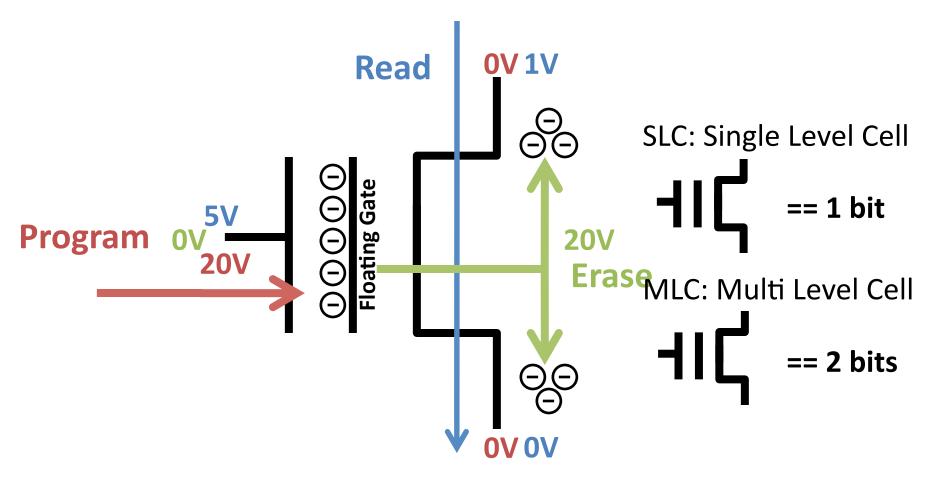


Outline

- Flash memory overview
- Experimental infrastructure
- Flash-based Physically Unclonable Functions (FPUFs)
 - Usage Model
 - Desiderata
 - Our FPUFs
- Conclusions

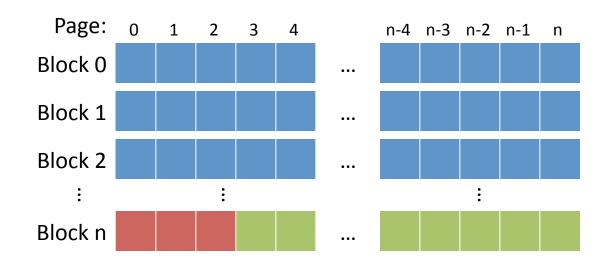


Flash Operations





NAND Flash Basics



Erase Blocks

Program Pages



Flash Failure Mechanisms

- Program/Erase (PE) Wear
 - Permanent damaged to the gate oxide at each flash cell
 - After 3000 (MLC) 100,000 (SLC) PE cycles, a cell becomes unreliable
- Program disturb
 - Data corruption caused by interference from programming adjacent cells.
 - No permanent damage



Experimental Setup

- Custom-Built
 Daughter Board
- Xilinx XUP Board
- EZ to integrate similar capabilities into existing systems





The Test Subjects

Chip Name	Node (nm)	Bytes Page	Pages Block	<u>Planes</u> Die	Dies	Chip Name	Node (nm)	Bytes Page	<u>Pages</u> Block	<u> </u>
A-SLC2		2048	64	2	1	A-MLC16		4096	128	
A-SLC4		2048	64	1	1	B-MLC8	72	2048	128	
A-SLC8		2048	64	2	1	B-MLC32	50	4096	128	
B-SLC2	50	2048	64	1	1	B-MLC32-2	34	4096	256	
B-SLC4	72	2048	64	2	1	B-MLC128	34	4096	128	
E-SLC8		2048	64	1	2	B-MLC128-2	34	4096	256	
				200		C-MLC64	43	8192	128	



Planes

Die

Dies

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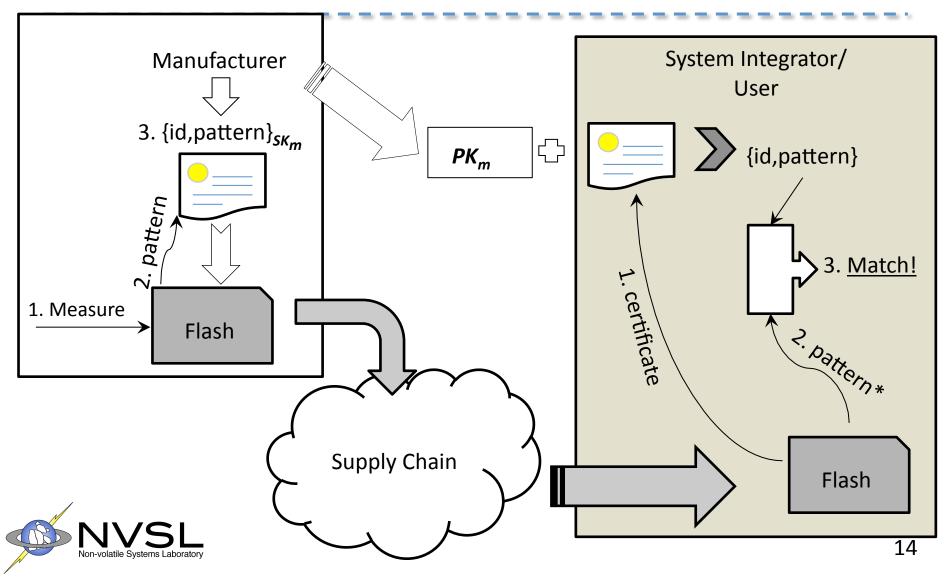


Flash-based Signatures

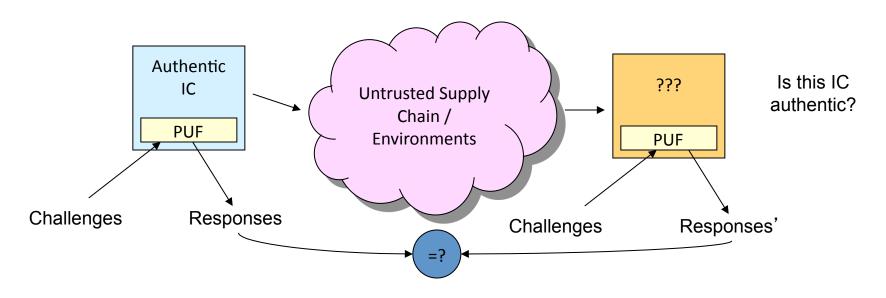
- Cell-level variation in flash devices makes each chip unique.
- Unique, unforgeable flash chip signatures have several uses
 - Device identification
 - Supply chain integrity



Authentication Model



Challenge-Response Based Authentication



- Create CRPs for IC with PUF when IC is in your possession
- Use CRPs to subsequently authenticate IC throughout the supply-chain and post-deployment
 - Use each CRP only once → prevent "replay"



Signature Characteristics

- Selectivity an FPUF should be able to reliably distinguish between flash devices
- Speed Computing an FPUF should be fast
- "Unforgeable" It should be prohibitively difficult to forge the FPUF
- Non-Destructive Extracting an FPUF should not wear out the flash device.



Basic Recipe for an FPUF

1.	Identify an aspect flash chip behavior that
	varies based on manufacturing inconsistencies

- Measure the variation at a bit, page, or block level
- Use the sequence of measured values as a 3. signature
- Use statistical correlation to determine whether Signatures two signatures are for the same device.

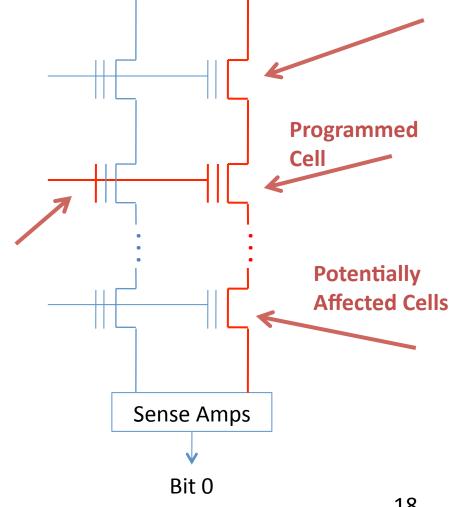
Chip #1	Chip #2
4	3
3	8
8	3
	•••

Correlated?



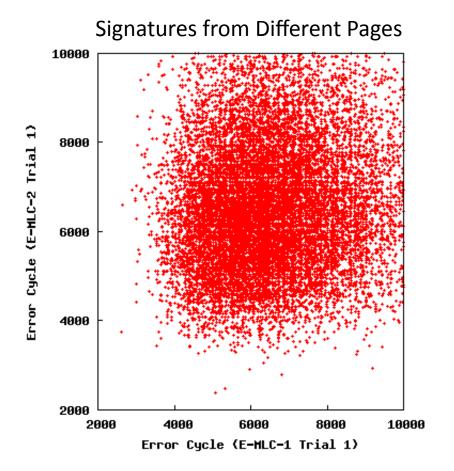
Program Disturb FPUF

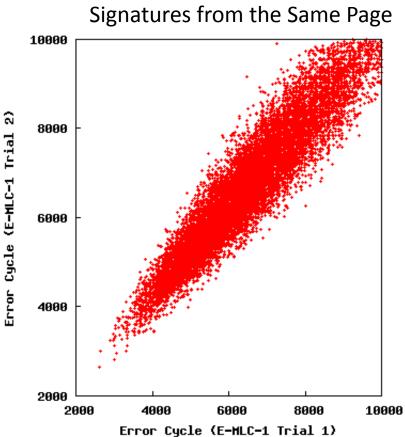
- Program one page repeatedly
- For each bit in the adjacent page
 - How many programs before the bit flips?
- The sequence of counts is a signature.





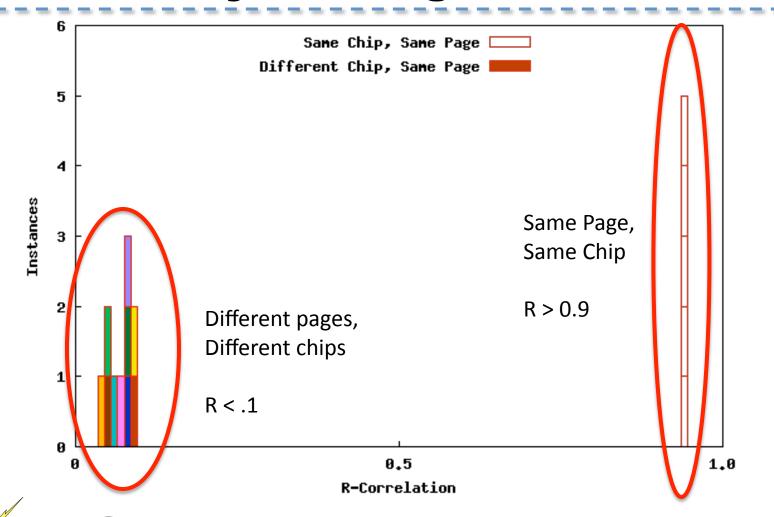
Signature Selectivity







Selectivity for Program Disturb



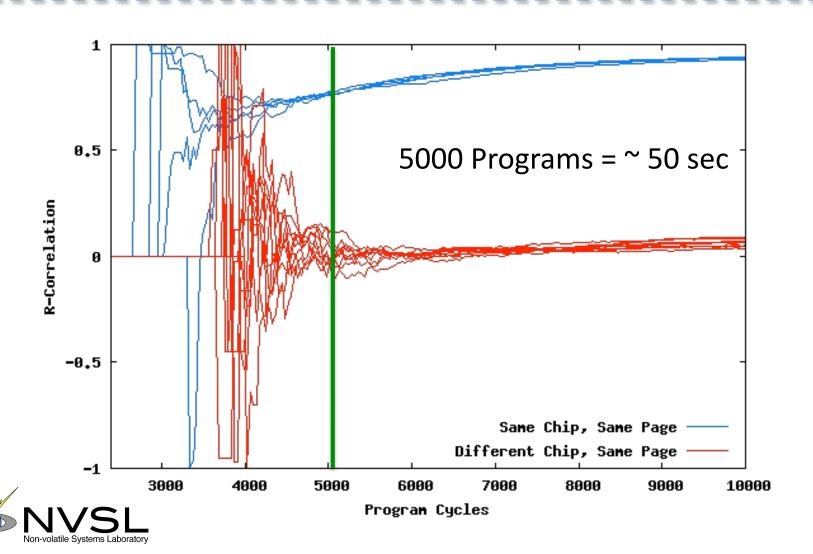


Program Disturb Latency

- Extract an program-disturb signature takes up to 5 minutes
 - Some usage models require many signatures from each chip
 - 5 minutes is prohibitively slow in these cases.
- Can we extract a useful signature with fewer program operations?



Reducing Programs/Signature



Forging Program Disturb FPUFs

- Forging an FPUF would require storing the signature in the flash device
- If the signature contains more than one bit of information per flash cell, storing the signature in the chip is not possible.
- However, our signatures are noisy, so precise forgery is not required.
 - It is possible to lossily compress signatures



Compressing the Signatures

- Raw signatures need 10 bits of program count information per flash cell
- We can quantizing program counts in to 4 values (i.e., the top two bits)
 - Quantized signatures correlate well (R = 0.8) with raw signatures
- The quantized signatures are not very compressible (entropy/bit is near 1)
 - It is still impossible to store the signature for every page in a flash chip



Program Disturb FPUF

- Selectivity: Very Good
- Speed: 1-5 Minutes per page
- Wear: 10,000 programs of the target page
- Forgeability: low

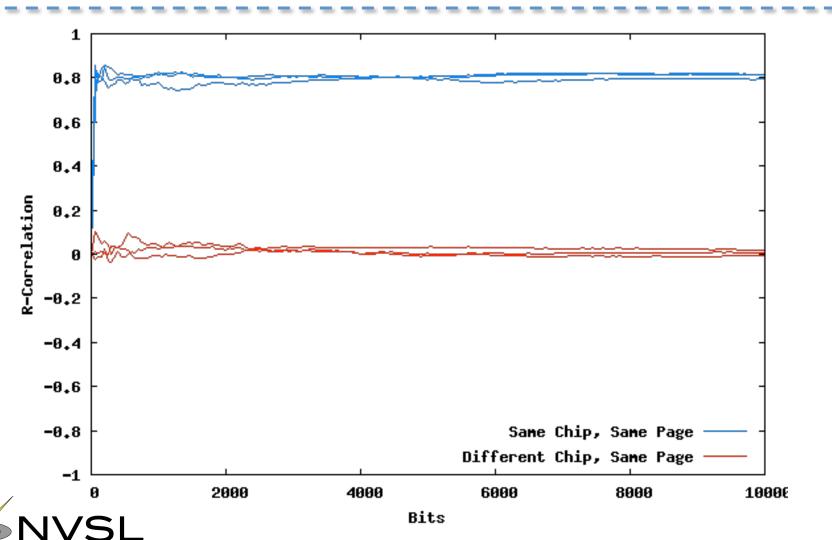


Per-bit Program Latency FPUF

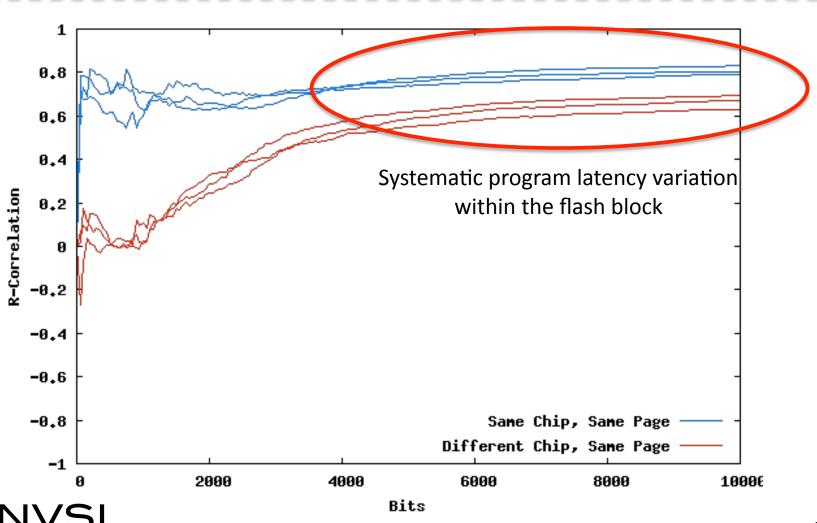
- Individual cells have different programming characteristics
- The chips only program bits that change
- We can measure per-bit program latency by programming one bit at a time.
 - Program bit 0 in page 0, bit 1 in page 1, etc.
 - The sequence of program latencies is the FPUF result



Program Latency FPUF Correlations (SLC)



Program Latency FPUF Correlations (MLC)



Per-bit Program Latency FPUF

Selectivity: Good

• Speed: 1-20s

Wear: 1 PE cycle

Forgeability: High



Other FPUFs

- Usable FPUFs
 - Per-bit program latency
 - Read disturb
- Unusable FPUFs
 - Per-block erase latency
 - Per-page read latency
 - Full page program latency (rather than bit-by-bit)



Conclusions

- FPUFs can provide a robust mechanism for identifying individual flash devices.
- Flash's ubiquity makes them an attractive method for device identification
 - Inexpensive
 - Easy to implement
- FPUFs will become even more useful as flash manufacturing variation grows







Questions?