



# Next Frontier in SSD Performance

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**STG**  
Storage Technologies Group

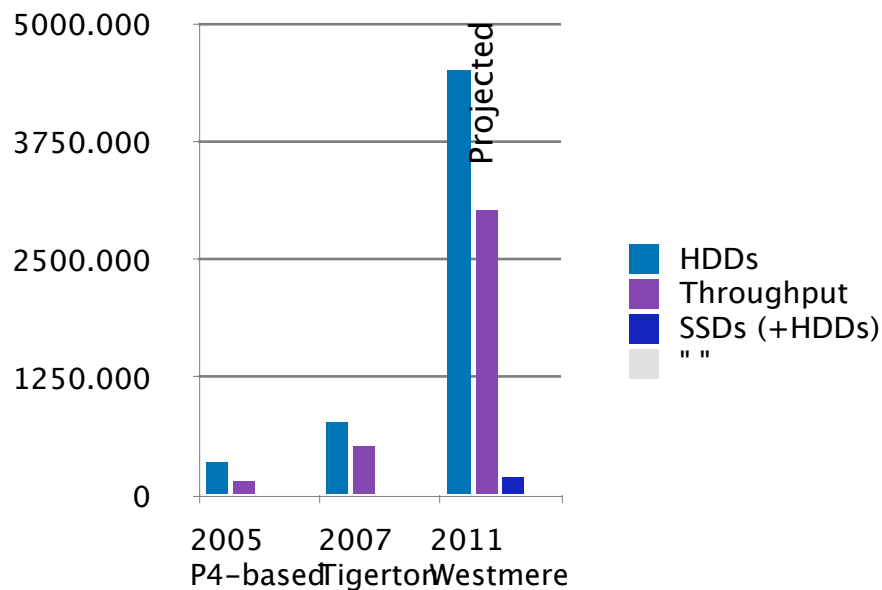
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# SSD Storage Already Revolutionary

- In some cases SSDs have already realized 20X improvements in enterprise

I/O Intensive OLTP Workload environments

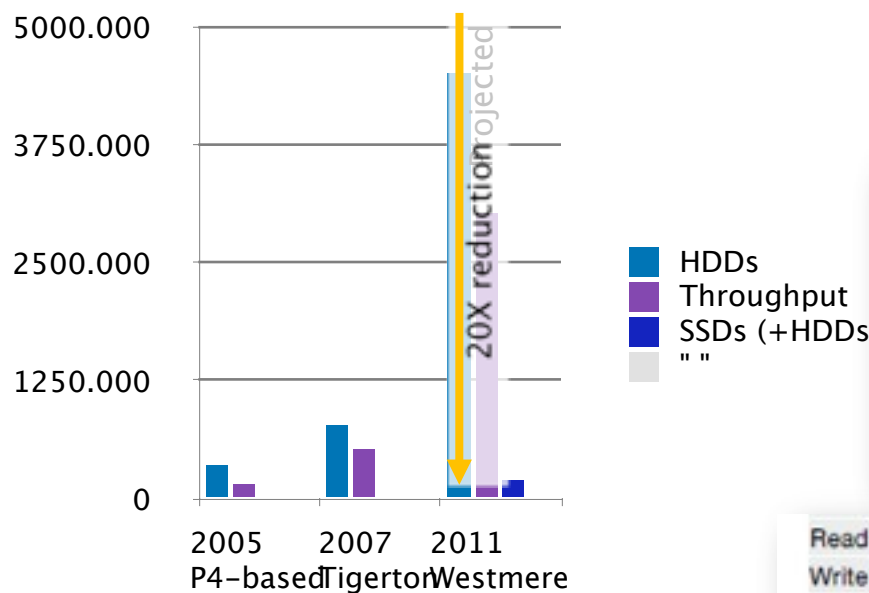




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Specifications		HUSSL4040ASS600	HUSSL4040ALF400
Model(s)		HUSSL4020ASS600	HUSSL4020ALF400
		HUSSL4010ASS600	HUSSL4010ALF400
Configuration			
Interface	SAS 6Gb/s	FCAL 4Gb/s	
Capacity (GB) at 512 Bytes/sector	400 / 200 / 100	←	←
Form factor	2.5"	←	←
Flash memory technology	Single-level cell (SLC)	←	←
Performance			
Read Throughput (max MB/s, sequential 64K)	516	390	←
Write Throughput (max MB/s, sequential 64K)	←	340	←
Read IOPS (max IOPS, random 4K)	41,000	←	←
Write IOPS (max IOPS, random 4K)	21,000	←	←

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The first 20X performance increase having dramatic impacts

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\*\*Datasheet image used with permission from Hitachi GST

\*\*\*OLTP system configuration data from Intel OLTP benchmarking lab

# What About the Next 20X?

- Let me show you

\* Prototype: Supports 8 NVMe queues, uses DRAM to highlight interface/stack capability, workload is 512B random reads (aligned to 4KB),  
System: Intel Core i7-2600K Sandy Bridge 3.4GHz (3.8GHz Turbo Boost) 4 x 256KB L2 Cache 8MB L3 Cache LGA 1155 95W Quad-Core  
Desktop Processor with Intel HD 3000 graphics and 8GB DRAM. Windows 7 Ultimate 64-bit. Rendering engine Iron Galaxy via Digital  
Storage ExtremesgBin/video encoder



# What About the Next 20X?

- Let **1M+ IOPs** from one slot in mainstream platform **you**



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# Efficient Interface Key Enabler

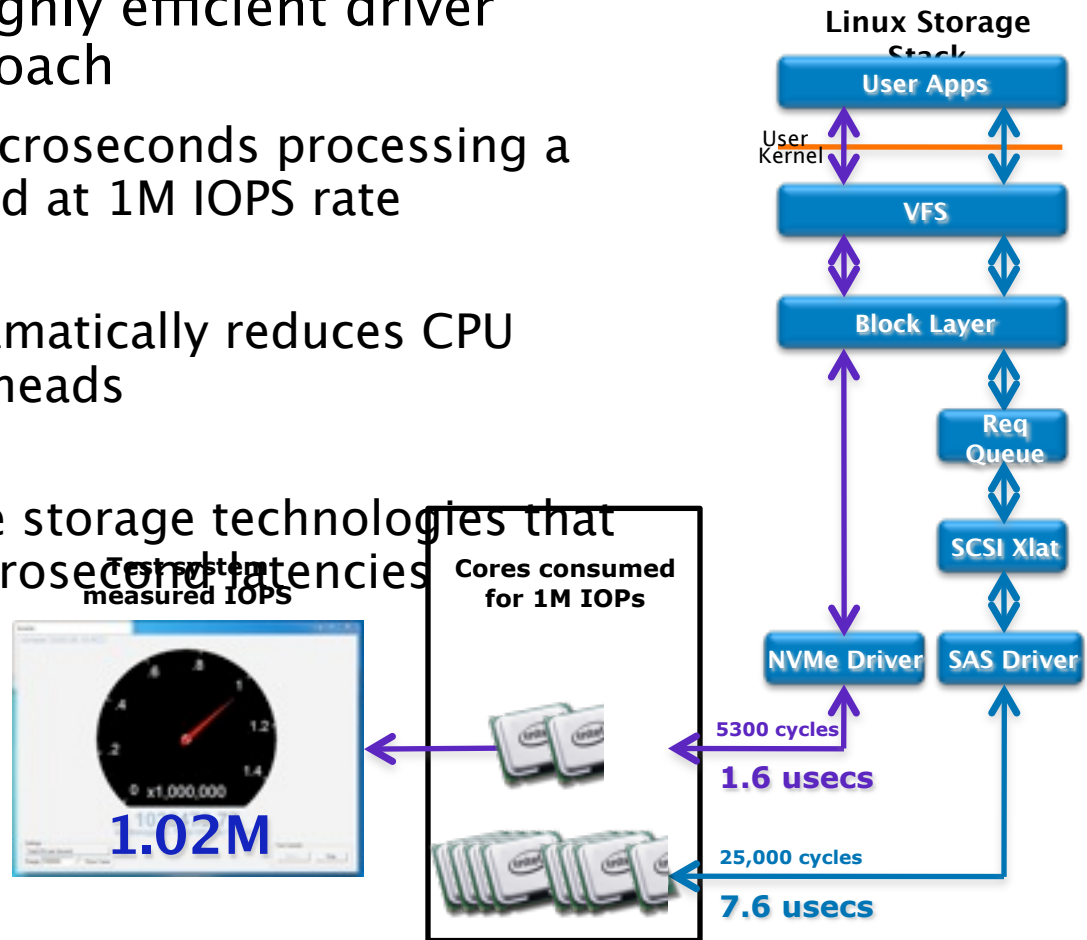
- PCIe physical interface for scalable bandwidth
    - 8X PCIe Gen3 provides up to ~8GB/s
  - Efficient NVMe logical programming interface
    - NVMe supports efficient all-hardware controller in device performance path to avoid firmware
  - Spec available and broad support
    - NVMe 1.0 published March and freely available
    - Broad 80+ member adoption
    - Infrastructure support maturing
    - NVMe now standard feature in all LeCroy PCIe protocol analyzers
    - Linux driver published, Windows baseline driver in development
- For more information, visit <http://nvmexpress.org>**





# Stack Must Also Be Efficient

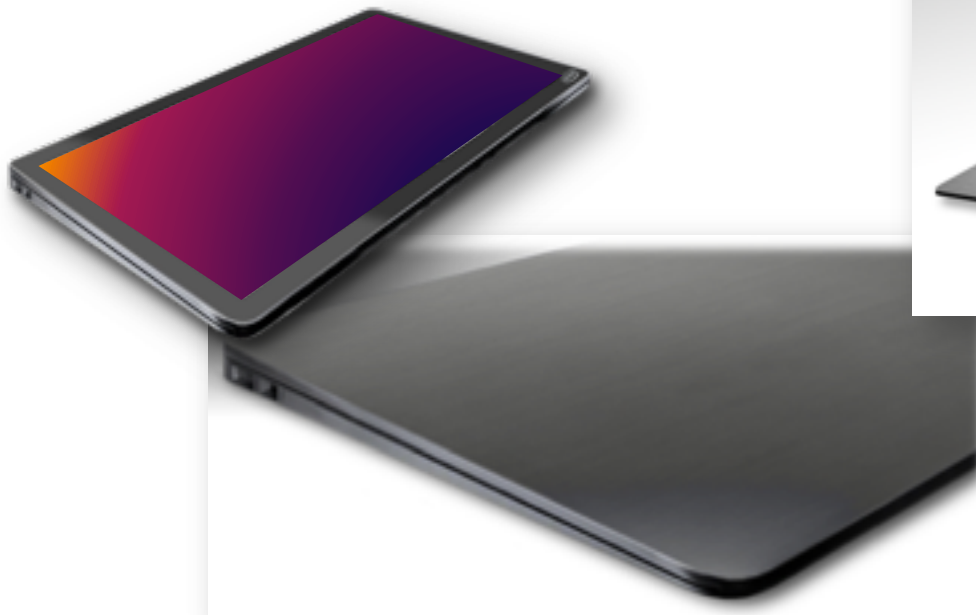
- 1M IOPS calls for highly efficient driver approach
  - Can't spend multiple microseconds processing a storage command at 1M IOPS rate
  - Streamlined stack dramatically reduces CPU overheads
  - Important for some future storage technologies that may have sub microsecond latencies



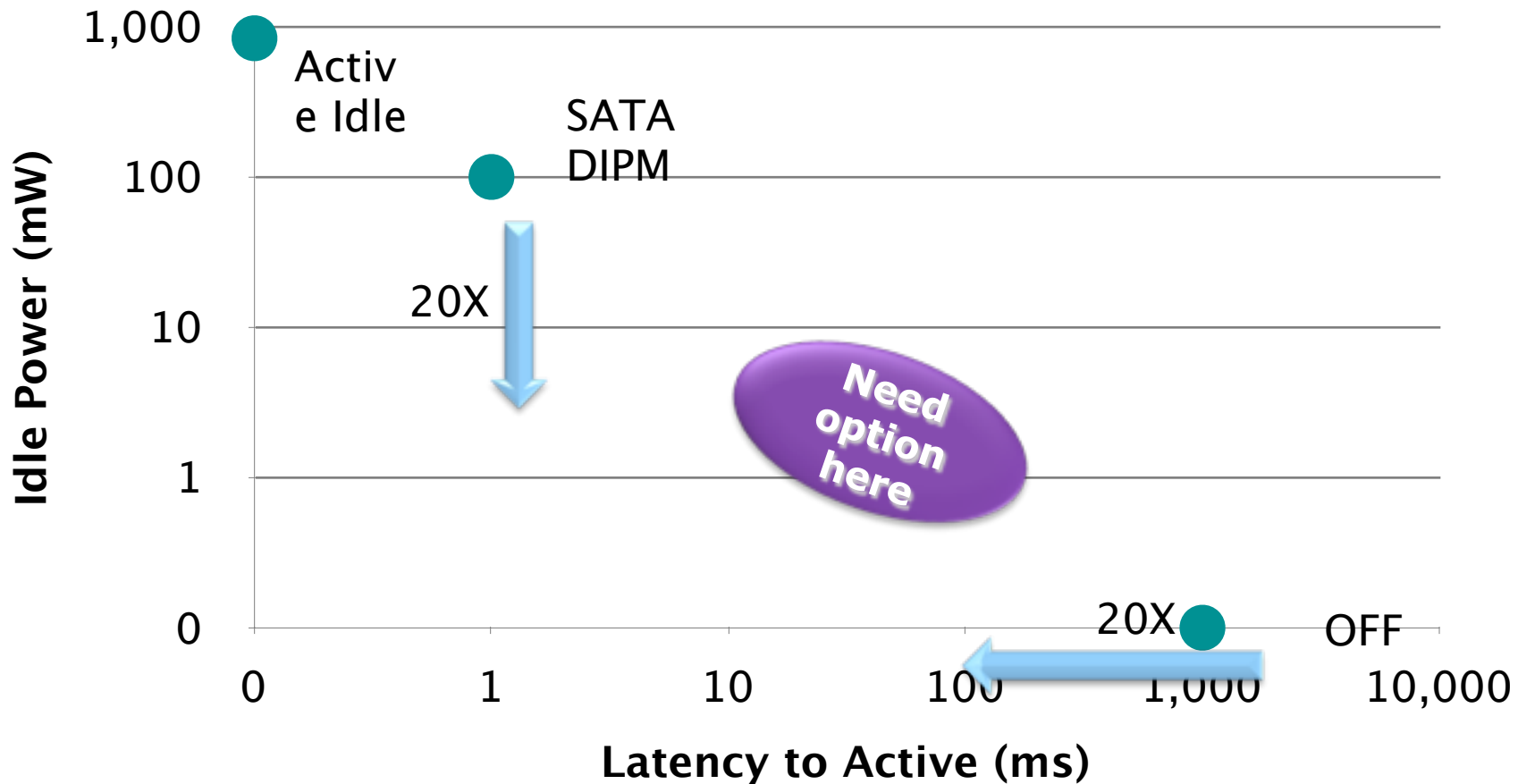
\* Measurement taken on Intel Core i5-2500K Sandy Bridge 3.3GHz 6MB L3 Cache Quad-Core Desktop Processor using Linux RedHat EL6.0 2.6.32-71 Kernel using FIO with raw IO

# It's Not All About Enterprise

- Coolest consumer and client devices use non-volatile memory for storage



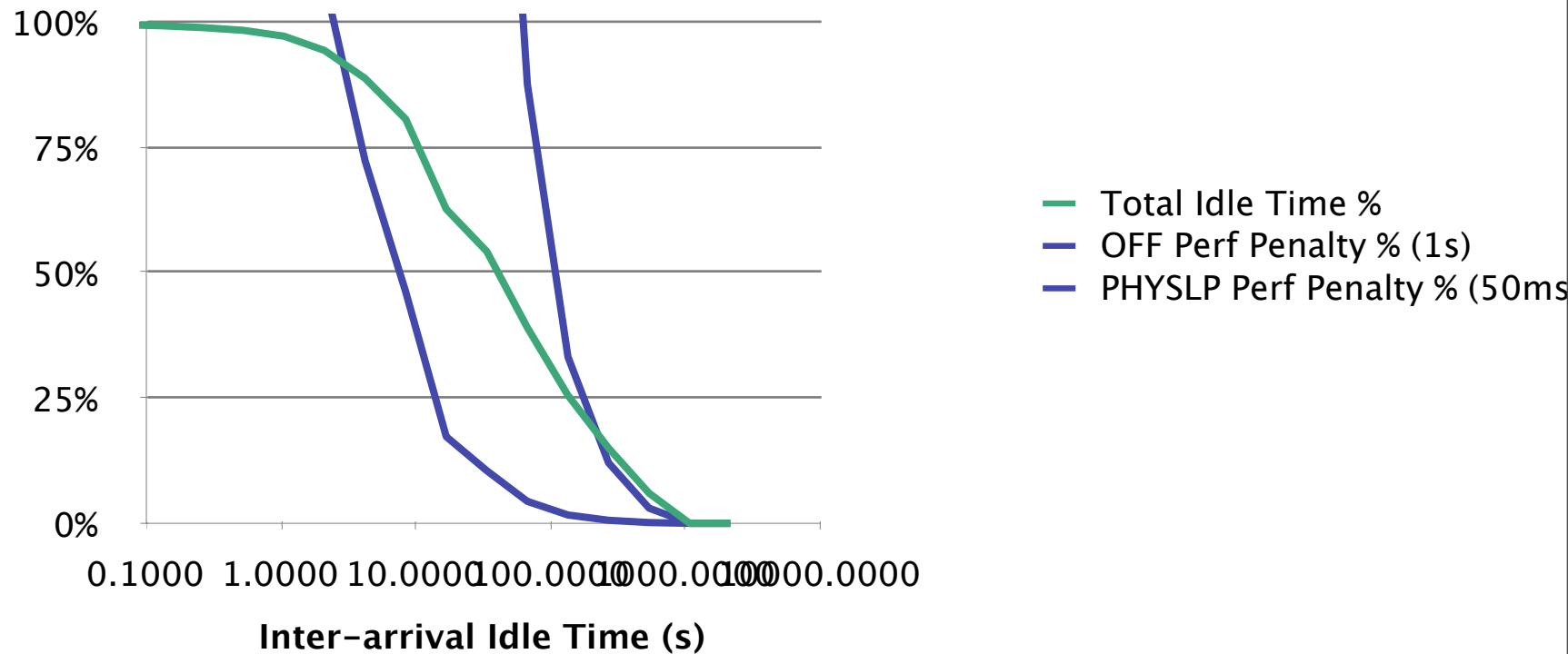
# Low Idle Power SATA SSD Requirements



**Target 5mW at 50ms. 20X lower power than DIPM or 20X faster than OFF.**

# Resume Latencies & Lost Time

- Why 5mW and 50ms?



\* Measurement taken from MobileMark 07\*\* workload using an Intel160GB Postville SSD

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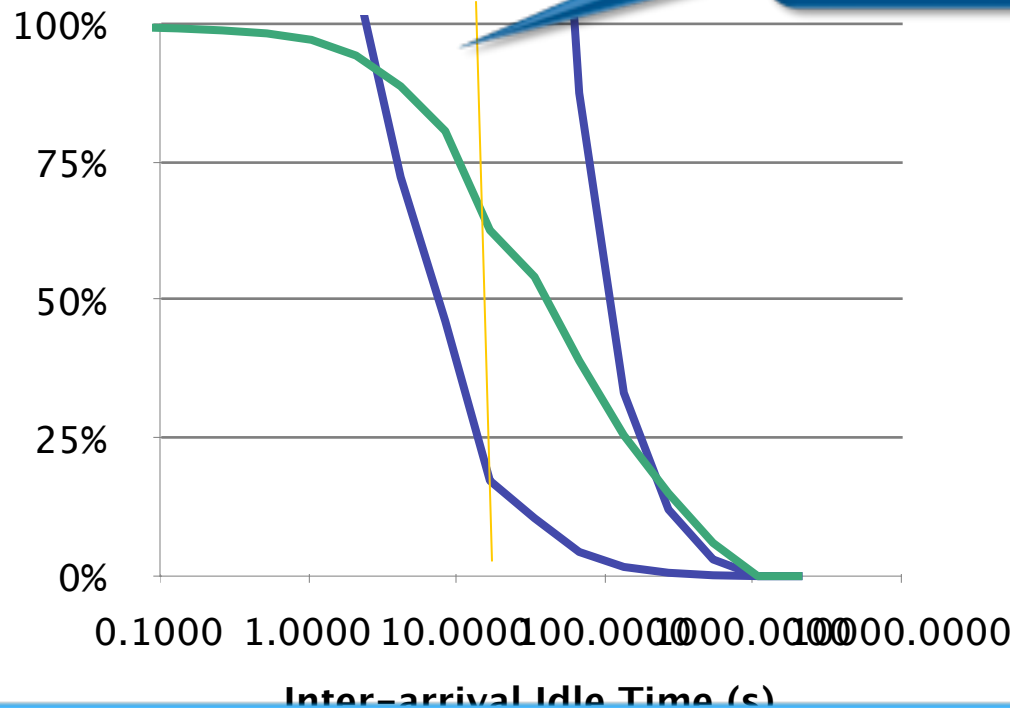


# Resume Latencies & Lost Time

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Why 5ms

Can take advantage of 80% of available idle time with modest (40%) disk utilization impact at 50ms resume latency. Near knee of curve (s).



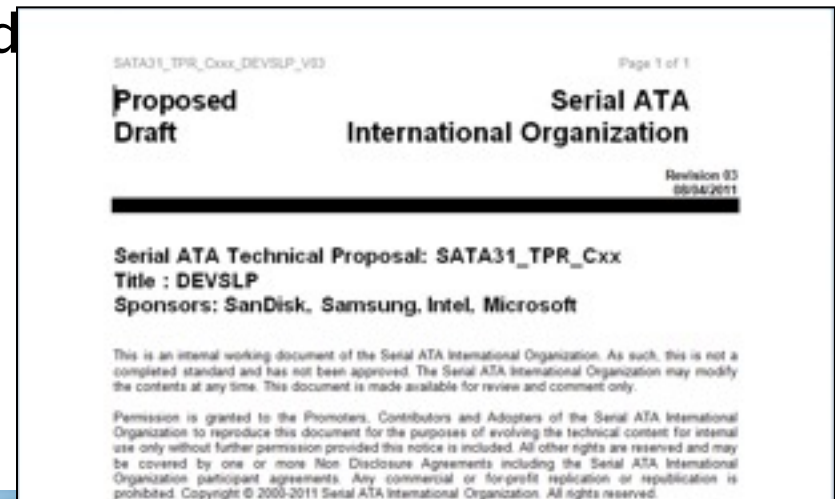
**Power models suggest net halving of overall workload power feasible**

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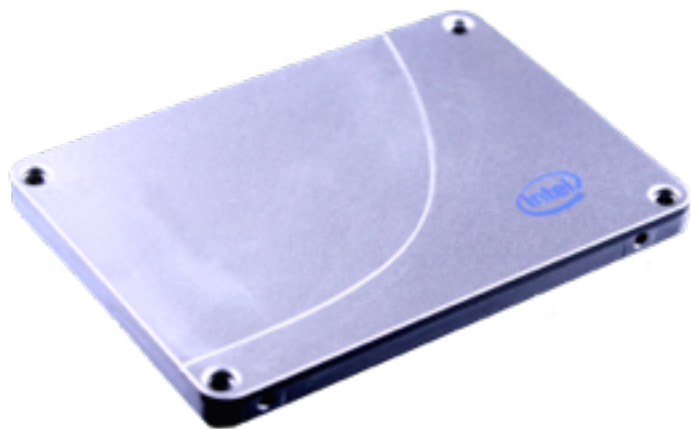
# New SATA Mechanism Key Enabler

- 20X idle power reduction requires enhanced mechanisms to support
  - The challenge is not going to sleep, but waking up
- Proposal for SATA key enabler in progress to address
  - Collaboration among lead



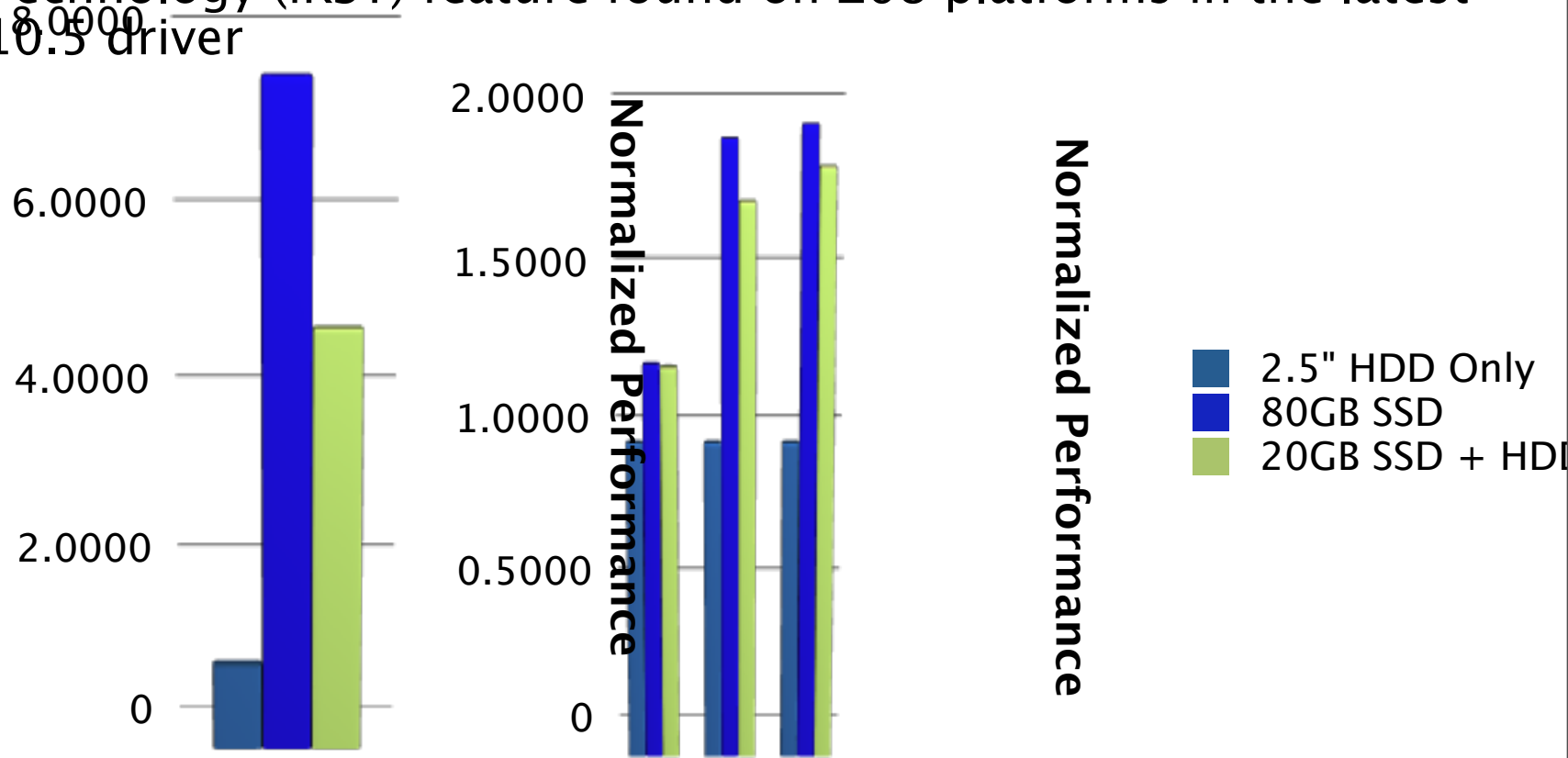
# Not Every Solution Needs 100% Non-Volatile Memory (NVM)

- NVM + HDD complements NVM-only solutions
  - Get most of the SSD performance & responsiveness benefits with HDD capacities



# Non-Volatile Memory + Mechanical Disk

- Smart Response Technology is an Intel Rapid Storage Technology (iRST) feature found on Z68 platforms in the latest 10.5 driver

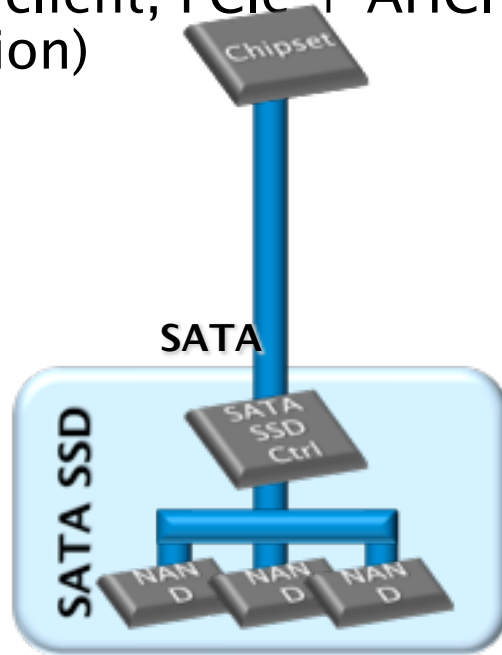


System configuration: PCMark Vantage v1.0.1, MobileMark 07 (patch 5, v1.0.1), Microsoft Windows\* 7 Ultimate 64-bit O/S, Intel Emerald Lake CRB (CPT mobile), Fab 2 board, B2 silicon, CPU: Sandy Bridge D2 2.4GHz 4+2, Chipset: Intel 6 Series (Cougar Point), SATA 2 for both SSD and HDD, Hitachi 7200RPM 320GB HDD, Postville 20GB SLC, 80GB MLC SSDs, Integrated Graphics, 4GB 1066MHz DDR3 DRAM (MB).  
 System performance improvement on platforms is configuration-dependent; system performance claim as measured by PCMark\* Vantage tests. Boot times taken with Microsoft Velocity v4.3 and Microsoft PwrTest (included in Microsoft WDK, for S4 times only).  
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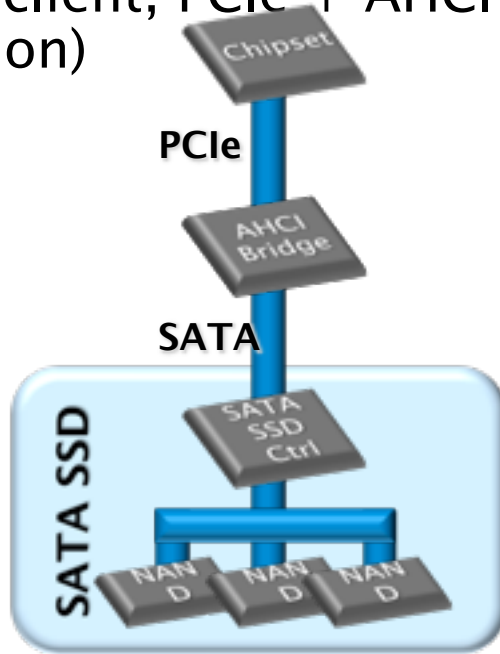
# PCIe Client Interfaces

- For solutions calling for bandwidth greater than supported by SATA, PCIe is the ubiquitous scalable physical interface option
- AHCI is ubiquitous logical interface supported by client infrastructure
- For client, PCIe + AHCI is obvious option (in addition to NVMe option)



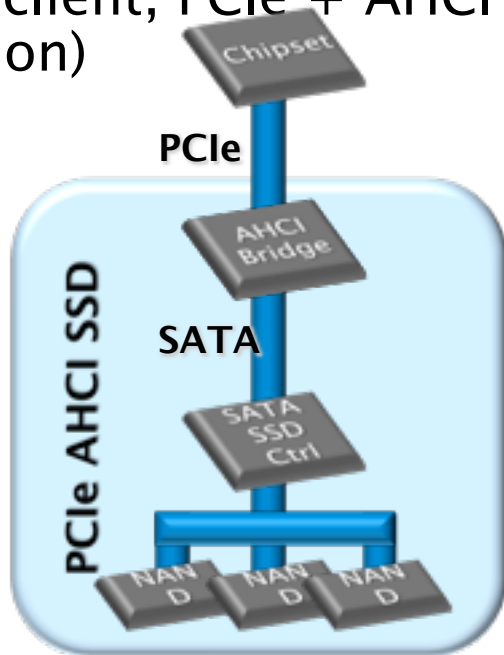
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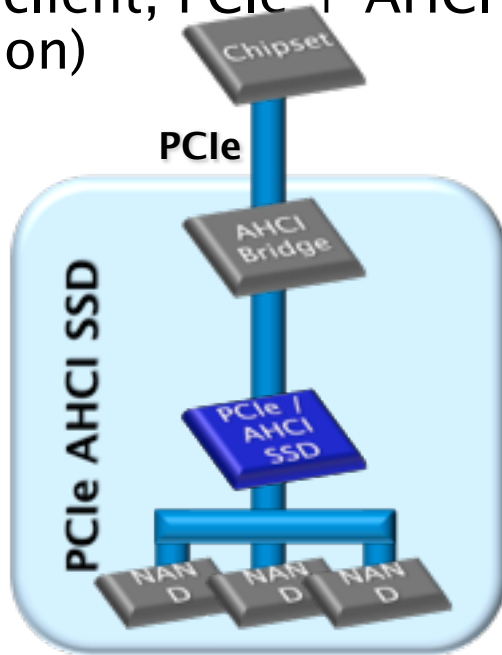
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# Future Outlook

- First 20X performance improvement in enterprise segments will be followed by another 20X improvement
  - Will call for efficient interfaces and storage stacks to realize
- 20X improvement in low power client idle power
  - New mechanisms required to enable
- NVM in client platforms will be 20X more prevalent
  - NVM+HDD solutions will complement NVM-only solutions
- Both physical and logical interfaces for storage will transition and host infrastructure will evolve