

# Future Technology Challenges For NAND Flash And HDD Products

Edward Grochowski

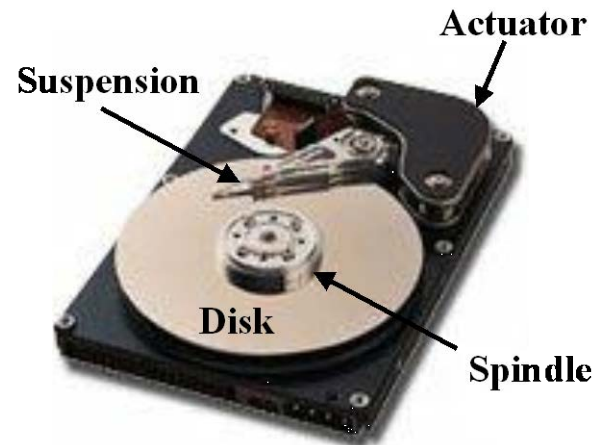
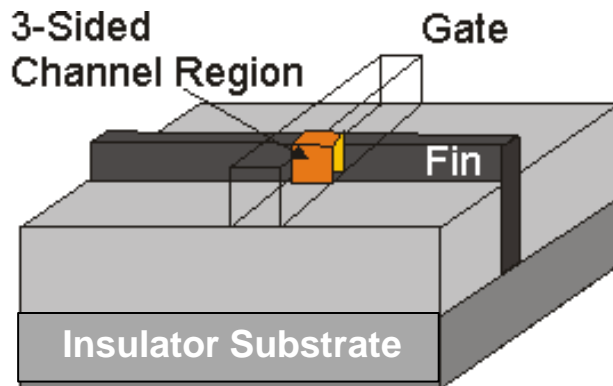
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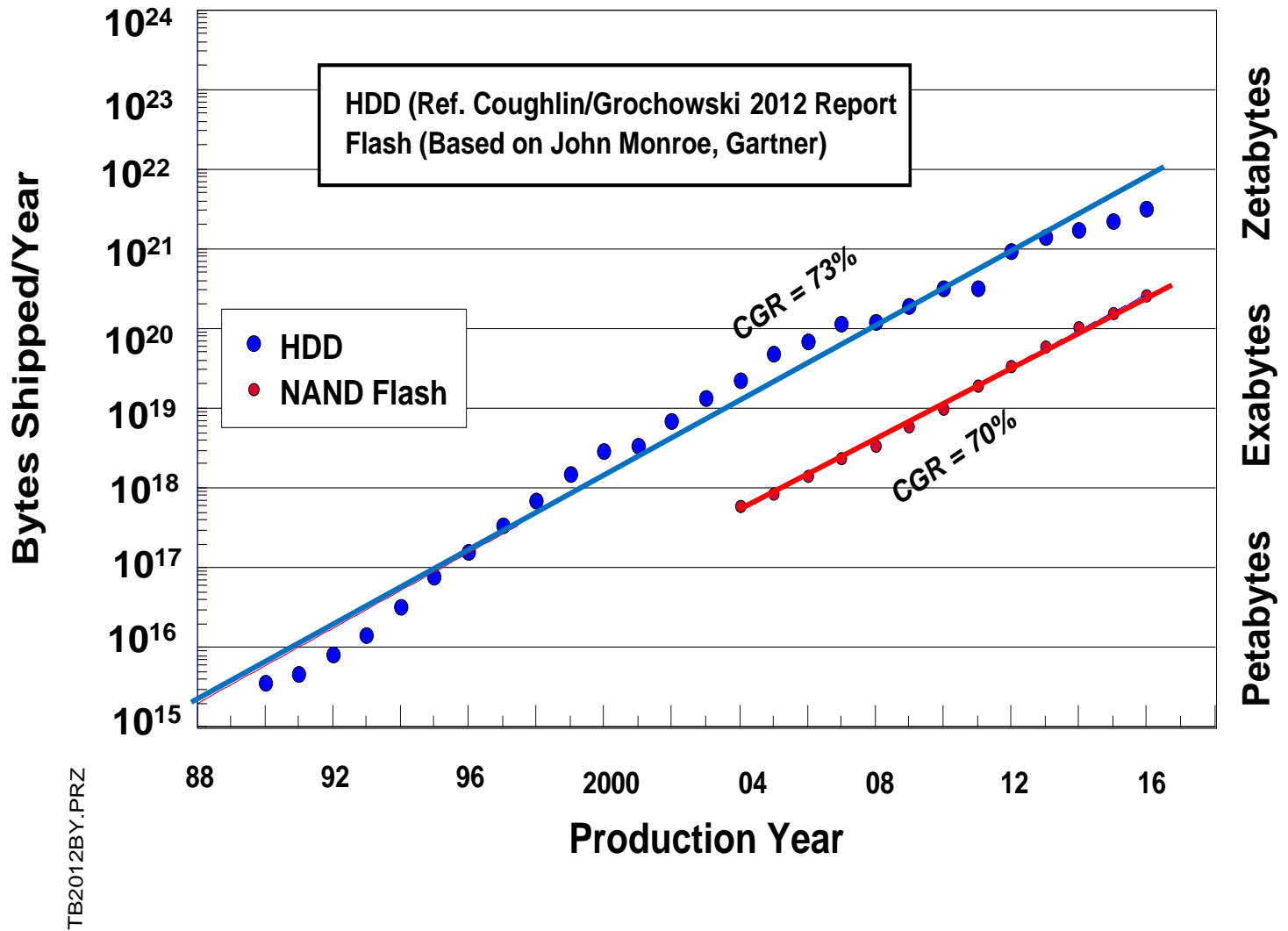




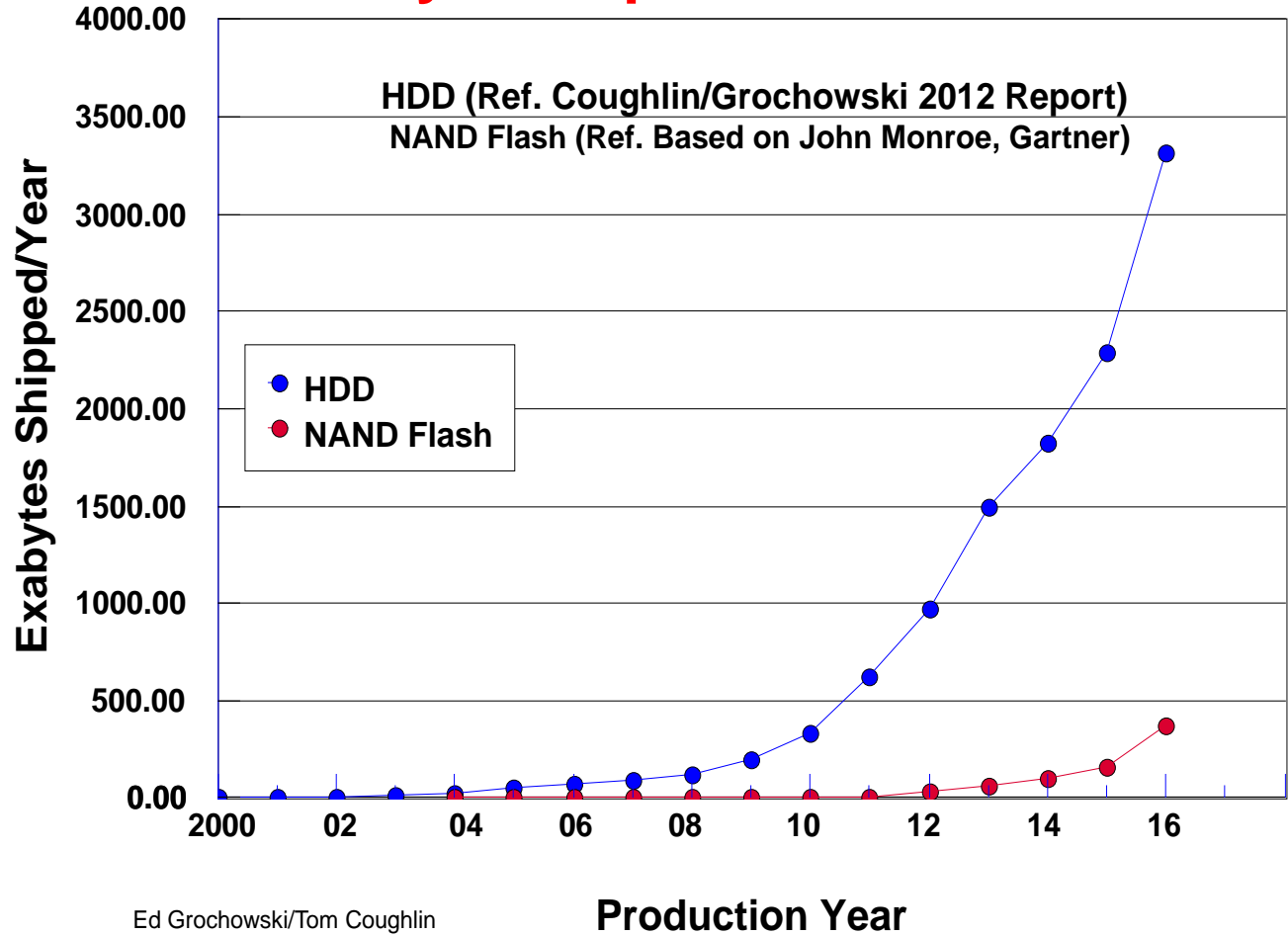
# Introduction and Topics

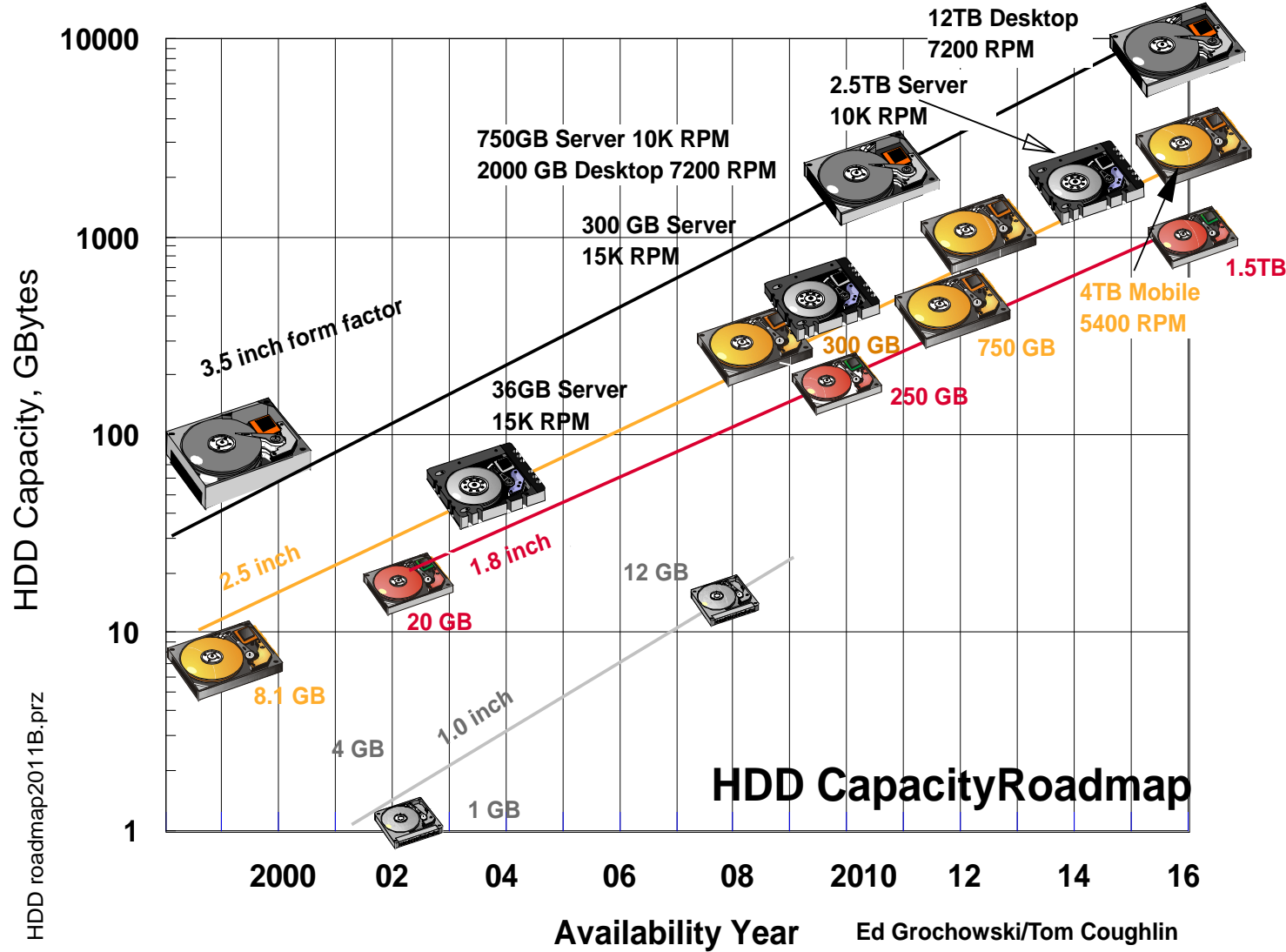
- 1. Analyze Storage Technology Trends For Flash And HDD**
- 2. Project Where These Principal Storage Technologies Will Evolve**
- 3. Project Data Density Growth, Costs Per Gigabyte**
- 4. Discuss Reliability And Endurance Issues**
- 5. Analyze Lithography Challenges**
- 6. Future Designs For Flash/HDD**
- 7. Discuss Competitive Non Volatile Alternative Technologies**

# HDD and Flash Byte Ships



# HDD and Flash Byte Ships



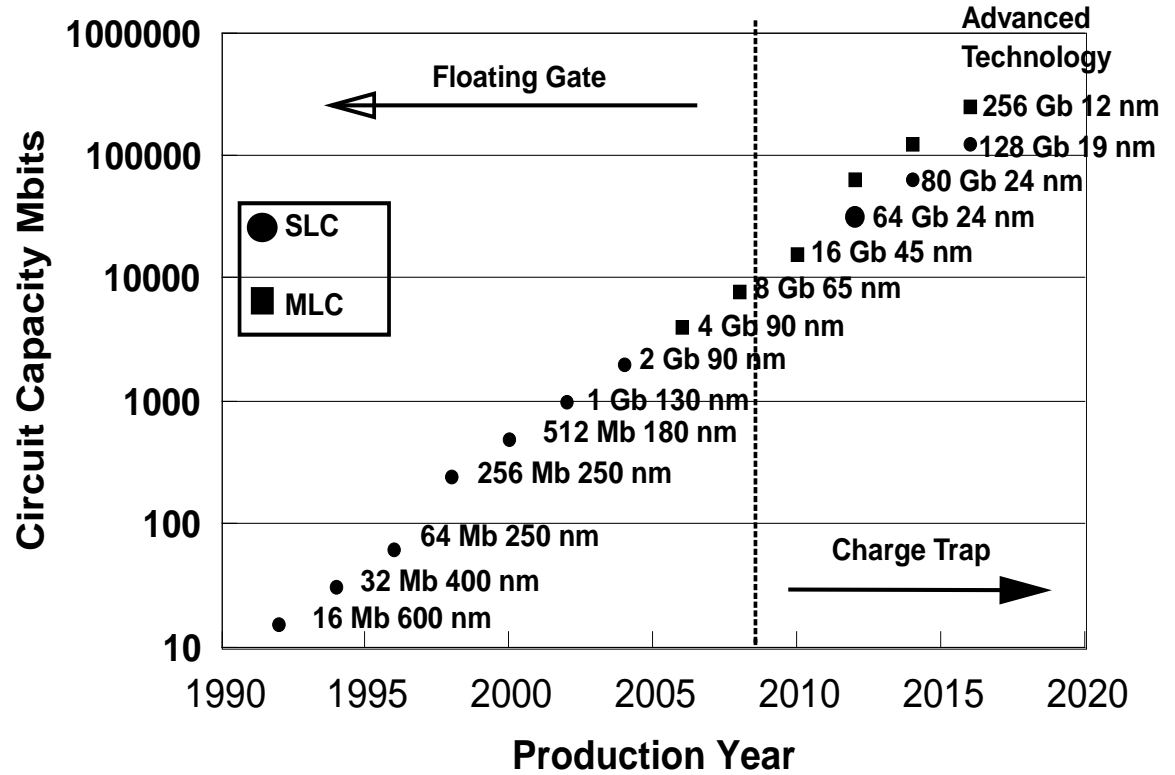


### Projected NAND Flash Memory Circuit Density Roadmap

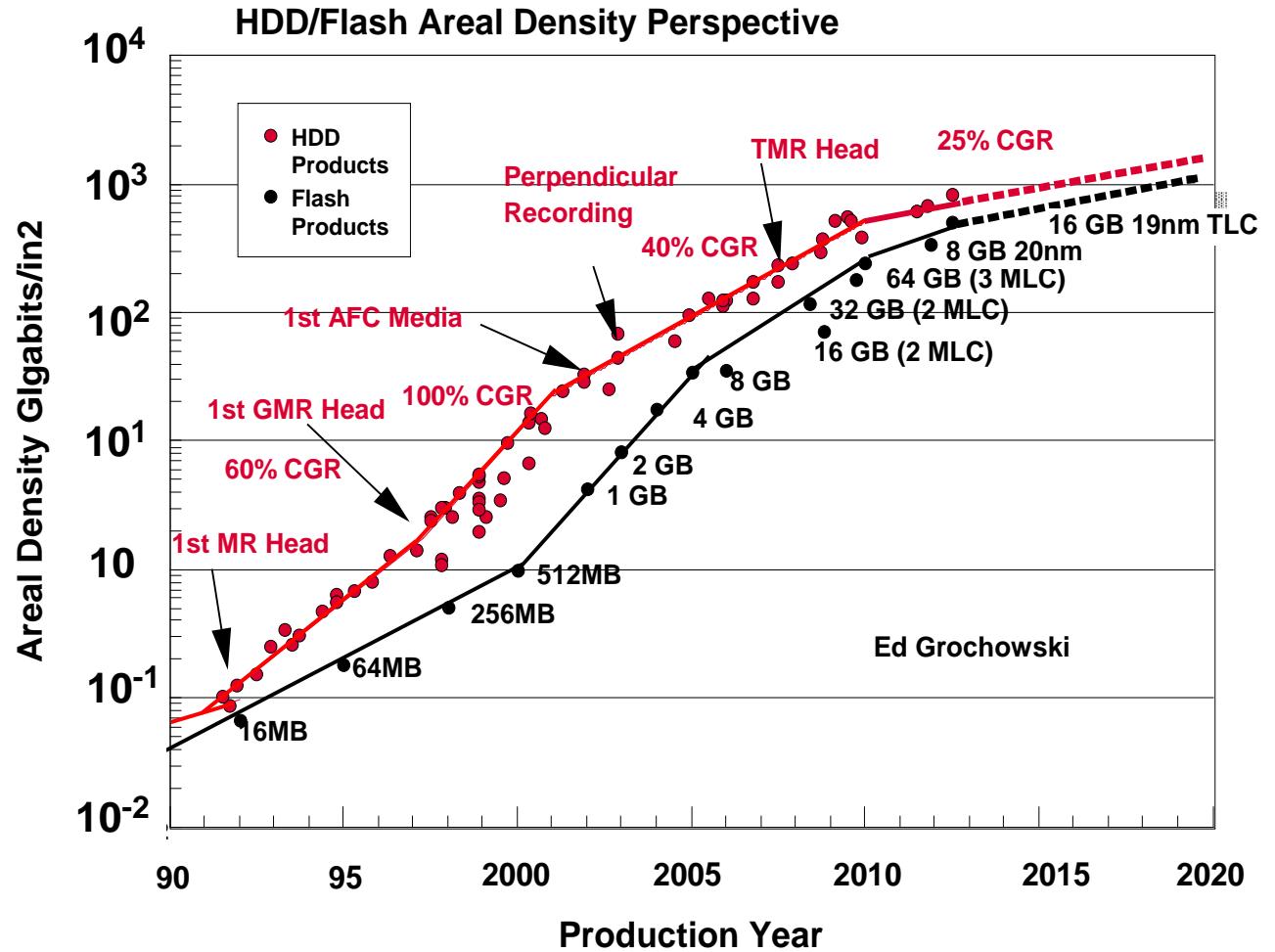


128 Gbit NAND 19nm  
San Disk/Toshiba  
2012

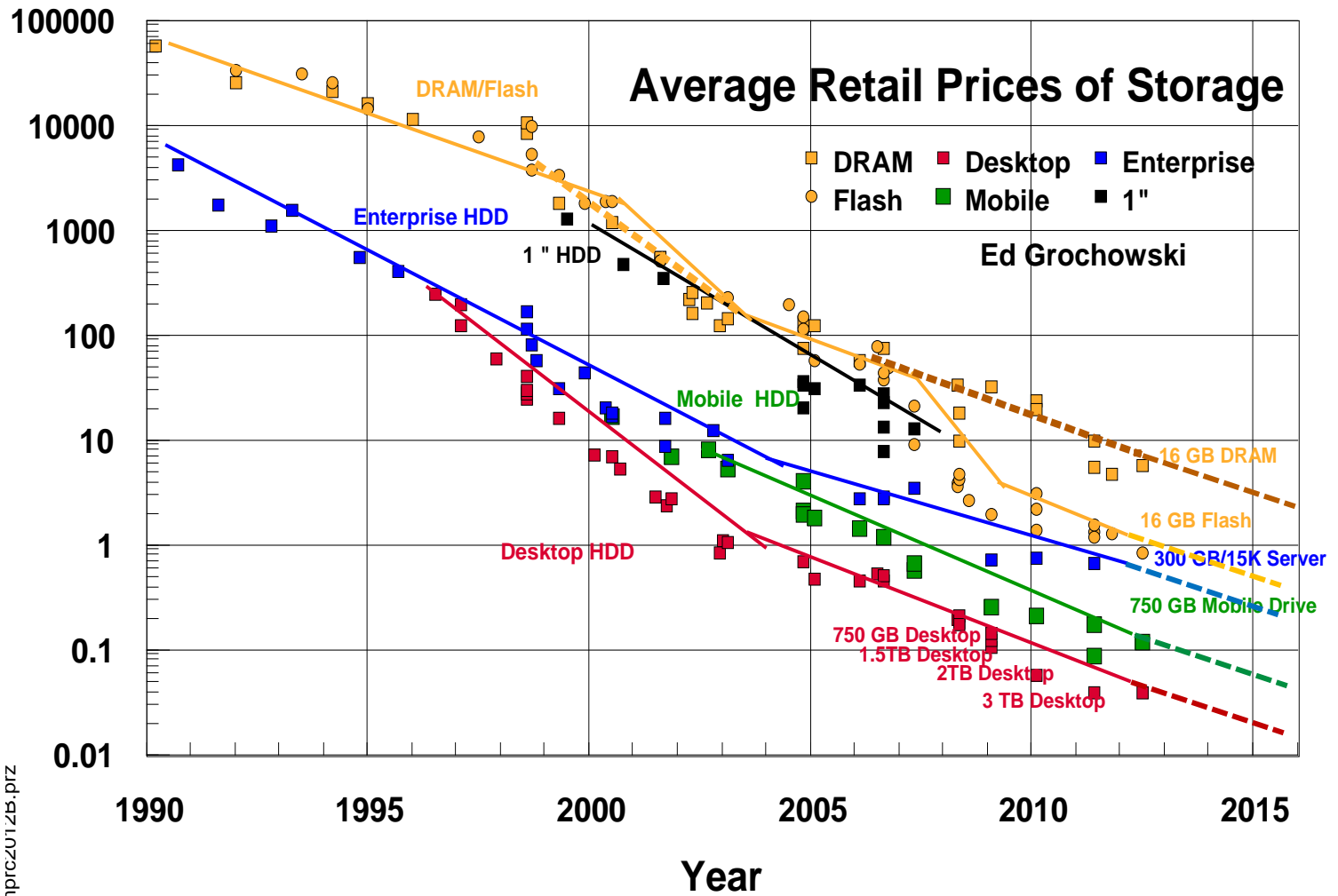
NAND Roadmap 2012X.prz



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areal2012egA.prz



oemprczv1zb.prz

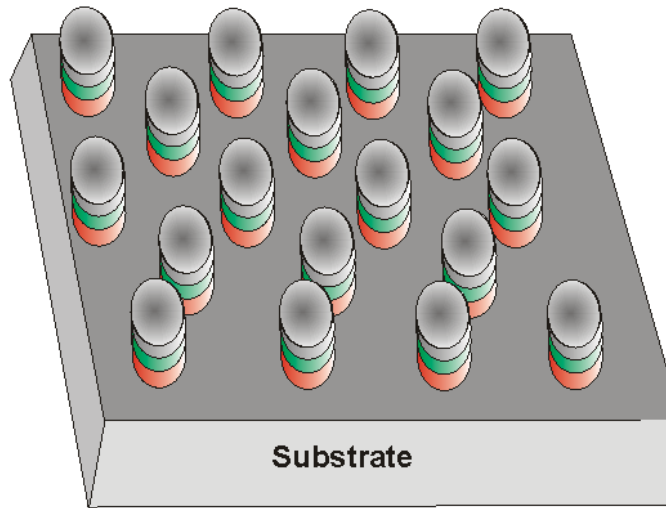


Magnetic "Islands"  
BAR = 2-4

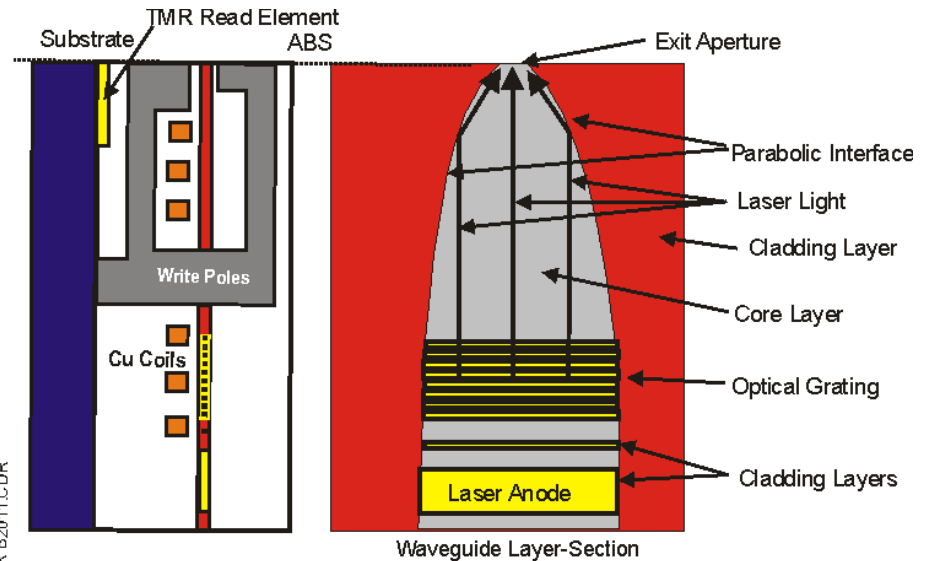
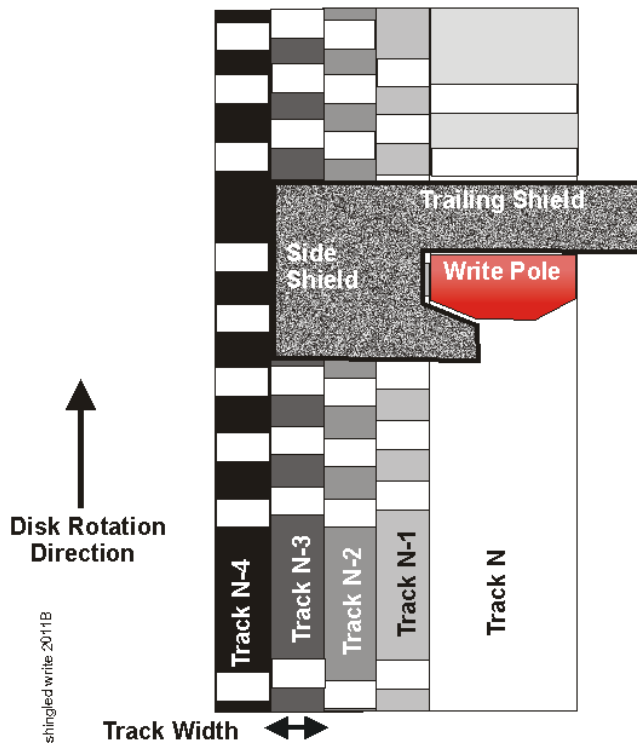
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Shingled Write

Three HDD  
"Innovations"

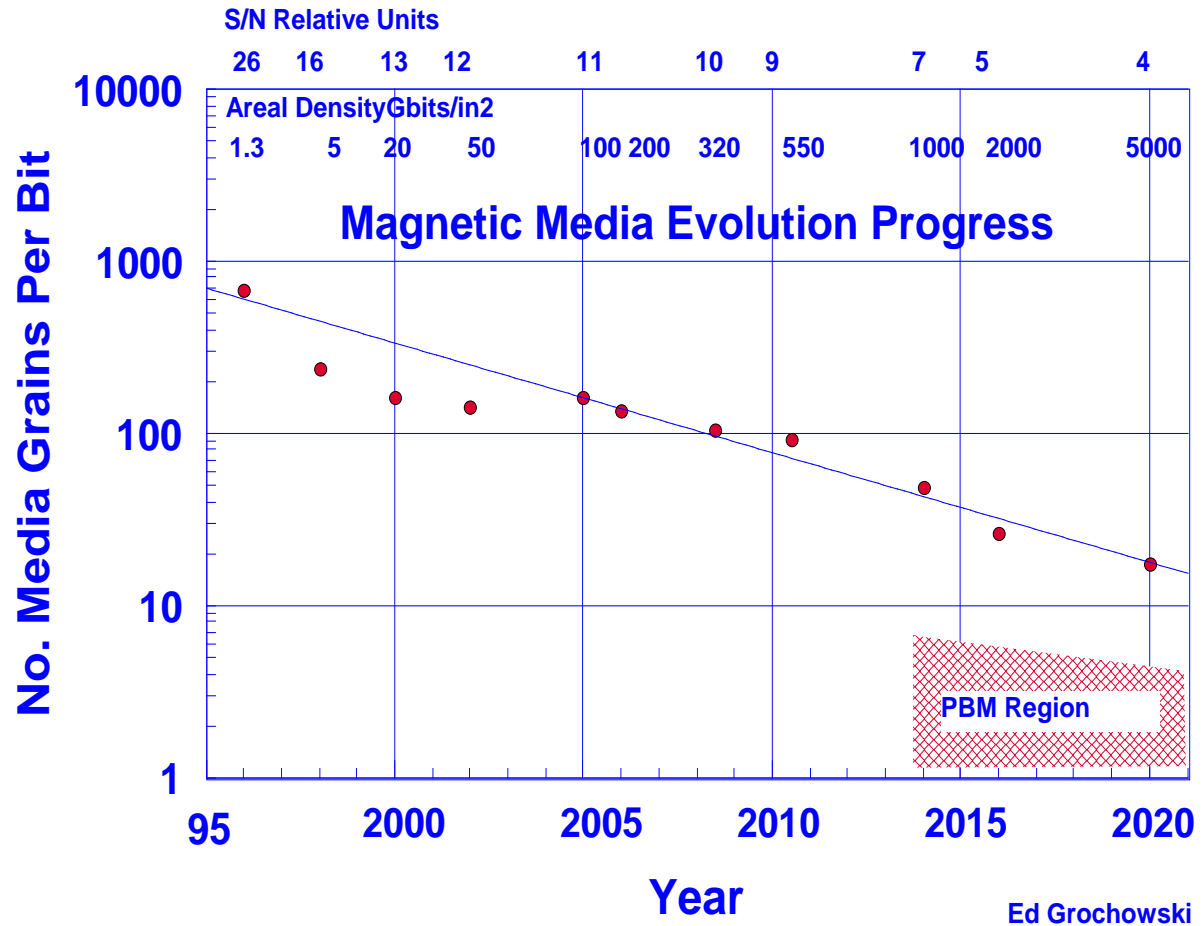


Shingled Write Dynamics

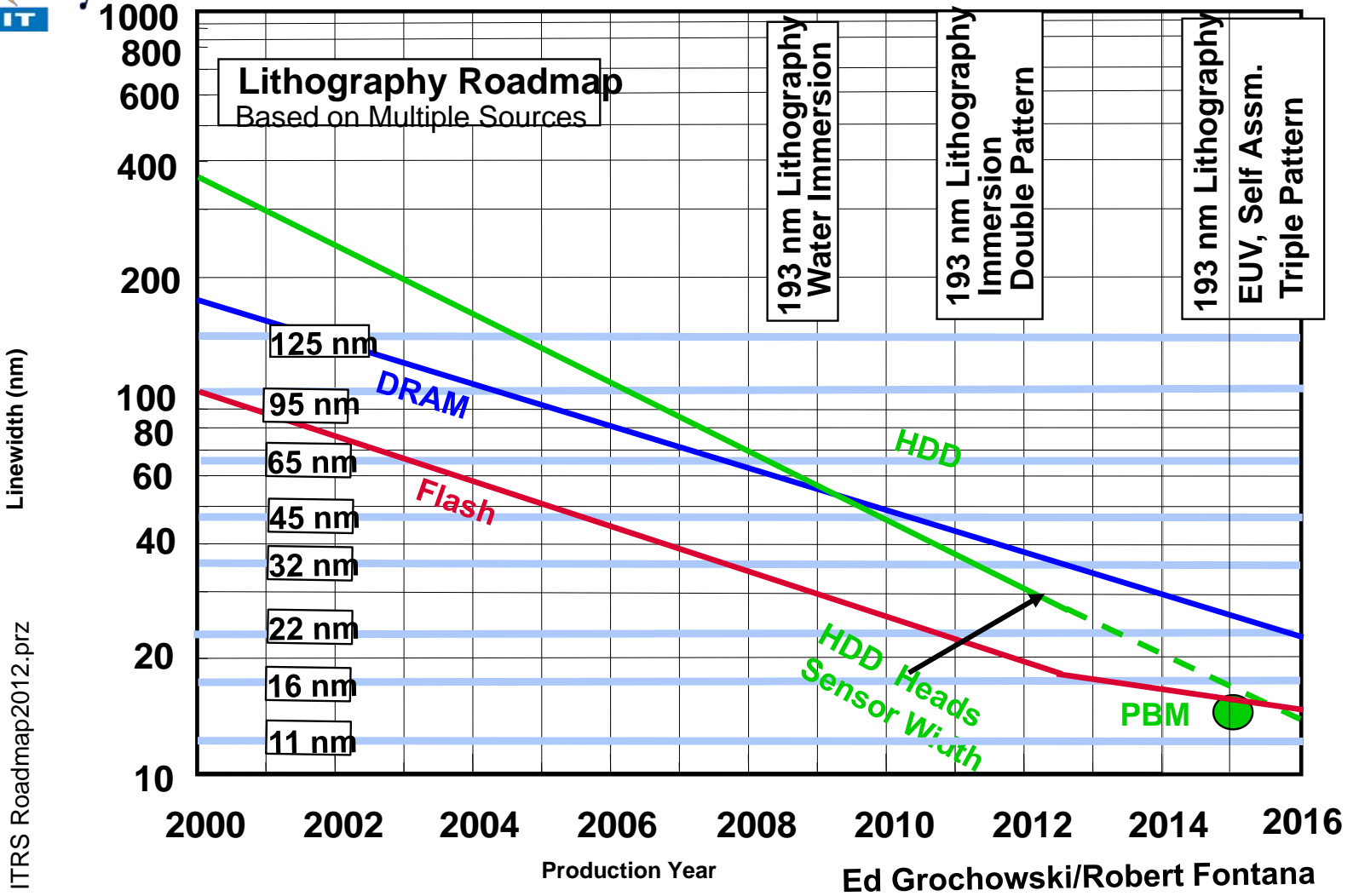


Source: Seagate Technology Corp.  
U.S. Patent 7609480 Shuk et al.

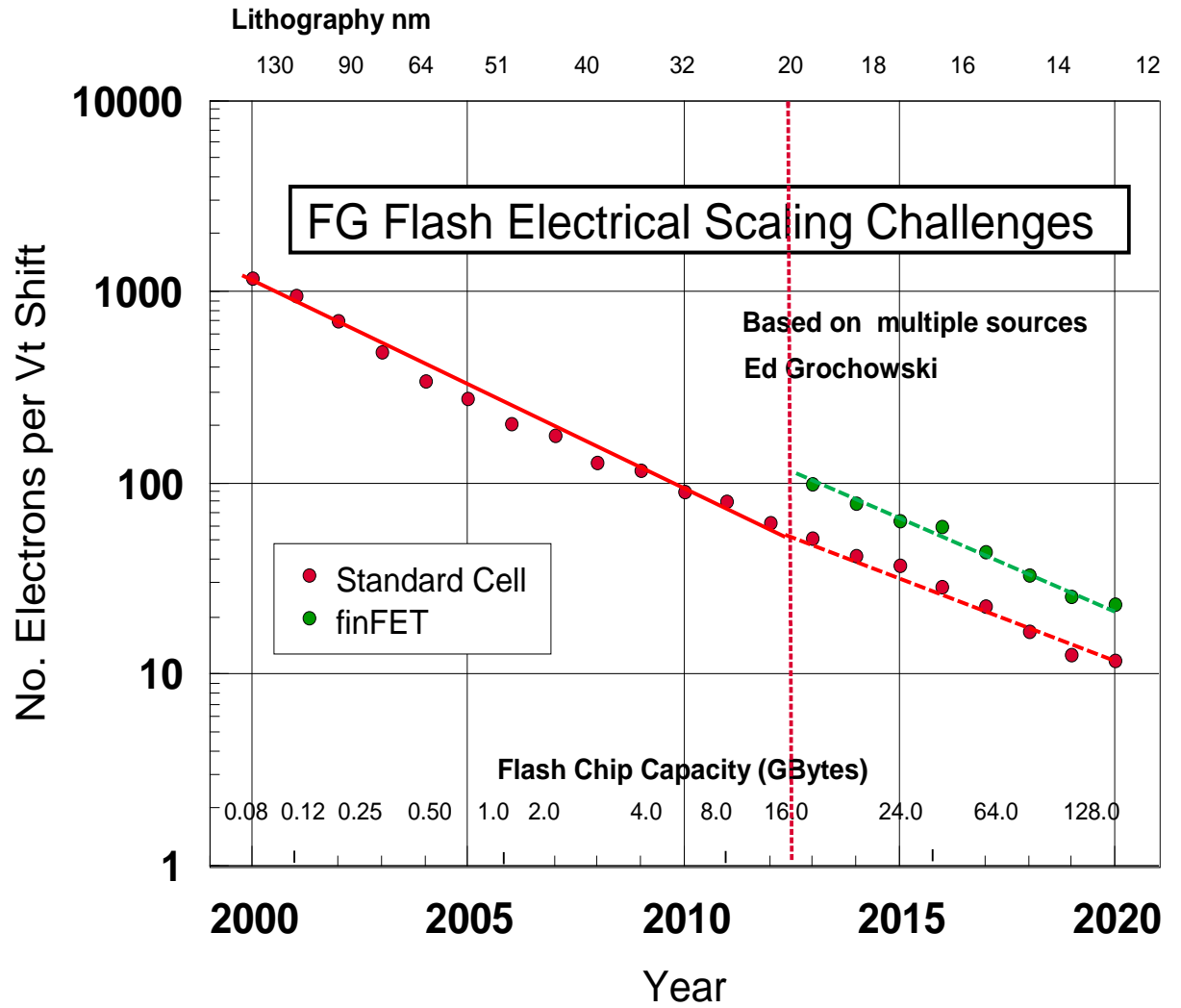
Heat Activated Magnetic Recording Head (HAMR)



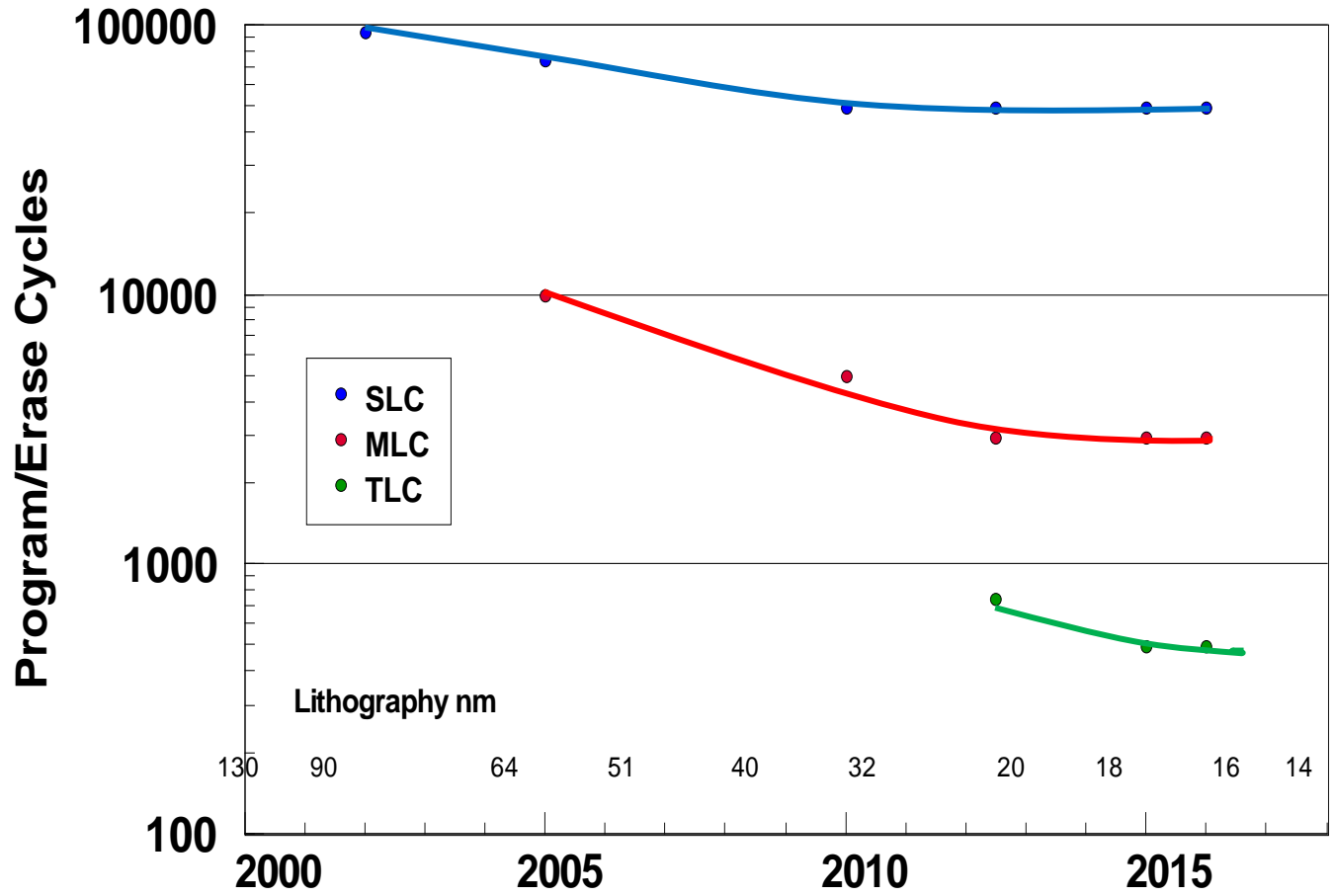
Grain Diam 2012B.prz



Flashchal2012B.prz



# NAND Flash Memory Endurance Properties

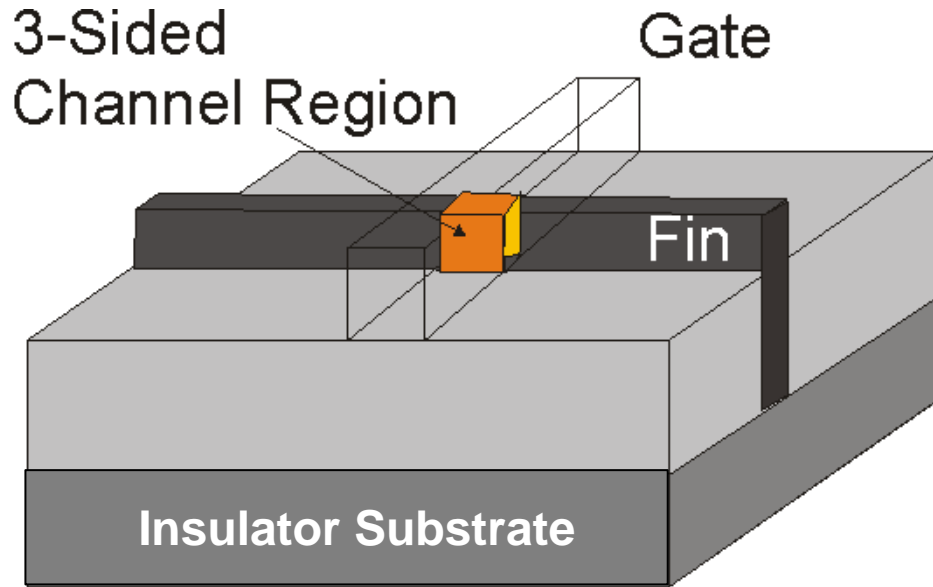


flashendur2012.prz

Production Year

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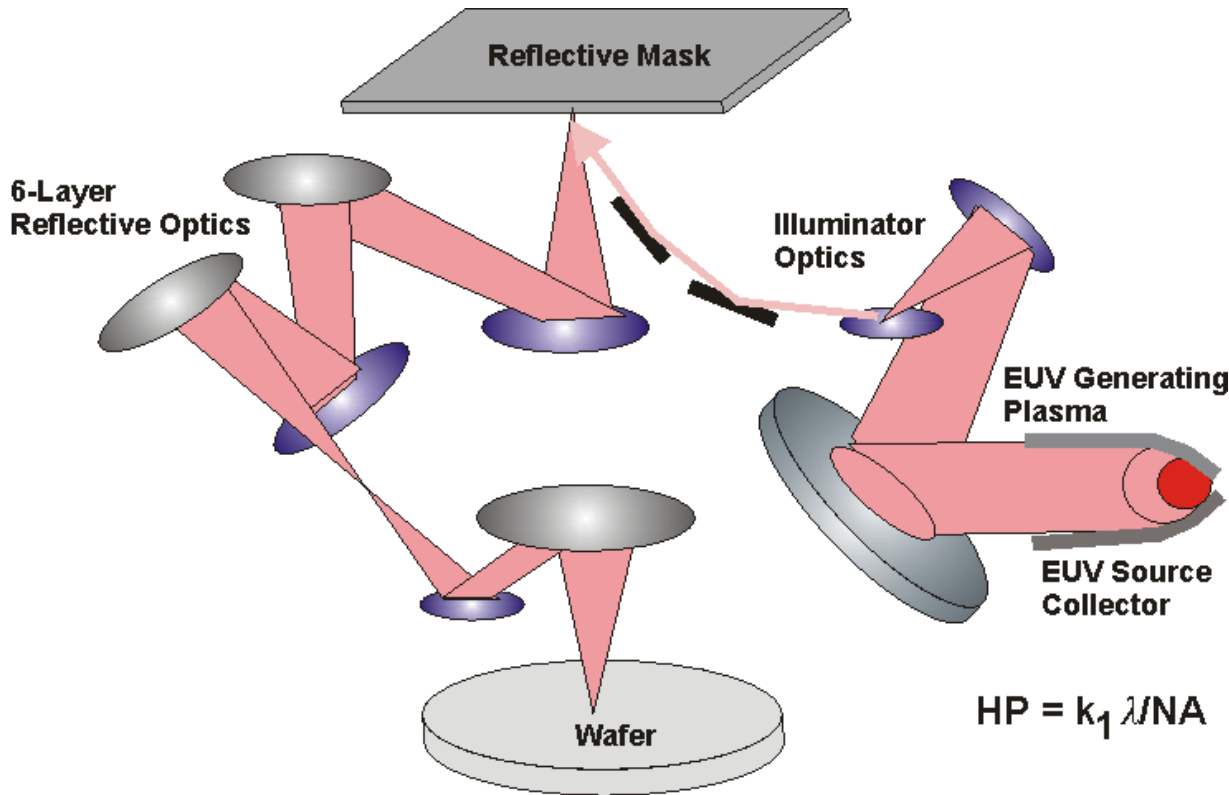
# The Promise of finFET Flash



- **2X Gate Peripheral Region**
- **Potential for Increase in Electron Storage**
- **3D Structure= Process Complexity**
- **Follows Microprocessor Technology**

**Based on INTEL 2012  
Ed Grochowski**

# The Future Promise of EUV

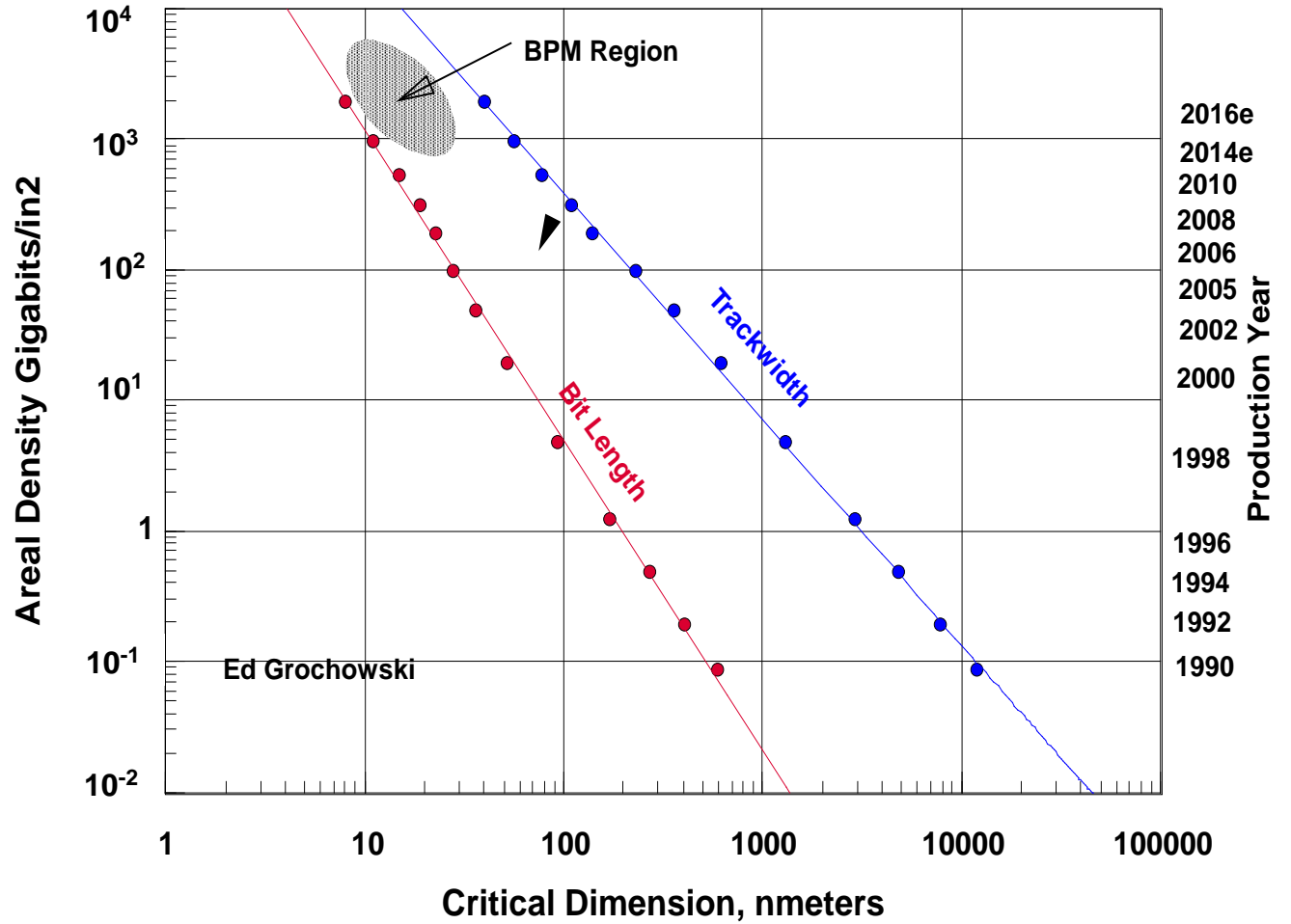


**EUV Exposure Tool**

- Experimental Tool Stage
- >\$100 M Tool Cost
- Reflective Mask Wearout
- Alternative For 14 nm?

Ref. Coughlin/Grochowski 2012 Report

# HDD Critical Dimensions



bitcell2012A.prz

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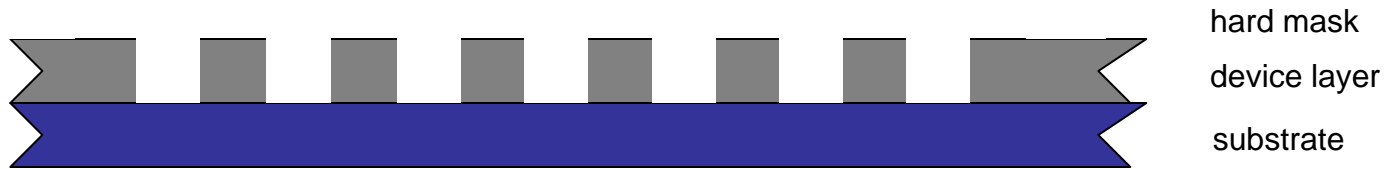


# Double Patterning

- Current 193nm Optics Allows The Ability To Produce 20nm Lines On 80nm Pitches
- NAND Requirements Are To Print Equal Lines And Spaces, I.E. 20nm Linewidths With A Space Of 20nm And A Pitch Of 40nm
- Double Patterning Enables The Formation of 20nm Lines On 40nm Pitches Using Two Masks, Each Having 20 nm Lines On 80 nm Spaces
- Future Technology Could Allow Triple Patterning

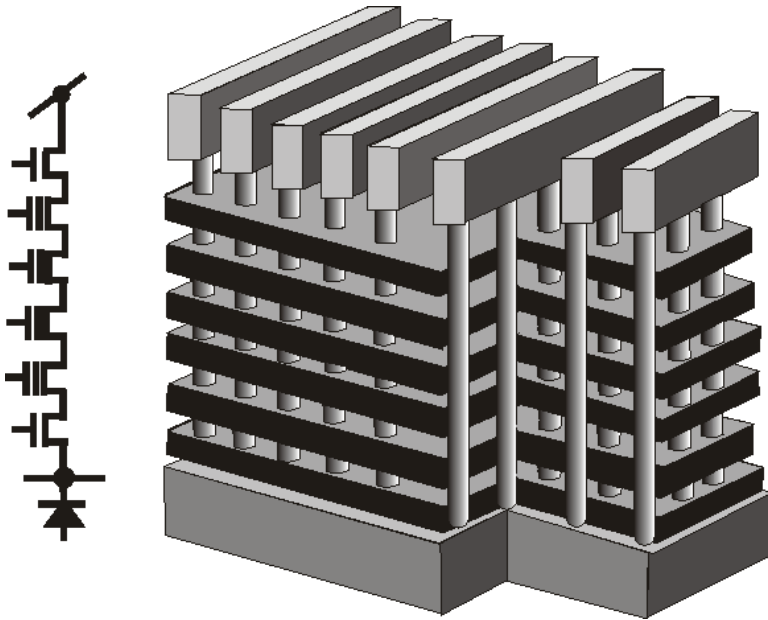
**Robert Fontana**

# Double Patterning



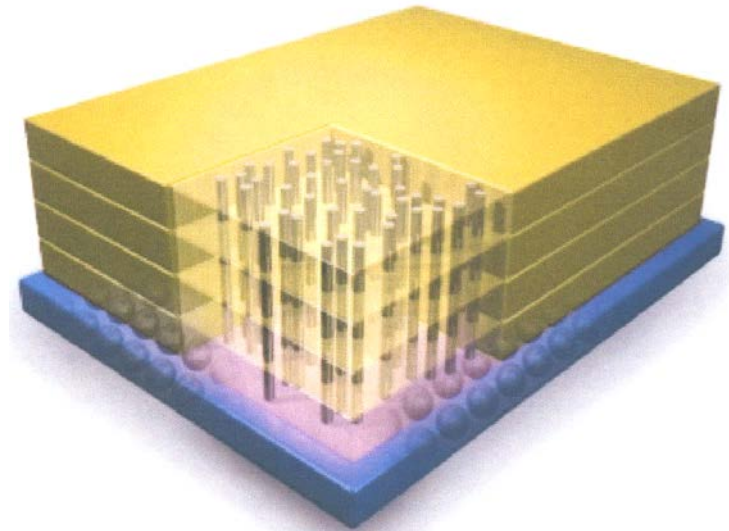
14) Strip hard mask

# 3D Packing Technology



**3D NAND Flash Design (Toshiba)**

**+’s: Real Estate Savings,  
Reduction Transfer Energy, Bandwidth**



**Hybrid Memory Cube (IBM/Micron)**

**-’s: Process Complexity, Costs/Yield**

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# Volumetric Density

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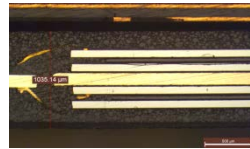
- Local (Media) Volumetric Density: The volumetric density of the memory media alone (using the maximum areal density) within a final memory component, i.e. tape on a reel, disks in an HDD stack, NAND flash in a package.
- Device Volumetric Density: The volumetric density of the memory component, i.e. a tape cartridge, an HDD drive, a SSD drive



HDD: 1 mm thick disks (two surfaces) separated by 1 mm gaps in a disk stack



SSD: 1 mm thick package with 4 thinned (75 um) NAND chips



TAPE: 5.2 um thick tape media wound on a cartridge spool

	HDD	SSD	TAPE (LTO)	TAPE <sup>3</sup> (Enterprise)
Maximum Areal Density	750 Gbit/in <sup>2</sup>	550 Gbit/in <sup>2</sup>	1.2 Gbit/in <sup>2</sup>	3.2 Gbit/in <sup>2</sup>
Local Volumetric Density	2.4 TB/in <sup>3</sup>	6.9 TB/in <sup>3</sup>	1.6 TB/in <sup>3</sup>	4.2 TB/in <sup>3</sup>
HDD/SSD/Cartridge Volume	23.8 in <sup>3</sup>	4.1 in <sup>3</sup>	14.1 in <sup>3</sup>	14.1 in <sup>3</sup>
HDD/SSD/Cartridge Capacity	3 TB	0.5 TB	1.5 TB	3.0 TB
Device Volumetric Density	126 GB/in <sup>3</sup>	122 GB/in <sup>3</sup>	106 GB/in <sup>3</sup>	212 GB/in <sup>3</sup>

		+'S	-'S
<b>HDD</b>	Shingled Write	Increase Track Density 50%	Significant Disk Architectural Changes, Ideally Suited For Streaming Data Sets
	HAMR	Increase Bit Density	New Media Alloy Based on FePt Addition Of Laser To Head Disk Lube Depletion
	BPM	Increased Areal Density, Data Stability	Disk Media Personalization, Massive Mfg. Operation, Capital Intensive, Lithographic Requirements (?)
	Packaging	Increased Unit Capacity	Cost Increased, Power
<b>Flash</b>	MLC	Increase Data Capacity Up To 3X	Significant Endurance Degradation New Controller Requirements
	Lithography	<20nm Required For Lower Costs	EUV Costs Data Leakage, Endurance
	3D finFET	Increase Data Capacity	Increased Process Complexity, Costs, New Device Structure
	Packaging	Increased Unit Capacity	Increased Costs, Power

# NV Memory Technology Comparison

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	HDD	Flash (SLC)	Flash (MLC)	FeRAM	MRAM	PCM	ReRAM	STT-RAM	Race-Track	Molecular
<b>Cell Structure</b>	None	1T/0C	1T/0C	1T/1R	1T/1R	1T/1R	1T/1R	1T/1R	None	1T/1R
<b>Cell size (F<sup>2</sup>)</b>	0.5	3.5	3.5	15	16=30	5-7	6-10	6-20	0.5e	10-5
<b>Read time (ns)</b>	2000	50	50	20-80	3-20	20-50	20-50	2-20	500	100e
<b>Write / Erase time (ns)</b>	1000	1 ms / 0.1 ms	1 ms / 0.1 ms	50/50	3-20	100	20-50	2-20	200	300e
<b>Endurance</b>	10 <sup>16</sup>	10 <sup>5</sup>	10 <sup>3</sup>	10 <sup>12</sup>	>10 <sup>15</sup>	10 <sup>12</sup>	10 <sup>8</sup>	>10 <sup>15</sup>	10 <sup>16</sup> e	10 <sup>8</sup> e
<b>Write power</b>	Low	Very high	Very high	Low	High	High	Low	Low	Low	Low
<b>Max. Areal Density Gbits/in<sup>2</sup></b>	750-1000	150	550	0.1	10	200e	200e	300e	>1000e	400e
<b>Voltage required</b>	3-5V	12 V	12 V	2-3 V	3 V	1.5-3 V	1.5-3 V	<1.5 V	3-5V	3-5V
	<b>Existing products</b>					<b>Prototype</b>				

## **CONCLUSION**

- 1. Storage Market Demands Expected To Continue For Both HDD And Flash Products.**
- 2. Flash Will Continue To Dominate Mobil/Handheld Market**
- 3. Technology Improvements Are Slowing Based On Implementation Costs, Implementation Difficulties**
- 4. Expect HDD Changes To Occur >2012 For Increased Data Density; HAMR, SMR, BPM**
- 5. Flash Will Adopt 3D Technologies**
- 6. No Alternative NV Technologies Have Demonstrated Leadership To Date; MRAM Progress Appears Attractive**
- 7. Legacy Storage (As Tape) Will Continue To Grow**