NVMe

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Agenda/Schedule

- AM Session – **8:30am – 11:20am**
  - Brief introduction to NVMe - Janene Ellefson (Micron)
  - NVMe Ecosystem Development - Amber Huffman (Intel)
  - Microsoft's perspective on NVMe – Tobias Klima (Microsoft)
  - NVMe Applications for Datacenter, Enterprise, and Client – Swapna Yasarapu (STEC)
  - Break
  - NVMe Conformance & Interoperability - David Woolf (UofH IOL)
  - SATA Express & NVMe Vision in End User Computing – Munif Farhan (Dell)
  - Q&A
- PM Session – **3:15pm – 4:25pm**
  - 1.1 Spec Overview and Future Directions – Peter Onufryk
  - Panel – "NVMe Deployment and What's Next?"
    - Moderator: Sergis Mushell, Gartner
    - Panel Members: Steve Sardella (EMC), David Landsman (Sandisk), David Dale (NetApp), Sumit Puri (LSI)

What is NVMe?

- NVMe is a scalable host controller interface designed to address the needs of Enterprise, Datacenter, and Client
- Target for PCIe based SSDs
- Provides optimization
- 13 Promoter companies
  - Intel, Micron, LSI, Marvell, Cisco, EMC, Dell, Oracle, NetApp, sTec, Samsung, SanDisk, PMC Sierra
- Over 90 NVMe member companies
- Plugfest 1.0 Complete
- 1.1 Spec

Why NVMe?

- Deliver the full potential of NVM in Enterprise and Client platforms for PCIe based SSDs
- Architected for performance
  - Performance across multiple cores
  - Optimized Register interface and command set
  - Scalability
  - End to End data protection
  - Lower power consumption

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The Optimized PCI Express® SSD Interface

The NVM Express specification defines an optimized register interface, command set and feature set for PCI Express (PCIe®)-based Solid-State Drives (SSDs). The goal of NVM Express is to unlock the potential of PCIe SSDs now and in the future, and standardize the PCIe SSD interface.

Questions may be directed to | info@nvmexpress.org