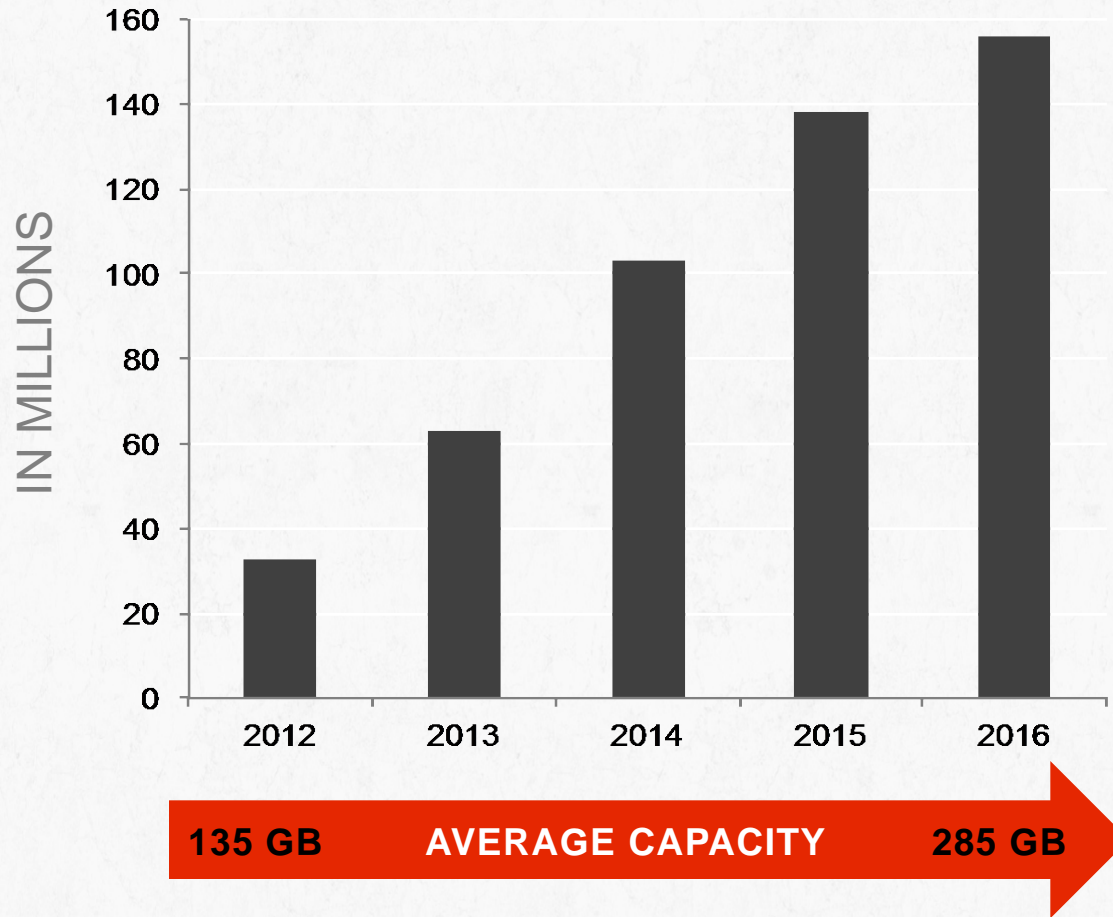




SSD REVOLUTION
IN CLIENT DEVICES
AND DATA CENTERS

PRESENTED BY IRI TRASHANSKI
VICE PRESIDENT, SSD, MARVELL SEMICONDUCTOR
FLASH MEMORY SUMMIT
AUGUST 13, 2013

THE SSD MARKET IS GROWING



SOURCE: MARVELL AND MARKET DATA



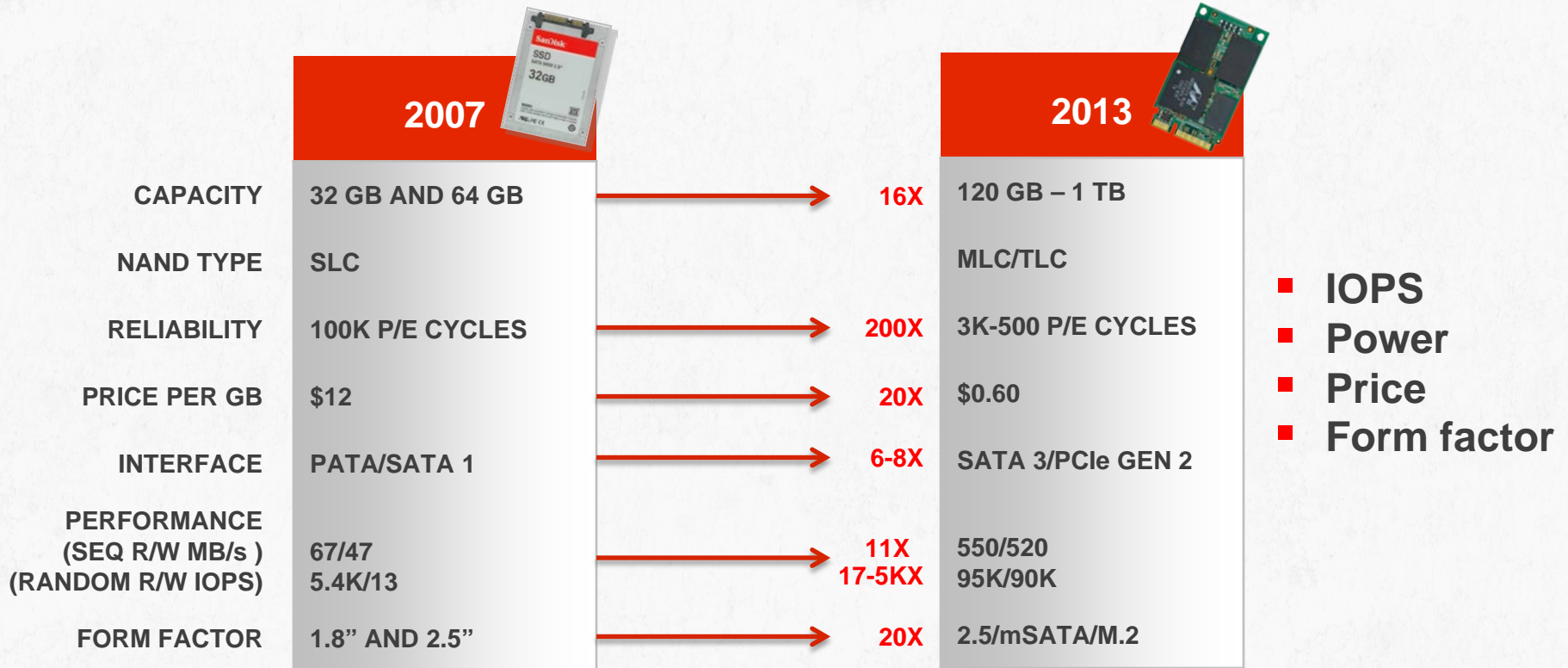
2007: DELL SHIPS FIRST CLIENT SSD USING SANDISK 5000

	2007
CAPACITY	32 GB AND 64 GB
NAND TYPE	SLC
RELIABILITY	100K P/E CYCLES
PRICE PER GB	\$12
INTERFACE	PATA/SATA 1
PERFORMANCE (SEQ R/W MB/s) (RANDOM R/W IOPS)	67/47 5.4K/13
FORM FACTOR	1.8" AND 2.5"




- Most durable
- More power-efficient and longer use time
- Cooler
- High-performance?



FAST-FORWARD TO TODAY



WHERE WILL WE BE IN 2016?

	2007 	2013 	2016 
CAPACITY	32 GB AND 64 GB	120 GB – 1 TB	120 GB – 2 TB
NAND TYPE	SLC	MLC/TLC	TLC/MLC/3D
RELIABILITY	100K P/E CYCLES	3K-500 P/E CYCLES	2K-200 P/E CYCLES
PRICE PER GB	\$12	\$0.60	\$0.30
INTERFACE	PATA/SATA 1	SATA 3/PCIe GEN 2	PCIe GEN 2\3
PERFORMANCE (SEQ R/W MB/s) (RANDOM R/W IOPS)	67/47 5.4K/13	550/520 95K/90K	3/1.4 GBs 250K/150K
FORM FACTOR	1.8" AND 2.5"	2.5/mSATA/M.2	M.2/BGA

Performance and Price per GB improvements from 2013 to 2016 are indicated by red arrows:

 - Capacity: 2X increase

 - Price per GB: 2X decrease

 - Performance (Seq R/W): 6X increase

 - Performance (Random R/W IOPS): 3X increase

SIX AREAS DRIVING EVOLUTION

NAND

**CONTROLLER
TECHNOLOGY**

**ERROR
CORRECTION CODE**

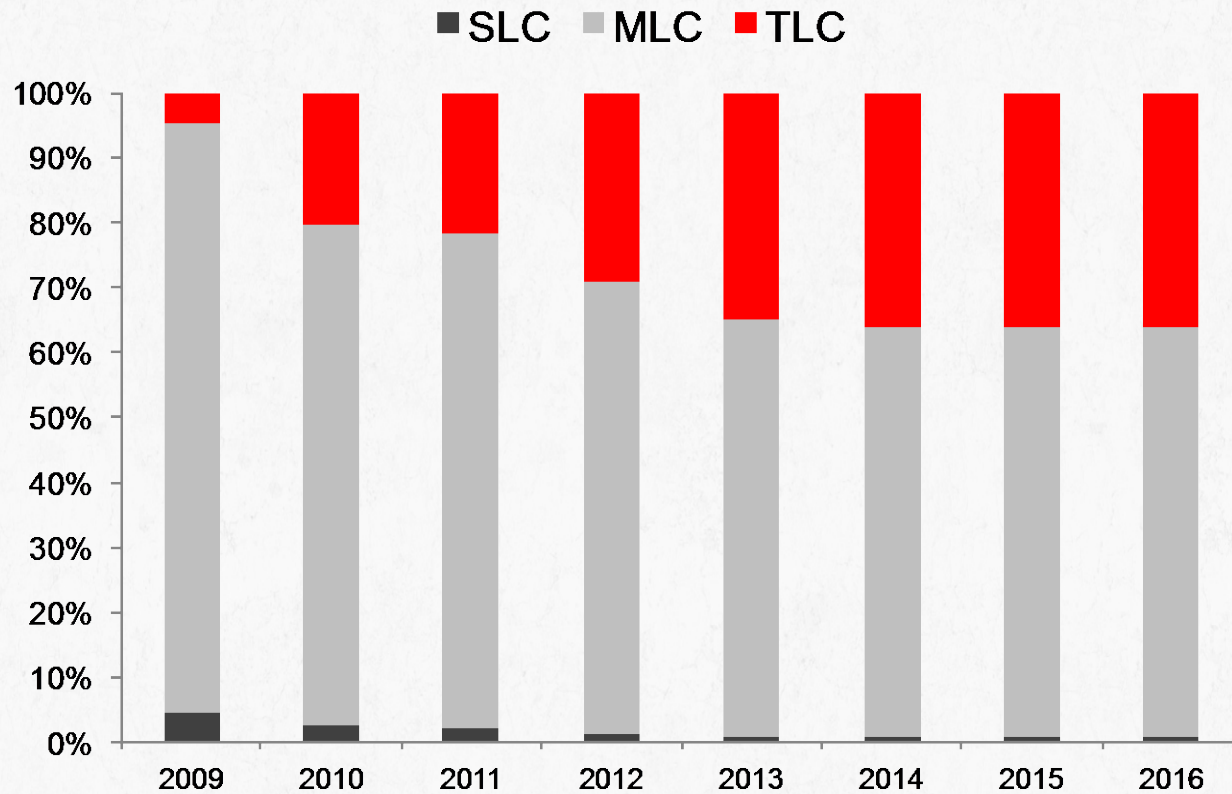
**INTERFACES AND
FORM FACTOR**

FIRMWARE

ECOSYSTEM

NAND

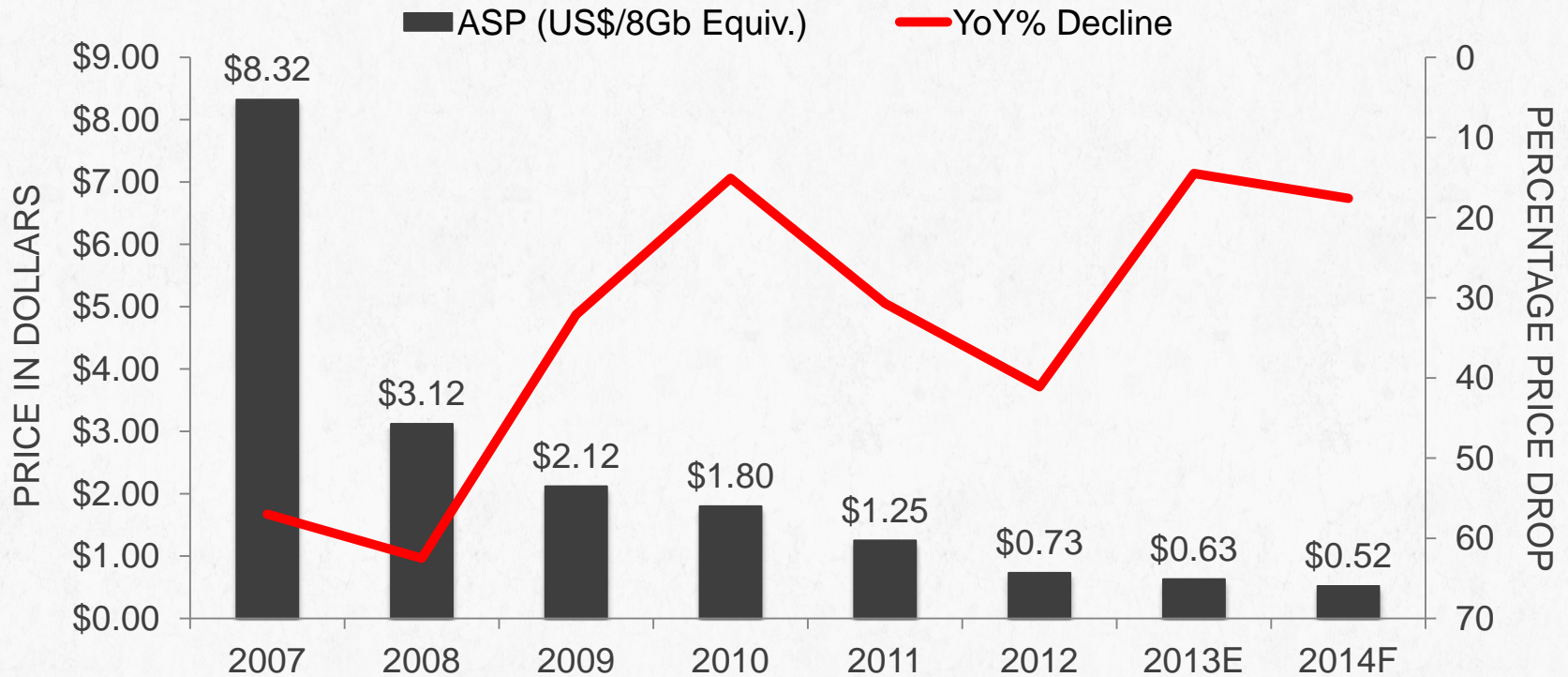
MIX IS MOVING TOWARD A HIGHER PORTION OF TLC, BUT MLC STILL DOMINATES



SOURCE: MARKET DATA

NAND

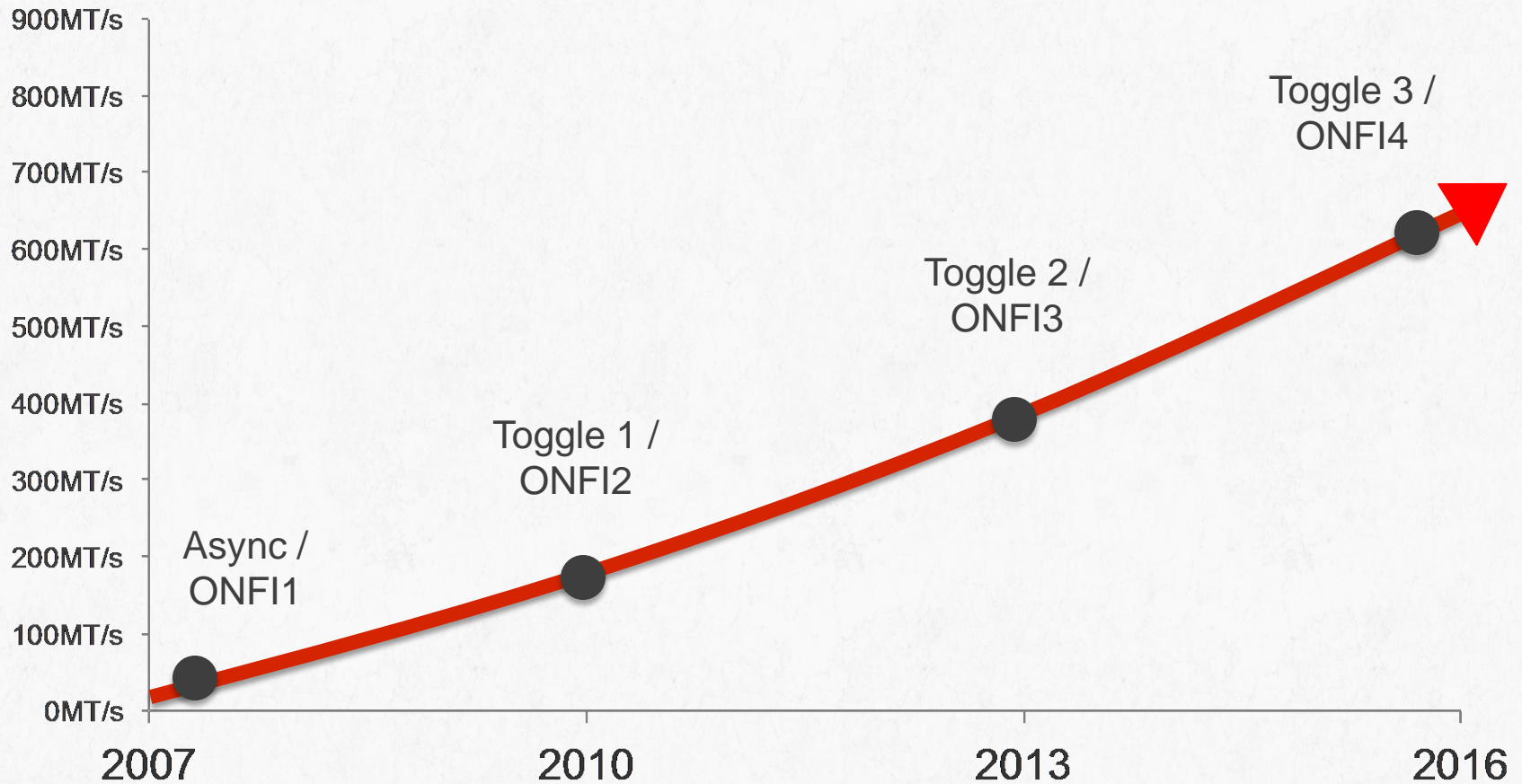
**PRICE PER GB WILL CONTINUE TO GO DOWN,
BUT MAJOR DECLINE IS ALREADY BEHIND US**



SOURCE: DRAMEXCHANGE, MAY 2013

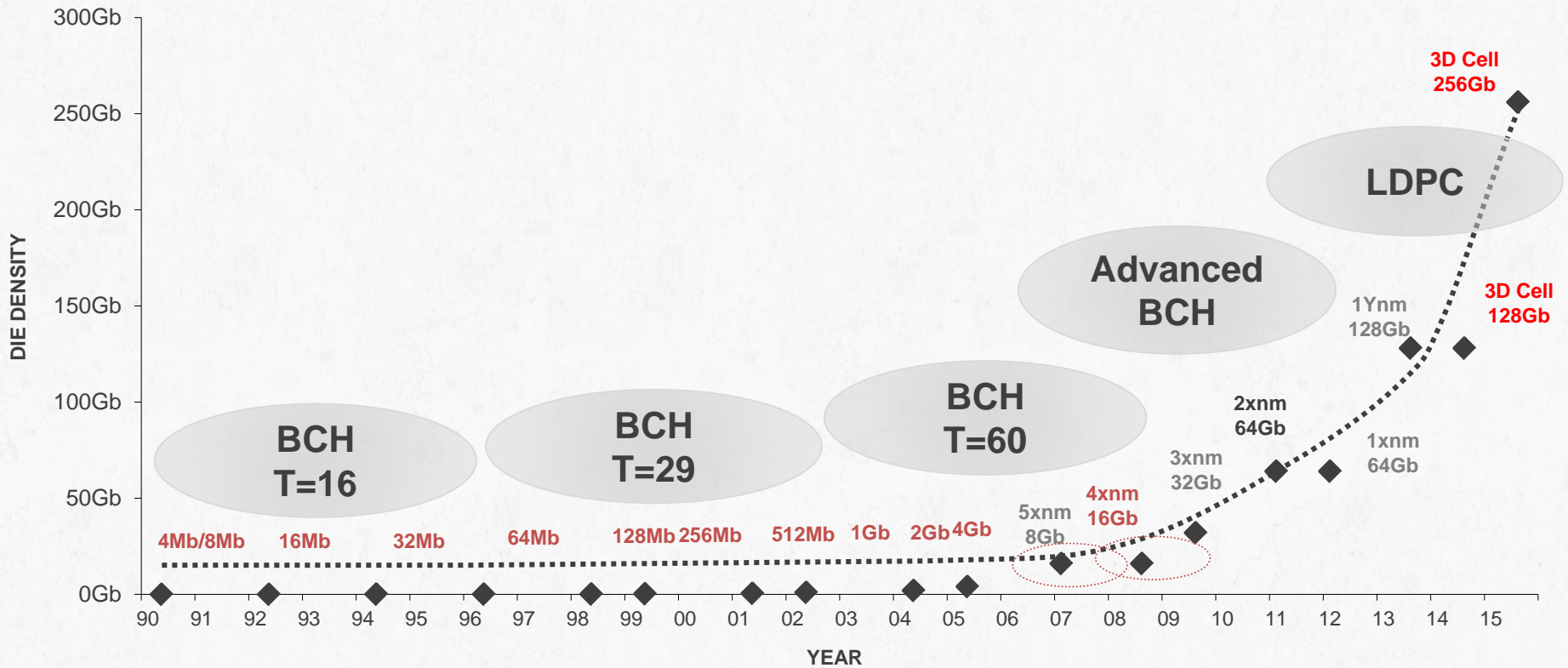
NAND

INTERFACES ARE STILL EVOLVING



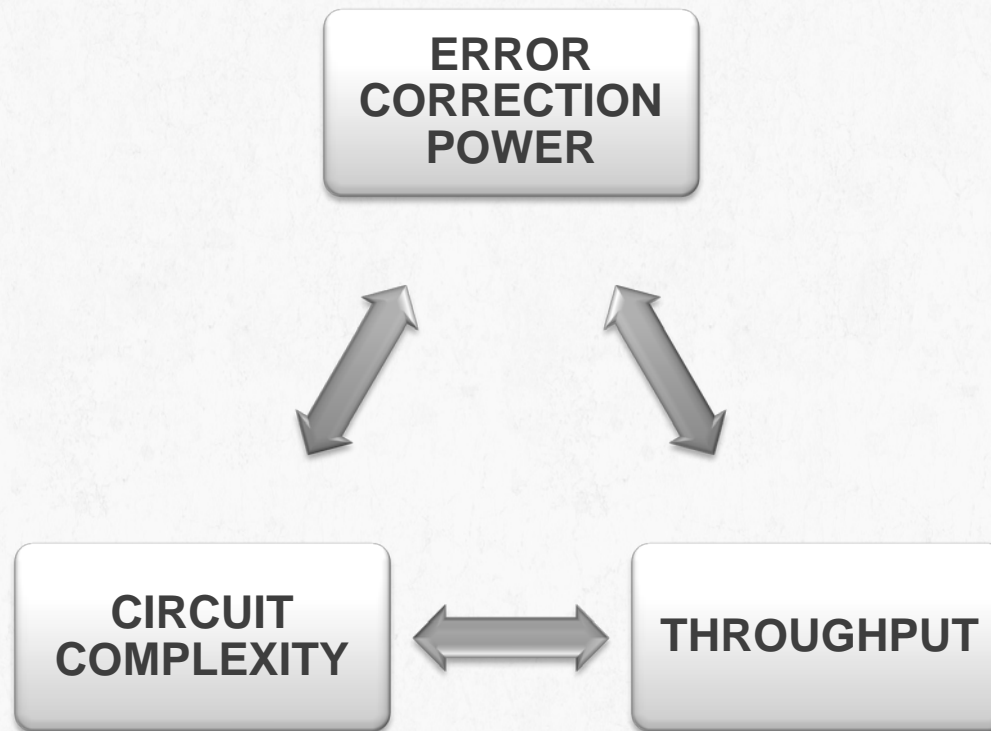
ERROR CORRECTION CODE

INCREASINGLY COMPLEX NAND DENSITY TREND
WITH REDUCED ENDURANCE
DRIVING ECC REQUIREMENTS



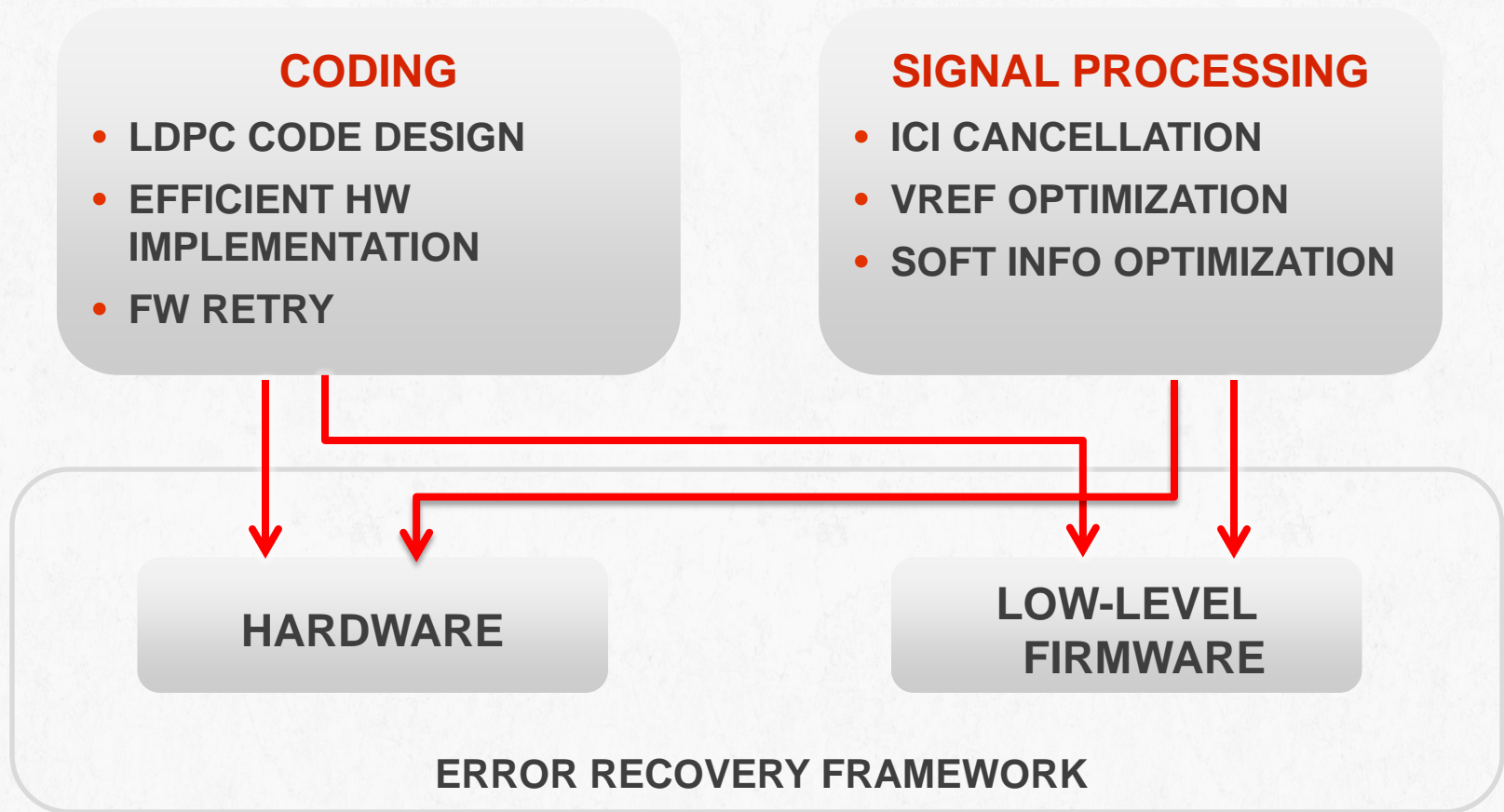
ERROR CORRECTION CODE

ECC DILEMMA – ERROR CORRECTION POWER, COMPLEXITY AND THROUGHPUT



ERROR CORRECTION CODE

**LDPC IS A TOTAL SOLUTION;
HARDWARE AND FIRMWARE INTERLEAVED**



FIRMWARE

BECOMING MORE COMPLEX AND TIGHTLY
COUPLED WITH NAND, DRIVING INNOVATION

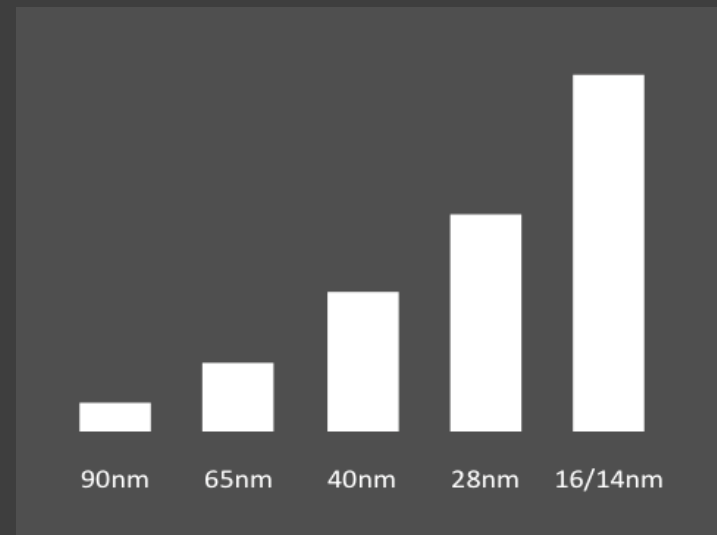


CONTROLLER TECHNOLOGY

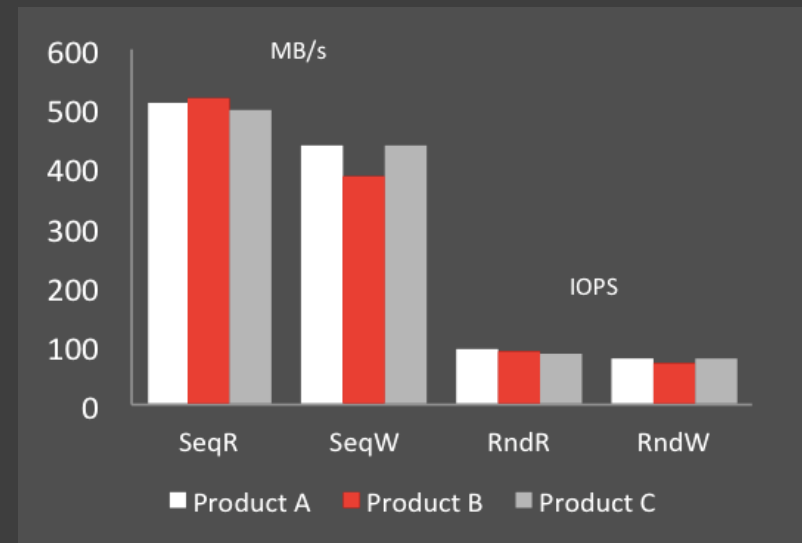
ADDRESSING THE NAND CHANGES AND EVOLVING REQUIREMENTS

- Single core to quad core
- 200 MHz to 600 MHz
- 40bit ECC to 128bit/code to LDPC
- Integrated security engines
- Power optimization
- DDRR-2 to DDRR-4 and LPDDR
- Asvnc/ONFI1- → ONFI3/Toggle 4
- Smaller package, POP, BGA, MCP
- HW automation for host commands to improve performance and reduce bottlenecks

RIISING DESIGN COSTS

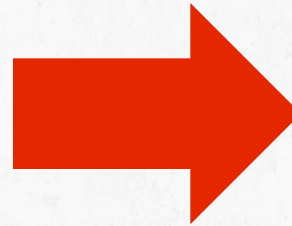


DRIVING FOR MERCHANT SILICON



FORM FACTOR AND INTERFACES

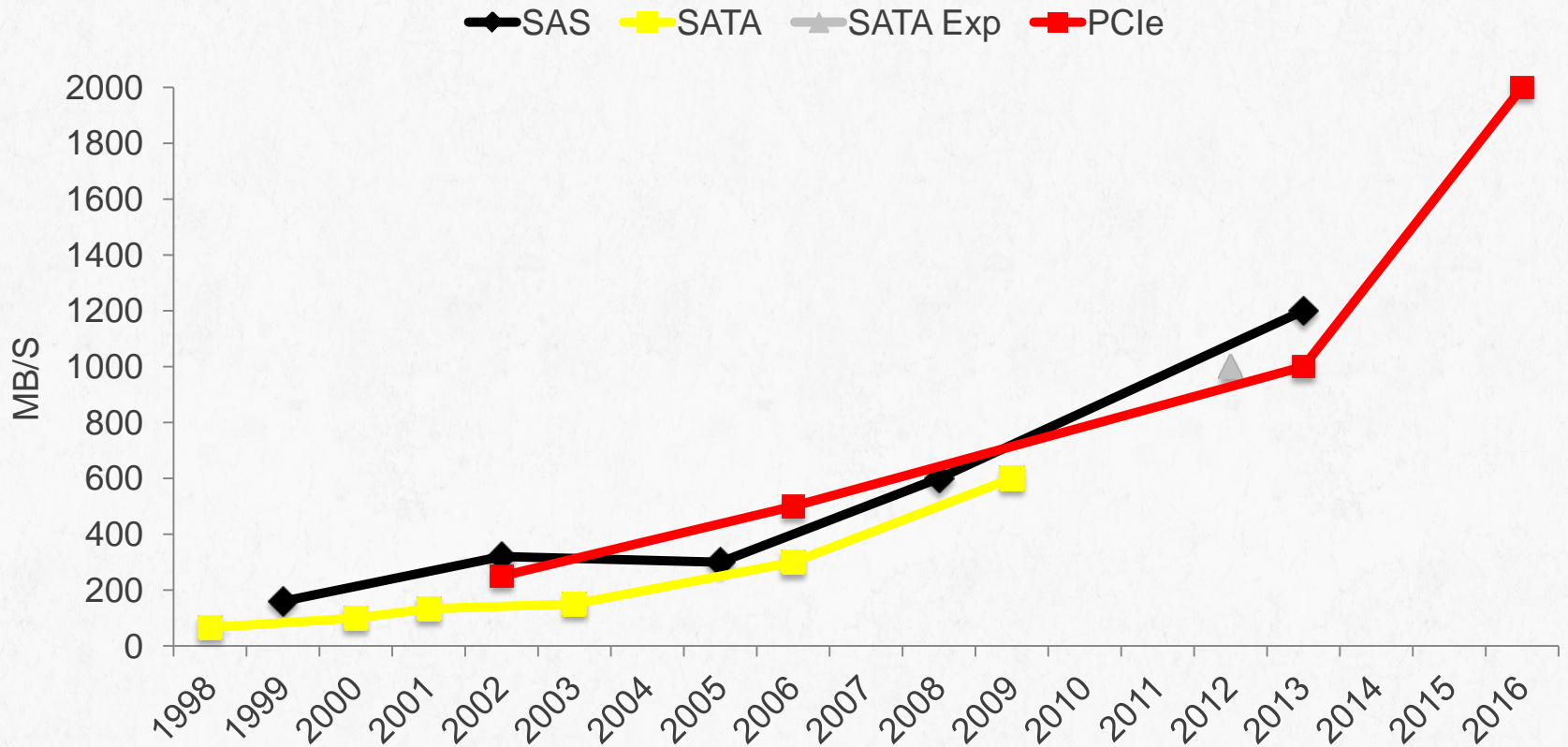
NEW CLIENT DEVICES ARE DRIVING SMALLER FORM FACTORS ALLOWING THINNER, SMALLER AND COOLER CLIENT DEVICES



1/40 SPACE

CHANGES IN SPEED AND INTERFACE

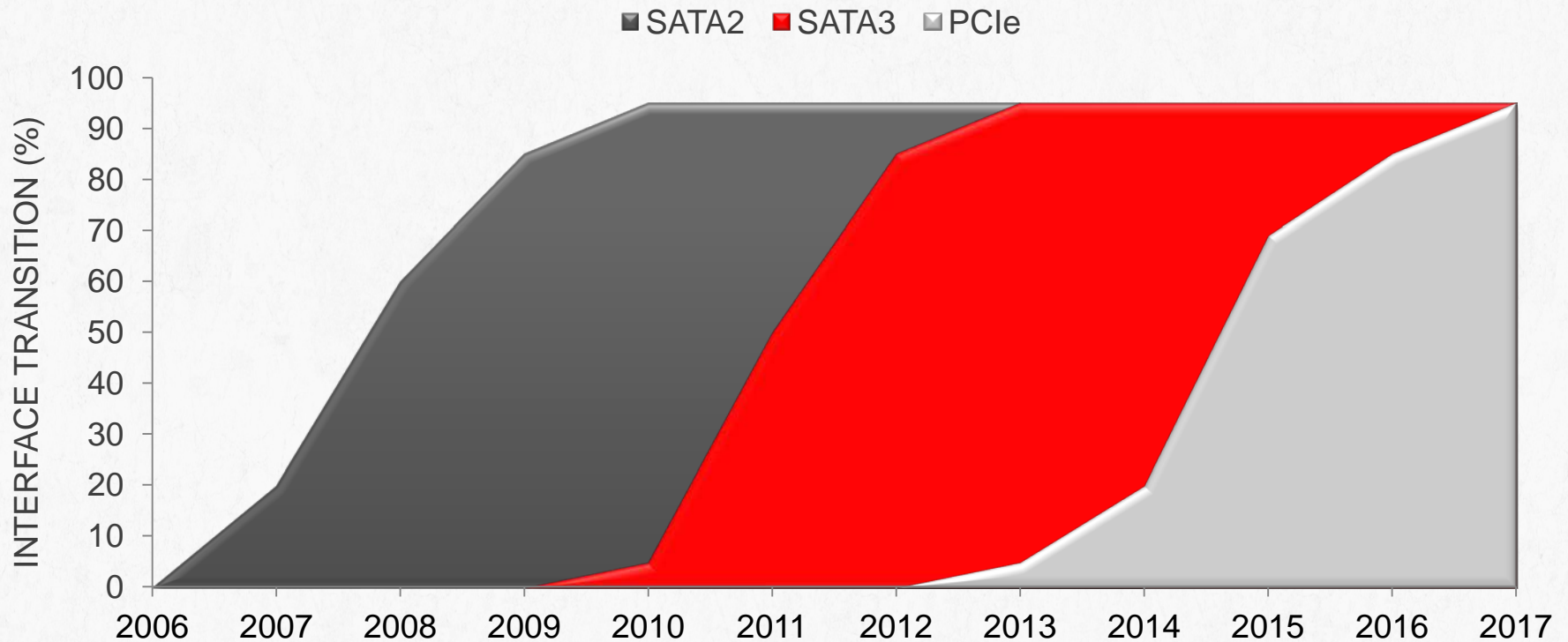
PCIe IS DRIVING THE PERFORMANCE AND POWER FOR NEXT-GENERATION SSDS



CHANGES IN SPEED AND INTERFACE

TRANSITION TO PCIe ALREADY STARTED

CLIENT STORAGE INTERFACE TRANSITIONS



SOURCE: MARVELL AND MARKET DATA

CLIENT SSD'S INTERFACE

FUTURE PATH



HIGH-END
DESKTOPS

2013

SATA
PCIe GEN2 X2/4



ULTRABOOKS

SATA
PCIe GEN2 X2/4



NOTEBOOKS

SATA



TABLETS

eMMC
SATA BGA

CLIENT SSD'S INTERFACE

FUTURE PATH



	2013	2014
HIGH-END DESKTOPS	SATA PCIe GEN2 X2/4	PCIe GEN2 X2/4 SATA
ULTRABOOKS	SATA PCIe GEN2 X2/4	PCIe GEN2 X2/4 SATA
NOTEBOOKS	SATA	SATA
TABLETS	eMMC SATA BGA	eMMC UFS SATA BGA

CLIENT SSD'S INTERFACE

FUTURE PATH



	2013	2014	2016
HIGH-END DESKTOPS	SATA PCIe GEN2 X2/4	PCIe GEN2 X2/4 SATA	PCI e GEN 2/3 SATA
ULTRABOOKS	SATA PCIe GEN2 X2/4	PCIe GEN2 X2/4 SATA	PCIe GEN 2/3 SATA
NOTEBOOKS	SATA	SATA	PCIe GEN 2 SATA SATA/PCIe BGA
TABLETS	eMMC SATA BGA	eMMC UFS SATA BGA	eMMC UFS PCIE/SATA BGA

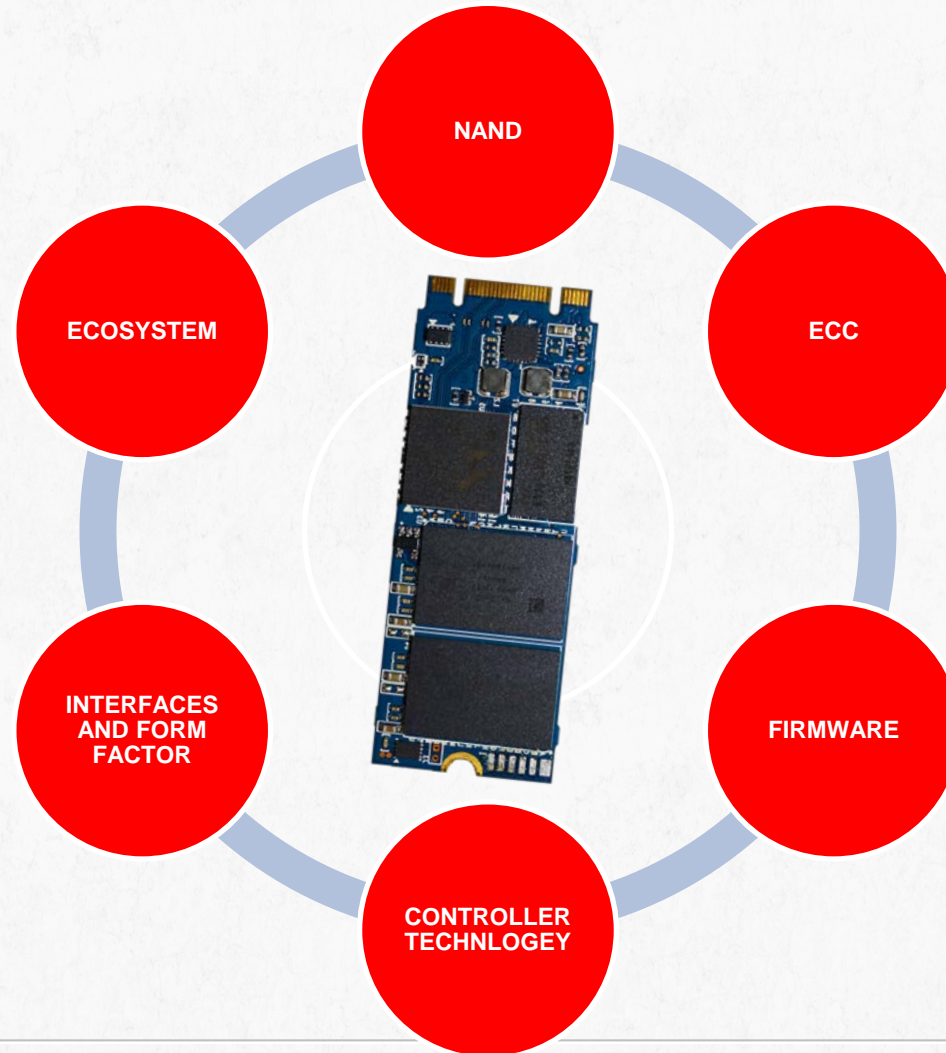


ECOSYSTEM

**DRIVING INNOVATION AND
SUPPORT FOR SMARTER AND
FASTER DEVICES**

- **CHIPSET AND OS**
 - **TRIM**
 - **LOW-POWER**
 - **NVME**
 - **PQI**
 - **STANDARDIZATION**
-

SIX AREAS DRIVING SSD EVOLUTION

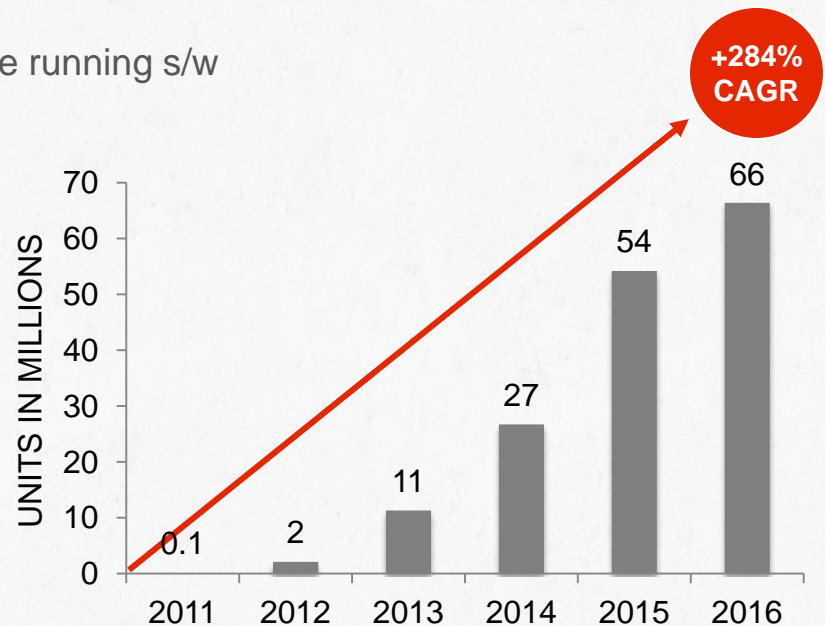




TWO MORE TOPICS ...

HYBRID HDD IS HERE!

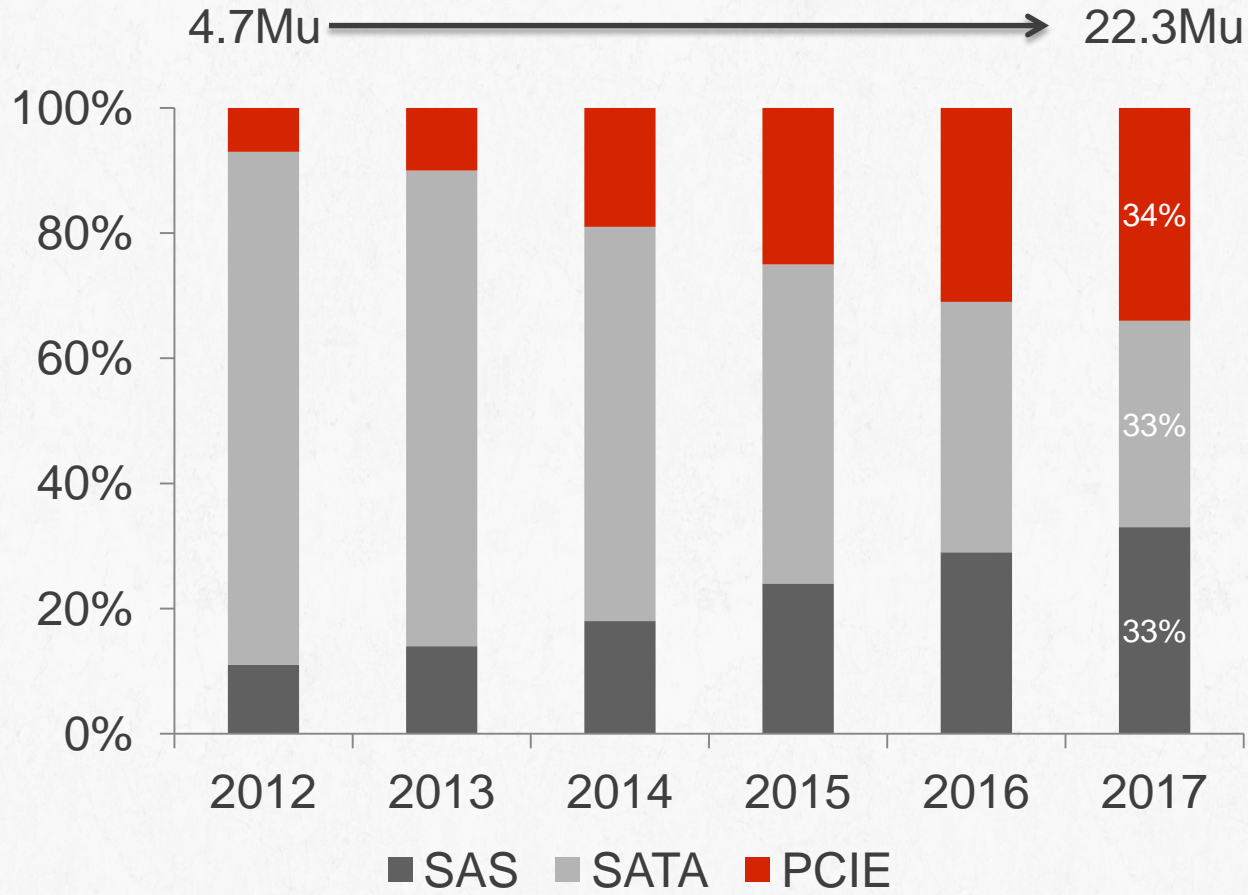
- Multiple announcements from all major HDD vendors
 - High-capacity HDD with 8-128GB NAND
- Ecosystem is being developed with chipset, drivers and OS support, and is moving in the right direction
- Two architectures exists:
 - Two-drive solution
 - HDD + SSD with chip or Southbridge running s/w for caching
 - Integrated hybrid
 - HDD + Flash with HDD SOC running s/w for caching
- Advantages of integrated hybrid HDD
 - Form factor
 - Ease of implementation
 - Cost



SOURCE: TREND FOCUS, WESTERN DIGITAL

DATA CENTER IS CHANGING

TREMENDOUS SSD GROWTH



SOURCE: MARVELL AND MARKET DATA

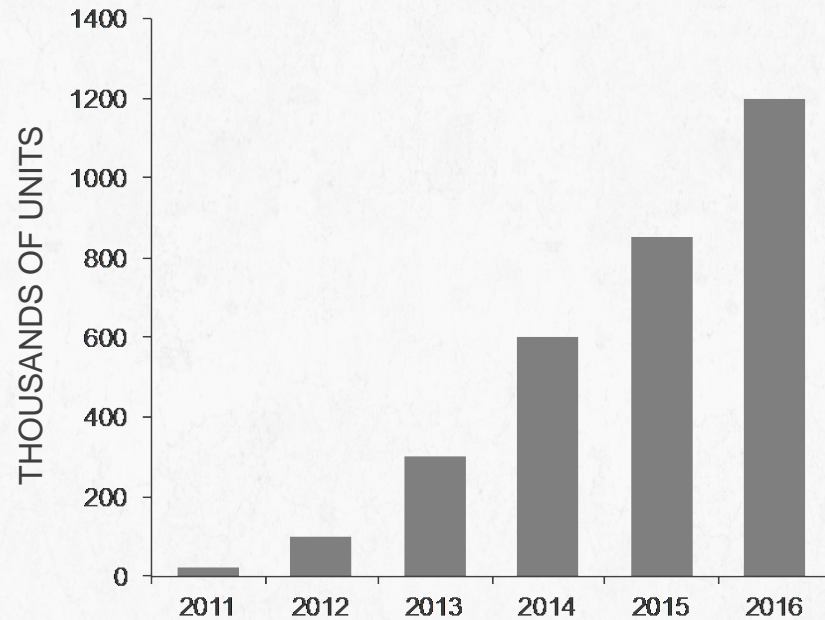
- Improved performance
- Smaller footprint
- Less power



DATA CENTER IS CHANGING

ARM-BASED MICRO SERVERS ARE A REALITY

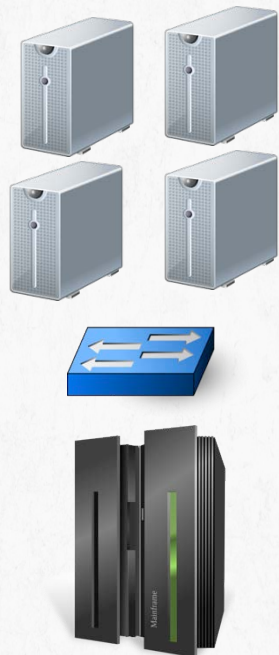
WORLDWIDE MICROSERVER SHIPMENT FORECAST



SOURCE: IHS ISUPPLI RESEARCH, FEBRUARY 2013

MARKET SHIFT TO **SCALE-OUT** **CONVERGENCE**

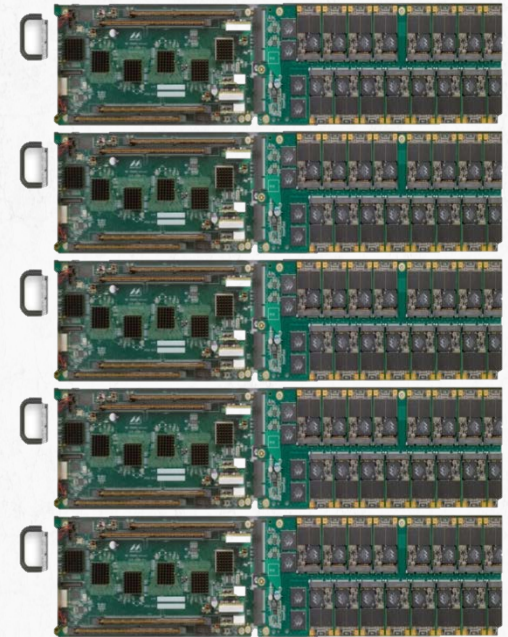
**SMP SERVERS AND
NETWORKED STORAGE**



**SCALE-OUT CONVERGED
STORAGE SERVERS**



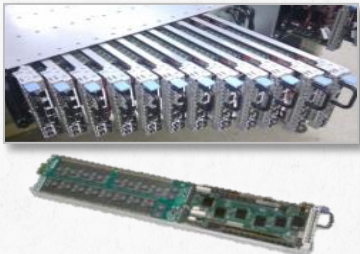
**ARM HDD/SSD CONVERGED
STORAGE SERVERS**



LONG TERM: **ALL-SILICON SERVERS**



**MARKET SHIFT FROM
MONOLITHIC SERVERS
TO RACK CONTAINERS
FOR HIGH-DENSITY
ARM SSD SERVERS**



**TODAY:
21 X86 HDD SERVERS**

25K IOS/RACK

42U RACK SPACE

8400W

100X HIGHER IOPS →

90% LESS SPACE →

80% LOWER POWER →

**FUTURE:
1X ARM SSD SERVERS**

2.5M IOS/RACK

3U RACK SPACE

1600W

2016 PREDICTIONS

Extremely high-performance client SSD based on TLC

**Integrated hybrid HDD to consume
3% of NAND output**

**LDPC is driving TLC-based SSD volume
in the mainstream enterprise**

**Integrated server, storage and networking for
scale-out systems driving innovation in the data center**



THANK YOU

MARVELL: SERVING A WIDE RANGE OF THE STORAGE INDUSTRY

