

MRAM, the Next Storage Memory

a non-volatile memory

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Flash Memory Summit 2013 Santa Clara, CA

Avalanche Technology Fremont, CA

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Flash Memory Increased Applications Demand Performance





Flash Memory MRAM System Applications

Buffer for Critical Data Storage



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Flash Memory SSD Applications

Tiered & Cached Storage

SSD Tiered Storage



SSD Cached Storage



Adding SSD & HDD capacity together (hot and cold files are periodically scanned at the File System level)

Avalanche Advantages

Lower Power Consumption

 Reduces Data Center Power Consumption by an Order of Magnitude

DRAM-Based Performance

10x Faster Read/Write Speed than NAND SSD

Superior Endurance

→ 10¹⁶ vs. 10⁵ for SLC NAND

Lower Latency

Latency Performance of DRAM-Based SSDs

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Flash Memory An Industry Leader

- Founded in 2006
- Seasoned Executive Team
- Extensive IP Portfolio
- Strong Balance Sheet
- Focused Go-To-Market
- ✤ 150+ Patents

Leading Technology



Fabless Universal Non-Volatile Memory (NVM) Technology

Led by an Experienced Team in Execution Mode

Addressing Large Markets with a Focused Strategy





Storage, Mobile, and Compute System Design Markets

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Today's Memory Challenges

DRAM

Cell Capacitance

<2xnm is Uncertain</p>

Capacity Challenges

>8Gb no feasible

FLASH

FG below 1xnm uncertain

- Retention
- Reliability

SLC <50K, MLC <3k



Avalanche MRAM



Rapid and Tremendous Progress





Go-to-Market Strategy



Highly Leveraged MTJ Memory Design Enables Parallel Product Positioning

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Thank You!

