



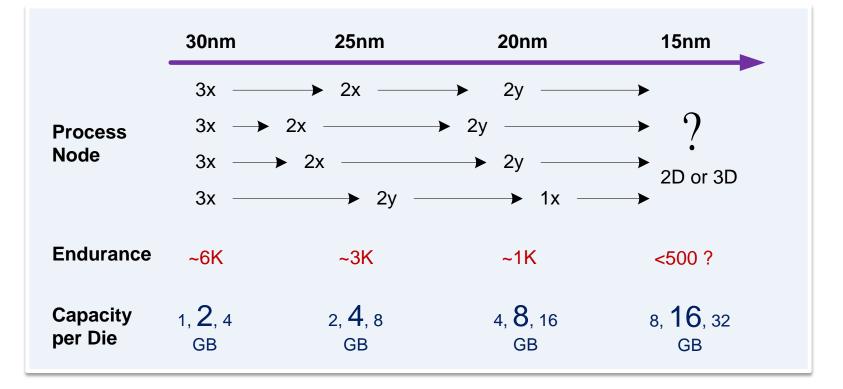
SSD Controller Technologies for TLC NAND

Stanley Huang

Product Marketing Manager Silicon Motion, Inc.

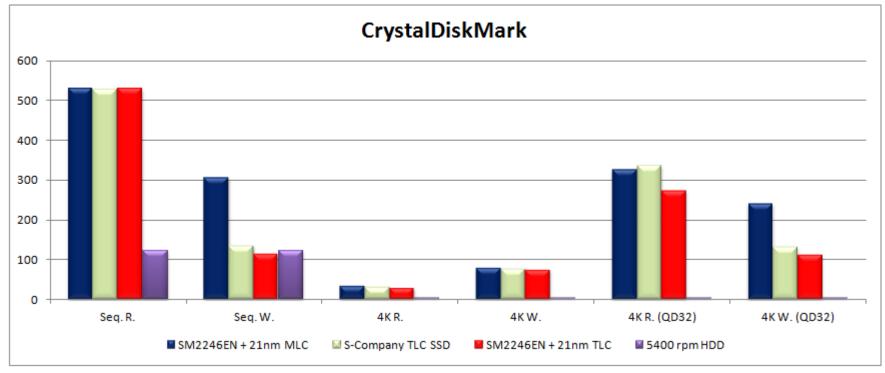


- New NAND Flash
 - Advanced smaller process nodes
 - Double capacity, but with half endurance capability





- 128GB TLC SSD vs MLC SSD vs HDD
- Read Performance: MLC = TLC = **4X** HDD!
- 4K Write Performance: MLC = 2X TLC; TLC = **120X** HDD



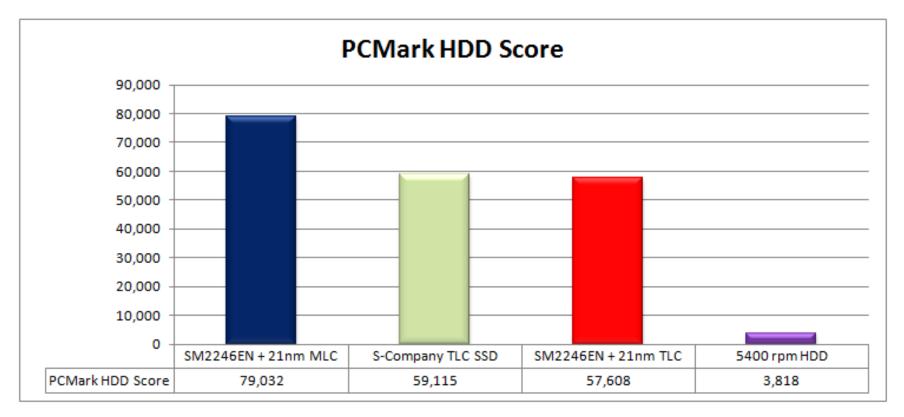
* Based on Silicon Motion Internal Testing







- TLC SSD is around 70% of MLC SSD (128GB)
- 128GB TLC SSD: 16X HDD!!



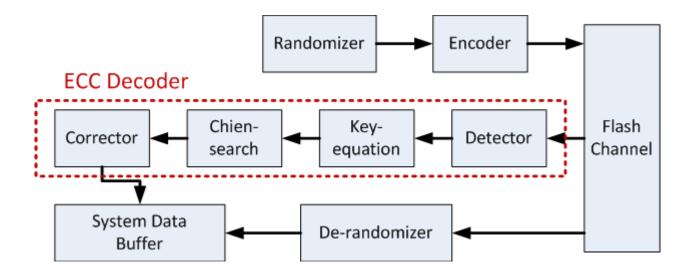
* Based on Silicon Motion Internal Testing

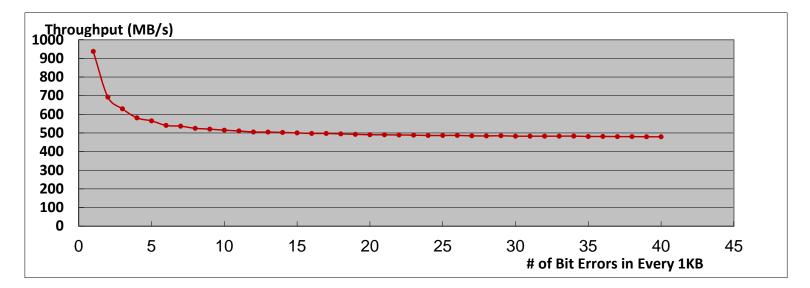


Memory Sustainable ECC Throughput

Fla

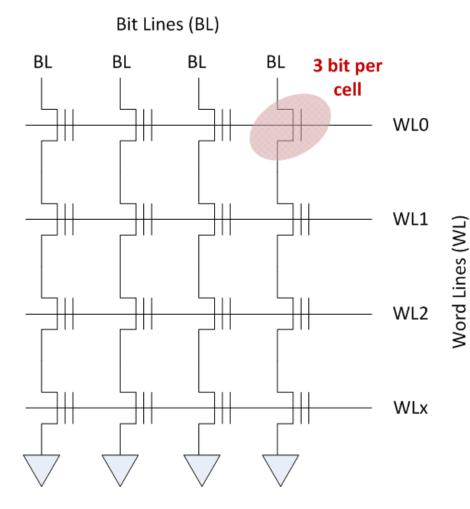
SUMMIT







TLC Architecture – Program Disturbance



Program Disturbance

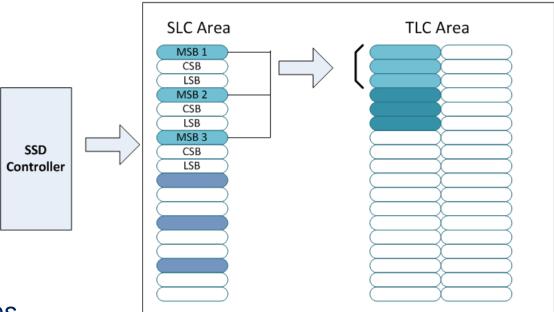
- Voltage state shifted when programming the adjacent WL
- 3 times in a special order
 - 0-1-0-2-3

• ECC errors occur if reading data before WL completely programmed.





• 3 SLC block -> TLC block



TLC NAND

Pros and Cons

- 1. Better data loss prevention when sudden power off
- 2. Write speed drops when SLC cache full
- 3. Smaller capacity

Flash Memory Summit 2013 Santa Clara, CA





Endurance vs. Retention, BCH vs. LDPC

1KB BCH Protection							1KB LDPC Prote		
84 hrs	0	х	х	Х	Х	х	84 hrs	0	
74 hrs	0	х	x	х	х	x	74 hrs	0	
68 hrs	0	х	х	х	х	х	68 hrs	0	
60 hrs	0	х	х	Х	Х	х	60 hrs	0	
52 hrs	0	х	x	х	х	x	52 hrs	0	
44 hrs	0	х	х	х	х	х	44 hrs	0	
36 hrs	0	х	х	Х	Х	х	36 hrs	0	
28 hrs	0	х	х	Х	x	x	28 hrs	0	
24 hrs	0	х	х	х	х	x	24 hrs	0	
20 hrs	0	х	х	Х	Х	х	20 hrs	0	
16 hrs	0	х	х	Х	х	x	16 hrs	0	
12 hrs	0	0	х	х	х	x	12 hrs	0	
8 hrs	0	0	х	Х	Х	х	8 hrs	0	
4 hrs	0	0	0	х	х	x	4 hrs	0	
Endurance	0	0	0	0	0	0	Endurance	0	
P/E Cycles	600	1200	1800	2400	3000	3600	P/E Cycles	600	

1KB LDPC Protection								
84 hrs	0	0	0	х	х	x		
74 hrs	0	0	0	х	х	x		
68 hrs	0	0	0	х	х	x		
60 hrs	0	0	0	0	Х	x		
52 hrs	0	0	0	0	Х	x		
44 hrs	0	0	0	0	Х	x		
36 hrs	0	0	0	0	X	x		
28 hrs	0	0	0	0	0	x		
24 hrs	0	0	0	0	0	x		
20 hrs	0	0	0	0	0	x		
16 hrs	0	0	0	0	0	x		
12 hrs	0	0	0	0	0	x		
8 hrs	0	0	0	0	0	x		
4 hrs	0	0	0	0	0	x		
Endurance	0	0	0	0	0	0		
P/E Cycles	600	1200	1800	2400	3000	3600		

Based on: 2ynm TLC, 120C baking

SiliconMotion 8

All data sectors are correctable

At least one data sector is uncorrectable

Flash Memory Summit 2013 Santa Clara, CA



Life Span Estimation – JEDEC Workload

- According to JESD-218 standard
 - 49GB workload data on 128GB SSD
 - Data collected on standard laptop used for office productivity,

storage of photos, music and Apps

Total # of Commands	39,923,531	%
# of Trims	2,498,963	6.26%
# of Writes	35,391,419	88.65%
# of Flush	2,033,149	5.09%
% of Sequential Writes		24.36%
% of Random Writes		75.64%
Total amount of trim data (GB)	764.92	
Total amount of write data (GB)	727.64	





- Manage the data on 4KB basis
- WAF = 1.5 (based on Silicon Motion's Firmware Management)

	LDPC									
	3K Endu	irance	1.5K En	durance	500 Endurance					
	49GB/day	20GB/day	49GB/day	20GB/day	49GB/day	20GB/day				
128GB	14 yrs	35 yrs	7 yrs	17 yrs	2.4 yrs	5.8 yrs				
256GB	28 yrs	70 yrs	14 yrs	35 yrs	4.8 yrs	11.7 yrs				
512GB	57 yrs	140 yrs	28 yrs	70 yrs	9.5 yrs	23.4 yrs				

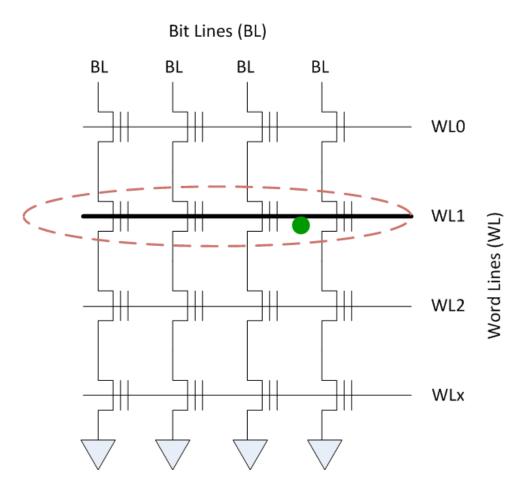


Flash Memory Summit 2013 Santa Clara, CA

© Copyright Silicon Motion, Inc., 2013. All Rights Reserved.





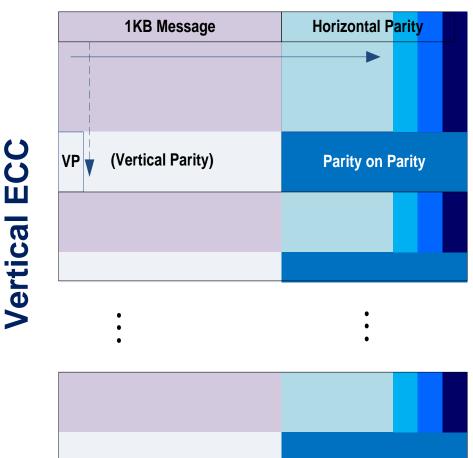


- Whole page data lost
- A particle on a Word Line to make WL thinner then broken
- Cannot be found by test
 patterns
- Long time burn in can screen some potential failure out



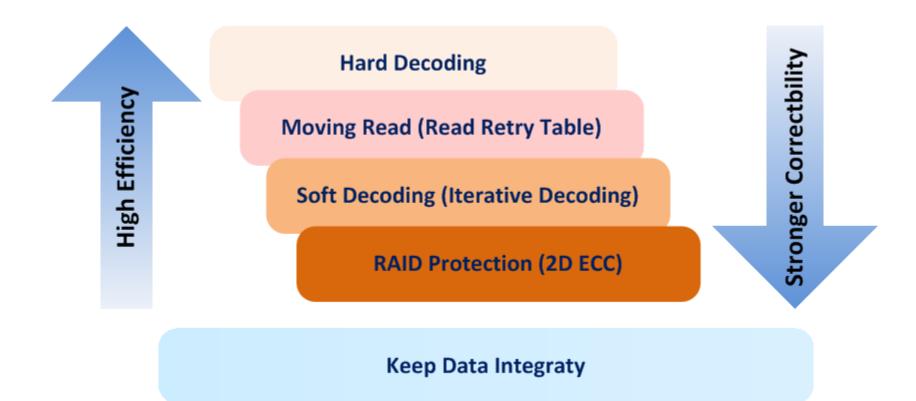


Horizontal ECC: LDPC or BCH or RS



- Vertical ECC on a page base
- RAID5-Like
 - (k, 1) by XOR,
- RAID6-Like
 - (k, 2) by RS-code
 - (2,2), (4,2),, (253,2)









- TLC has the cost advantage for consumer SSD
- TLC SSD speed is much faster than hard drive
 - Read/write throughput and overall performance
- LDPC and different technologies extend the endurance and

retention effectively, also secure the user data

• It will be coming soon



Flash Memory Summit 2013 Santa Clara, CA



Email: stanley.huang@siliconmotion.com

Visit us at the booth 615

Disclaimer Notice

Although efforts were made to verify the completeness and accuracy of the information contained in this presentation, it is provided "as is" as of the date of this document and always subject to change.

Flash Memory Summit 2013 Santa Clara, CA



© Copyright Silicon Motion, Inc., 2013. All Rights Reserved.