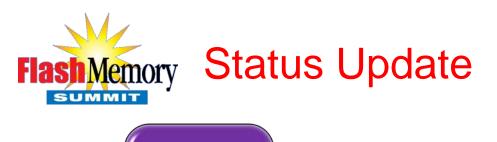


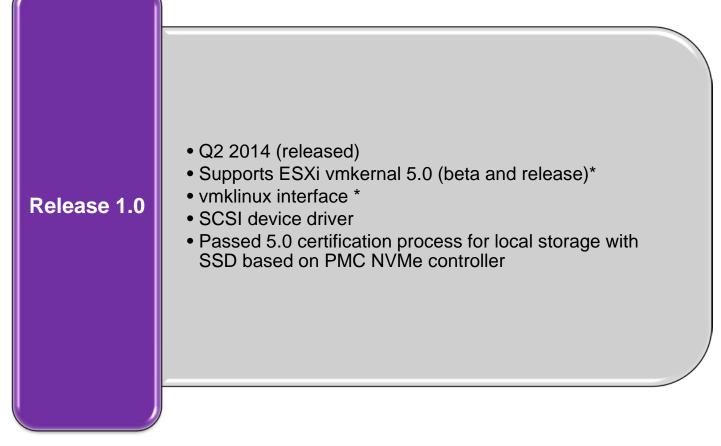
## NVMe VMware vmklinux Driver

## Kwok Kong Sr Director of Software Engineering PMC-Sierra



- Status Update
- Driver Architecture
- Driver Features
- Future





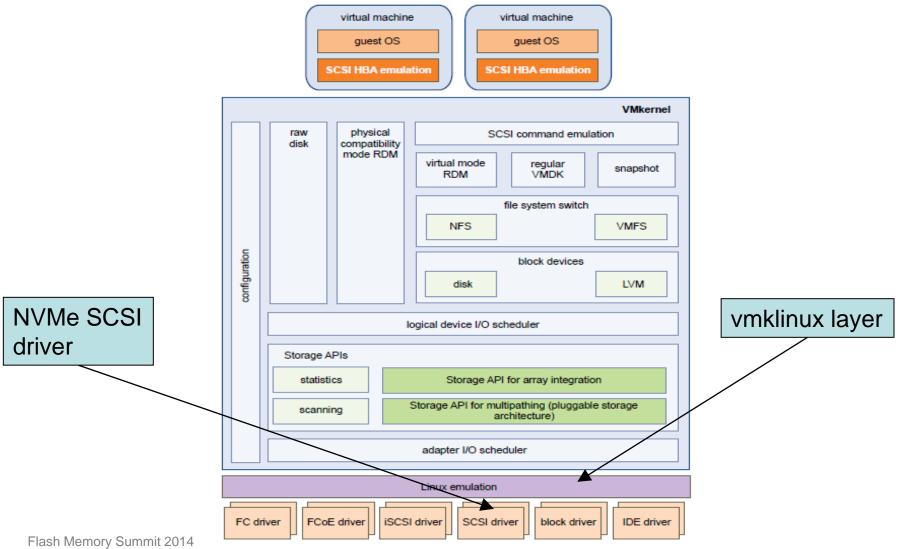
First release

Joint development between VMware and PMC started in Q3 2013

Santa Clara, CA

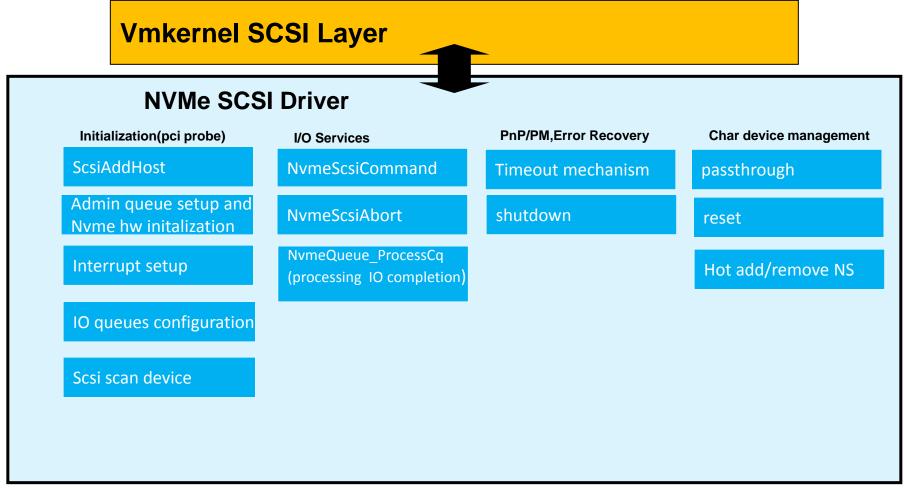
\*Other names and brands may be claimed as the property of others.

# Flash Memory VM ware Storage Architecture



Santa Clara, CA







# Flash Memory System Features Overview

Features	Supported
NVM Express version	1.0e
ESXi versions	5.0 (beta and release)
NUMA Optimized Queues	Dedicated IO queues per NUMA node Single Admin Queue for all CPU cores
Interrupt	MSI-X only(coalescing not supported)
Queue Arbitration / Priority	Round Robin only 1:1 mapping between Submission and Completion queues
Pass Through	Yes(both Admin and IO)
Multiple Namespaces	Up to 8, Mapped to Bus 0, Tgt 0, LUN 0-7
Registry Parameters	Name Space, Max Transfer Size, Admin Queue Size, IO Queue size
End-to-End Protection	No
NVMe reset	Yes (using ioctl)



### Need your contribution to write the future



- Register with Sourceforge
  - <u>https://sourceforge.net/user/registration</u>
- Subscribe to the project update
  - <u>http://sourceforge.net/projects/nvmevmklinuxdriver/</u>
- Driver Source Code
  - <u>http://nvmevmklinuxdriver.sourceforge.net/</u>



# Thank You!Image: Descent of the second second

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