

Building efficient RAID-5 systems across SSDs at the FTL Layer

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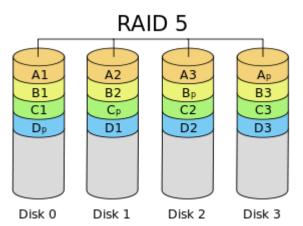
- Motivation
- Conventional RAID5 architecture drawbacks
- Building efficient RAID5 systems across SSDs at the FTL layer
- Conclusions



- SSDs provides sufficient IOPS, but RAID-5 is still commonly employed for
 - Higher capacity (e.g., 8 to 12 drives)
 - Increased bandwidth
 - Improved reliability
- Issues:
 - RAID-5 write hole
 - Performance loss due to partial-stripe writes
 - Performance degrades due to performance fluctuation between SSDs



• Rotational parity, resilient to one SSD failure



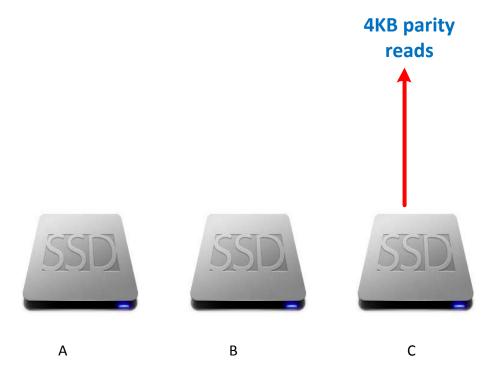
- Without parity cache, random writes result in
 - Write amplification >>2
 - Faster wear out
 - Write holes



4KB writes Image: SSDD state Image: SSDD state A B C

Write amplification occurs when Drive-A is full



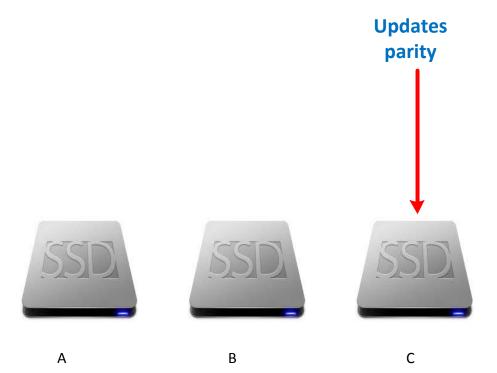




Computes updated parity









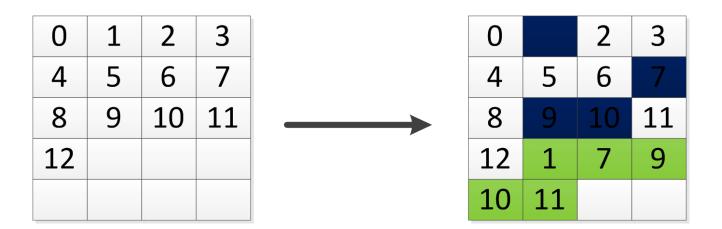
 Random write performance does not scale linearly to # of SSDs





Dynamic mapping of LBA and PBA in SSDs

- Conventional RAID controller treats SSD just as another type of HDD
- SSDs are fundamentally different from HDDs viz. dynamic association of LBAs to PBAs

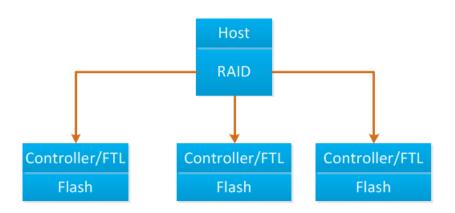




- In SSDs, the invariants are Physical Block Addresses (PBAs)
- RAID could be built upon the invariants of PBAs
- Off-loading FTL from SSDs facilitates RAID construction on PBAs

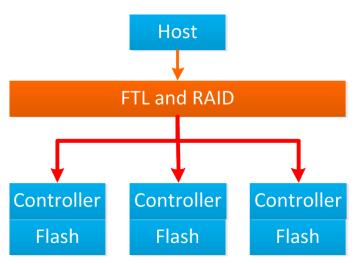


- Off-loading FTL from SSDs
- Unified FTL and RAID layer solves the issues with conventional RAID.



Conventional RAID5 of SSDs

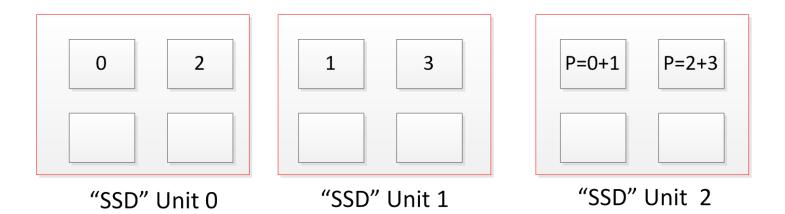
Flash Memory Summit 2014 Santa Clara, CA



The new RAID5 architecture



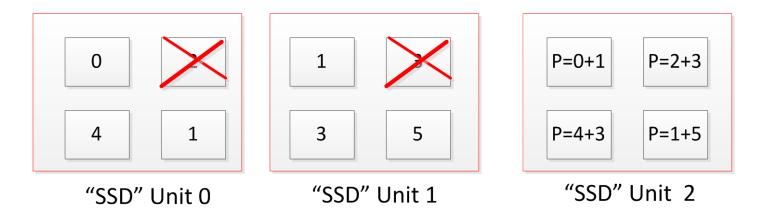
- Initial state
 - Numbers are LBAs
 - Blocks are PBAs





• Next state

- Numbers are LBAs
- Blocks are PBAs





- Joint FTL and RAID-5 layer provide better system efficiency:
 - Global GC and wear leveling
 - Random small writes WAF can be less than 2
 - No more read-modify-write
 - No partial parity stripes write performance loss
 - Write hole can be gracefully mitigated by partial parity writes or filling dummy data.



Hardware accelerated parity calculation

- FTL and RAID are co-located
- FTL are primarily based on software
- Parity computation can be hardware accelerated by seeding raw data to the hardware controller.
 - Parity computation are carried out in hardware
 - Parity are subsequently written out to the parity "SSD"



Shannon Direct-IO[™] SFF-8639 PCIe Flash



- Natively PCIe, supports hot plug
- Built in cross-drive RAID with hardware acceleration
- Superior sustained 4KB random write performance



 Shannon Systems is the leading provider of PCIe Flash and associated flash systems in the China market.



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