

Competing Technologies and Architectures for Networked Flash Storage

Ethernet/InfiniBand/OmniPath/PCI/FibreChannel/SAS

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- API are evolving for optimal use of SSD
- FC and SAS falling behind the speed curve
- Ethernet, IB and OmniPath on same PHY curve
- PCI on different slightly slower PHY curve
- Ethernet, IB, and OmniPath
 - Have different reach
 - Same protocol stack efficiencies



	Bandwidth (Gbps)	Reach
SAS	3, 6, 12	Rack
Fibre Channel	4, 8, 16, 32	Rack, Data center
PCI x1/2/4/8/16	8, 16, 32, 64,128	Rack
Ethernet	1, 2.5, 5, 10, 25, 40, 50, 100	Rack, Data Center, LAN, MAN, WAN
Infiniband	8, 16, 32, 56, 112	Rack, Data Center
OmniPath	100, 200	Rack, Data Center



PHY SERDES (single lane) curves



	2010	2015	2018
SAS	6	12	24
Infiniband	10	25	50
Ethernet	10	25	50

Infiniband and Ethernet

- Same PHY curve
- Same speed curve





- Preserves software investment
- Realizes some of the SSD speedup benefits
- Disaster Recovery (DR) requires MAN or WAN



NVMe Storage Server

60-300+ miles





- Ethernet or IB or OmniPath fabric
- PCIe fabric not sufficient reach or scaling
- RDMA required for sufficient efficiency
 - IB and OmniPath use RDMA
 - Ethernet has RoCE, iWARP and iSCSI with RDMA
- Disaster Recovery (DR) requires MAN or WAN







- Preserve software investment
- Alternatively jump directly to native SSD API



Ethernet, Infiniband, OmniPath

- Infiniband, OmniPath
 - Reliable link layer
 - Credit based flow control
- Ethernet
 - Ubiquitous
 - Pause and Prioritized Pause (PPC) for lossless operation that propagates through some switches and fewer routers
 - Flow Control and Reliability at higher layer e.g. TCP, and IB Transport Layer for RoCE



Flash Memory Comparing Ethernet Options

	DCB Required	Reach	IP routable	RDMA
FCoE	\checkmark	Rack, LAN		\checkmark
iSCSI	No	Rack, datacenter, LAN, MAN, WAN Wired, wireless	\checkmark	\checkmark
iWARP	No	Rack, datacenter, LAN, MAN, WAN Wired, wireless	\checkmark	\checkmark
RoCEv2		Rack, LAN, datacenter	\checkmark	\checkmark



Memory Comparing Ethernet Options

iSCSI, iWARP

- Use DCB when it is available but not required for high performance
- iSCSI
 - Has RDMA WRITE and accomplishes RDMA READ by using an RDMA WRITE from other end-point
 - Concurrent support for legacy soft-iSCSI



RDMA bypasses the host software stack

- RoCE
- iWARP
- iSCSI with offload



Flash Memory NVMe over RDMA fabrics Target

 Bypass RDMA



Application Buffer **Sockets Buffer Buffer** TCP/IP NIC **Buffer** Driver iWARP/RoCE/IB **Buffer** Offload

Initiator



Flash Memory iSCSI with offload Target

 Bypass RDMA



Initiator





Flash Memory iSER with offload Target

 Bypass RDMA



Initiator

Application		Buffer	
Sockets		Buffer	
TCP/IP		Buffer	
NIC Driver		Buffer	
iWARP/Ro Offload	CE	Buffer	5













RDMA

- Control Plane on host or ASIC/FPGA/SoC •
- Data Plane PCIe-host-PCie or PCIe only



- Control Plane Intel/ARM host
- Data Plane PCIe-ASIC/FPGA/SoC-PCIe



Memory NVMe over fabrics comparison

- Option 1
 - Flexible
 - Extra latency incurred by copy/copies
- Option 2
 - Minimizes latency by removing host and host memory system from data path



- API are evolving for optimal use of SSD
- Ethernet, IB, and OmniPath
 - On same SERDES PHY (single lane) curve
 - Have different reach
 - Same protocol stack efficiencies



Questions?

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