

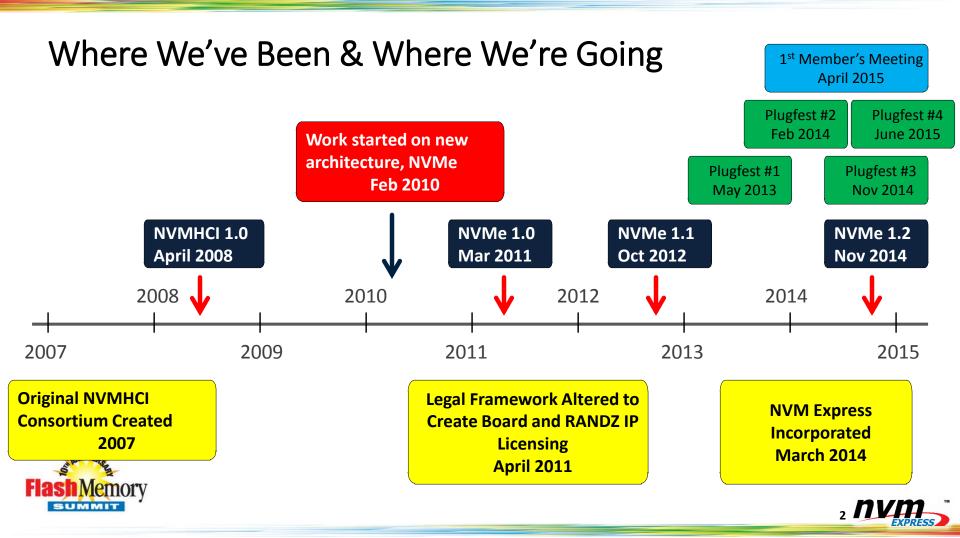
NVMe

Where We Are
Where We Came From
Where We're Going
And what you're going to hear today!

Don H Walker

July 25, 2015





Where We Are Today

- Governance and Legal Structure
 - Delaware Non-profit Corporation since 2014
 - Corporate Officers and Board-of-Directors
 - Governed by a set of By-Laws and Corporate Charter
- Members and Membership Growth
 - Currently have 89 members
 - 104 members at the end of 2013
 - 76 members at the end of 2014

- Technical groups and Efforts
 - Management Interface TWG Started 2014
 - Working on NVMe Fabrics
 - NVMe-T11 MOU signed
 - NVMe-TCG Liaison Agreement underway
 - Technical proposals around client and even consumer related applications
 - Benchmarking tools
- Budget
 - We have ~ \$200K yearly income





Board Of Directors

Governed by an elected board of 13 directors from member companies

Cisco



Dell



HGS

Micron

EMC



HGST



Micron

Intel





NetApp























AMSUNG

ORACLE!





NVMe Ecosystem – Going Native!

- NVMe Drivers Available on Windows*, Linux*, Solaris*, VMware*, UEFI
- Many are native / in-box drivers















SLES 12

Wide Selection of Protocol Analyzers





- Agilent
- Teledyne LeCroy
- Others



Windows

Server 2012 R2

Certified



VIAVI





The Future is Bright



- NVMe products are accelerating in 2015 and 2016!
- 2nd NVMe Members Meeting next year
- Management Interface 1.0 coming in Q3
- NVMe over Fabrics specification in Q4
- More innovation for revision 1.3

"There's not much else to say. NVMe seems to have achieved its goal of making PCIe drives as painless as their SATA peers..."

Digital Trends, 4/2/15





Its Real!

Go To

WWW.NVMExpress.org/Products

or

Google "NVMe Products"





Staying True to Our Principals and Ideals

<u>Feature</u> <u>Benefit</u>

Leading IOP and Throughput Parallelism and Scalability Performance in all Configurations Allows You to Put the Power Where it **Resource Allocation & Partitioning Matters Most** No Locks and minimized un-cacheable **Optimized Queuing Layer** Register reads/writes No Bloated and Unnecessary Commands to Efficient command Set Slow I/O



NVM Express is architected for performance!



The Track

- Management Interface
- Fabrics
- Security
- Ecosystem
- Real World use cases
- Mobile Platforms
- Future Directions





Architected for Performance