

Scale-out Storage Architectures in the NVM Era

"Evolution or Revolution?"

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Flash Memory Summit 2015 Santa Clara, CA

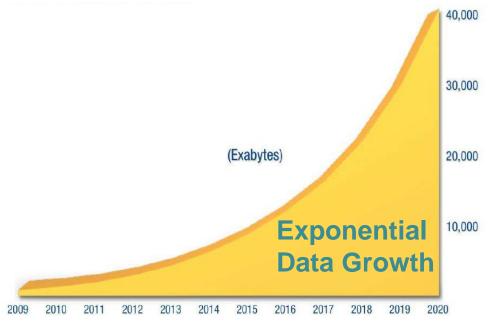


- Why Enterprise Storage → Scale-out Architectures
- Why NVM != <Yet Another Storage Tier>
- Why New Scale-out Design != Clean slate





Exponential Data Growth in Enterprises

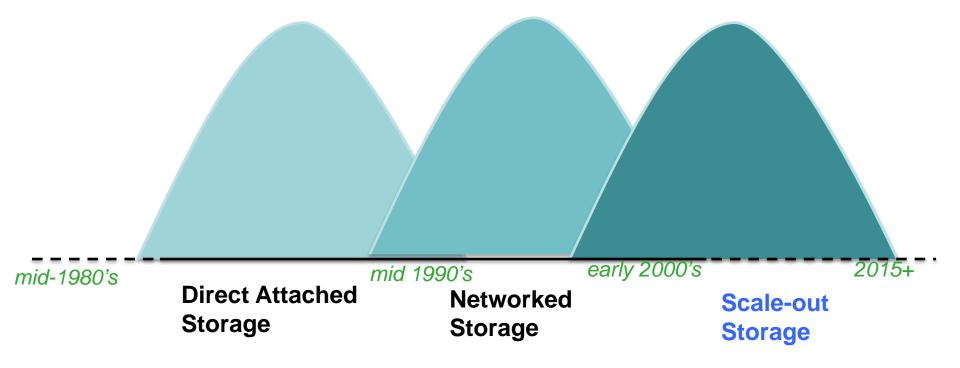


Source: IDC's Digital Universe Study, 2012

"There were 5 Exabytes of information created between the dawn of civilization through 2003, but that much information is now created every 2 days."

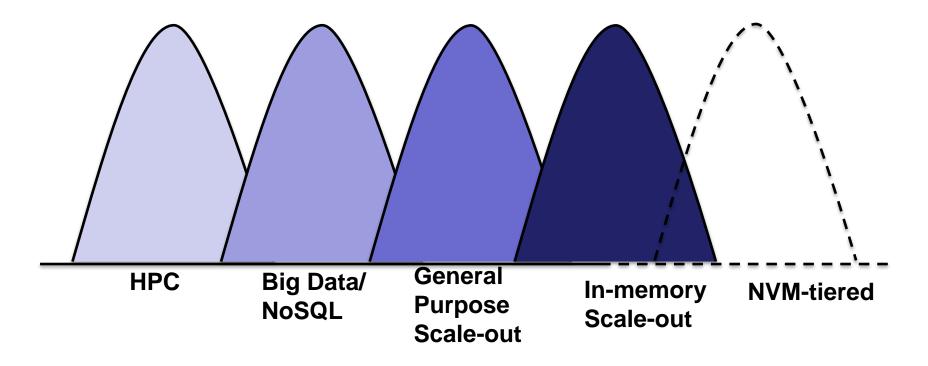
Eric Schmidt, Google 2010 Convention





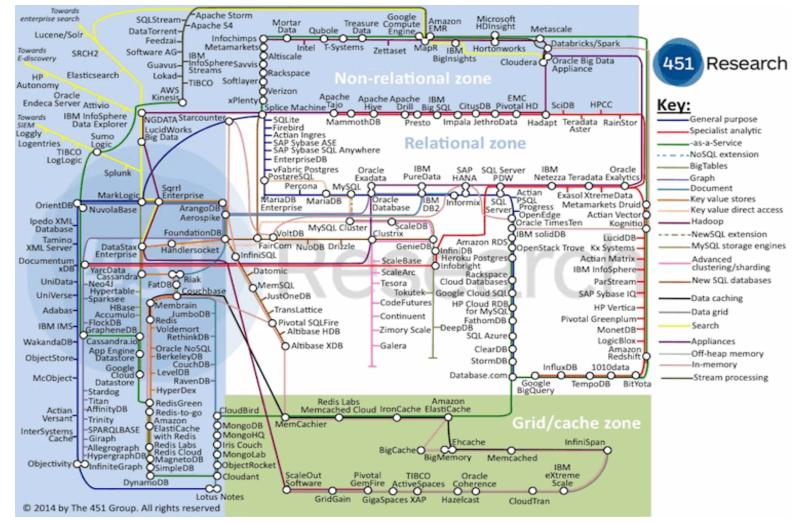


 Lustre IBM GPFS VMWare VMFS Veritas CFS Cassandra HDFS MongoDB HBase 	 VMWare VSAN Ceph GlusterFS Swift Redis Cluster Stanford RamClou Spark/Tachyon VoltDB/H-Store 	d
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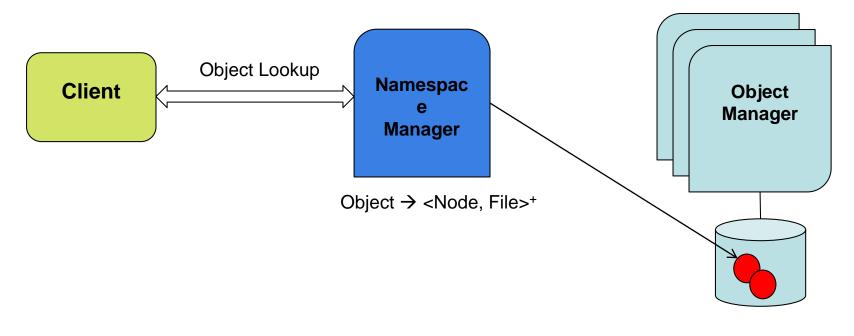
Scale-out Architectures: End of *one-size-fits-all*





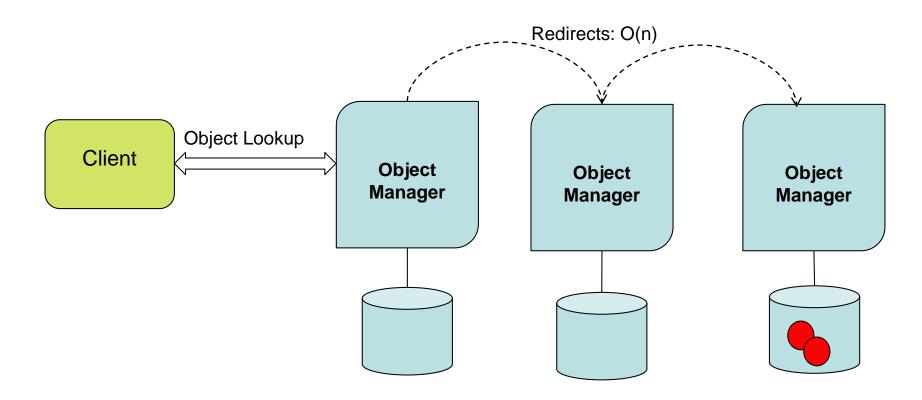
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Directory Lookup Design Pattern

Flash Memory Summit 2015 Flash Memory Scale-out Design: Object Lookup

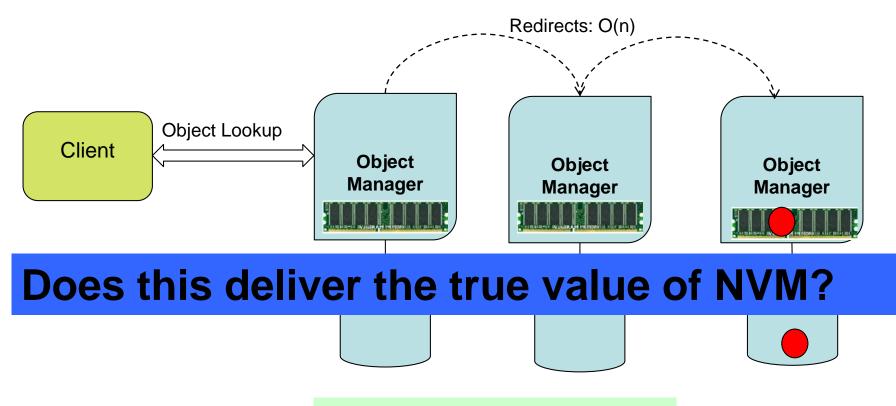


Range-based Lookup Design Pattern

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Scale-out Design: Object Lookup



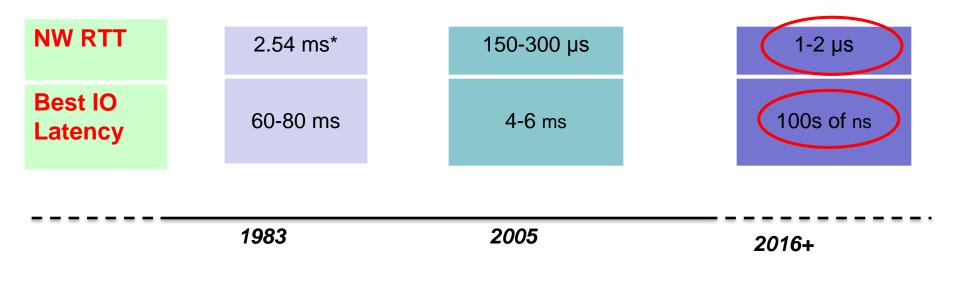
Range-based Lookup Pattern

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Network is the new bottleneck

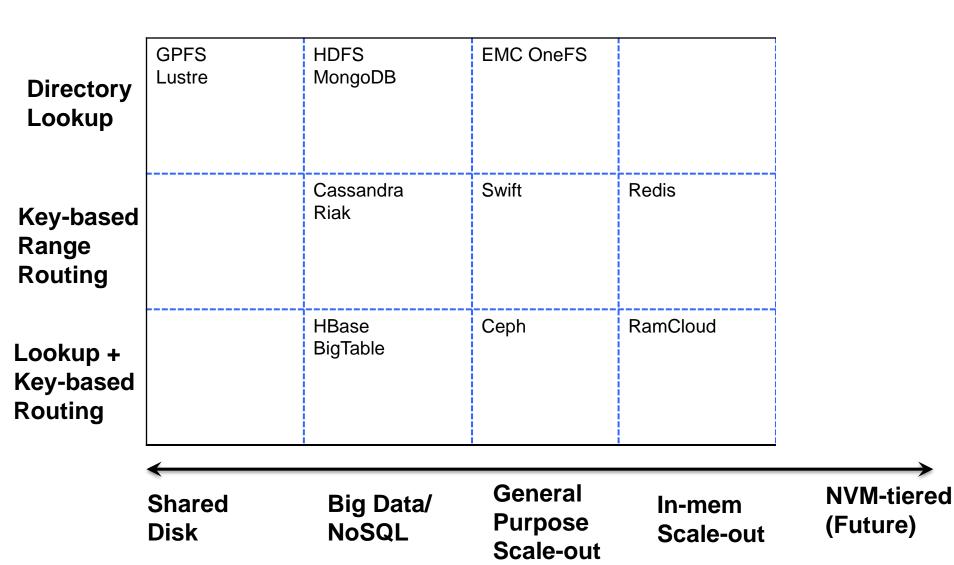


*S. Rumble, et. al. Its time for low Latency. HotOS 2011

Slide 11



Object Lookup: ★ Client-aware OClient-opaque

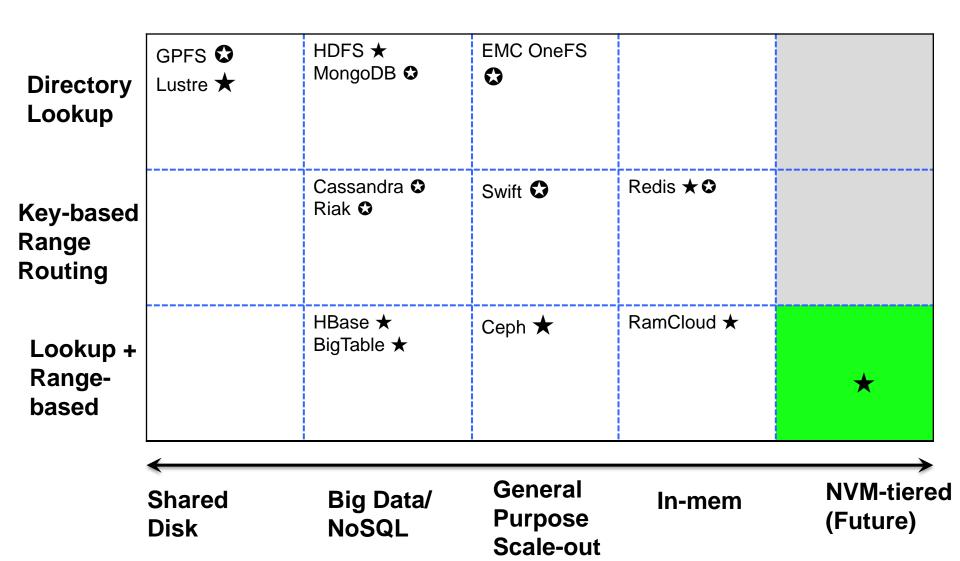




Object Lookup: ★ Client-aware O Client-opaque

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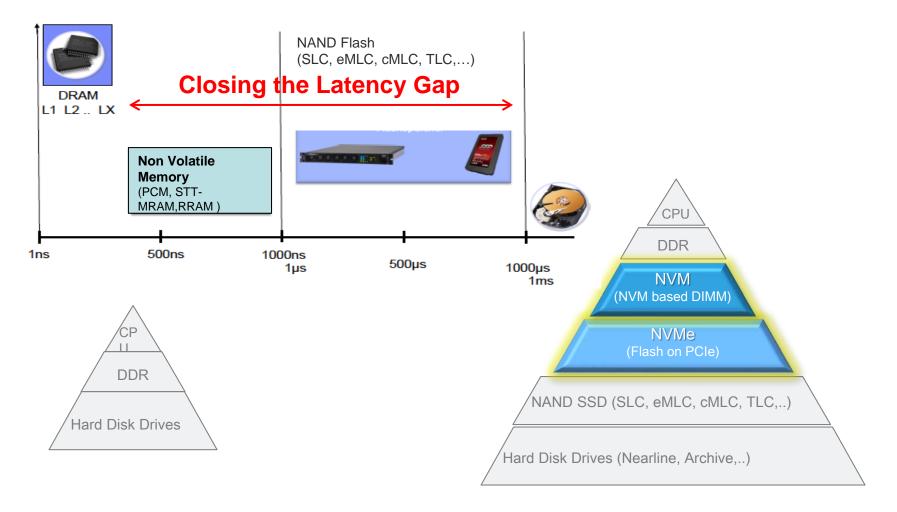




		BW Capacity & Cost Latency				
Application	POSIX/ACID Batch-oriented	Non-POSIX/BASE Real-time	Beyond traditional block/file POSIX applications			
CPU	1x10MHz	16x3GHz	Slow-down of Moore's law			
Memory	< 2 MB	>16GB	Bigger & Cheaper			
Network	3Mbps	10Gbps	Network is becoming the new latency bottleneck			
Storage	<30MB	>4TB	Emergence of distinct Capacity and Performance Storage Tiers			
Avg. Node Configuration 1984 2012						



Memory The New Storage Hierarchy



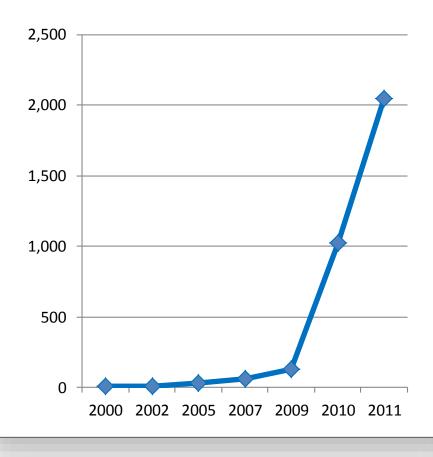
mid-1980's

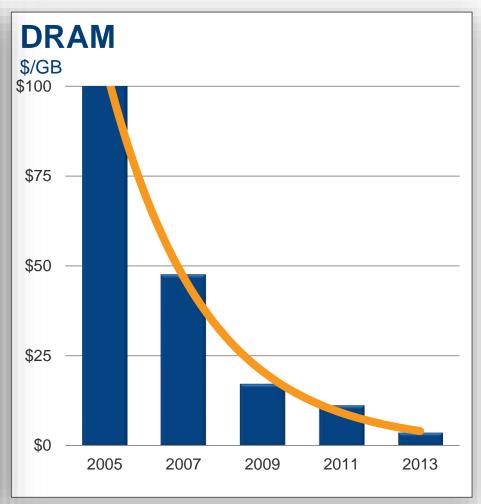
Memory Bigger and Cheaper

Maximum DRAM in GB

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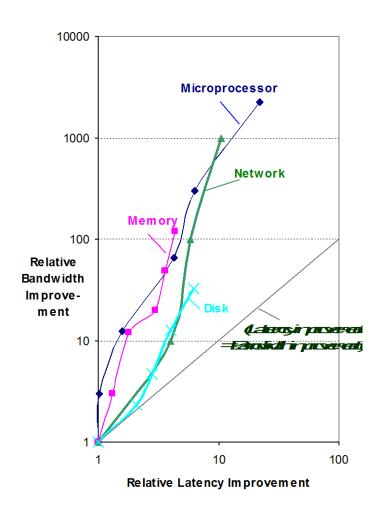


Source: Forrester Research, The x86 Server Grows Up And Out (October 8, 2010)

Source: Gartner Dataquest, Forecast: DRAM Market Statistics (1Q11)



Latency lags Bandwidth*



	Mid-1980s	2009	Change
Disk capacity	30 MB	500 GB	16667x
Max. transfer rate	2 MB/s	100 MB/s	50x
Latency (seek & rotate)	20 ms	10 ms	2x

Source: Stanford RamCloud Talk, Feb 2010

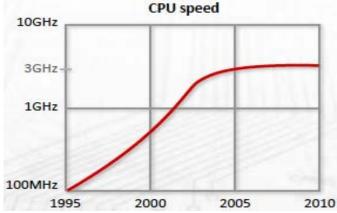
*David Patterson, Latency Lags Bandwidth, CACM, 2004



CPU Scaling: Slow-down of Moore's Law

CPU Scaling is not exponential anymore

- 3000% increase from 1995-2004
- 30-40% increase since 2004
- Multi-core scaling is linear
 - NUMA, locking, sharing latencies, programming models...
- Compute per unit of data is decreasing

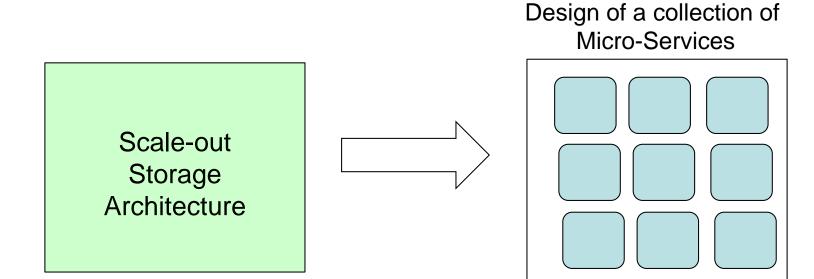




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- Why NVM != <Yet Another Storage Tier>
- Why New Scale-out Design != Clean slate Re-design

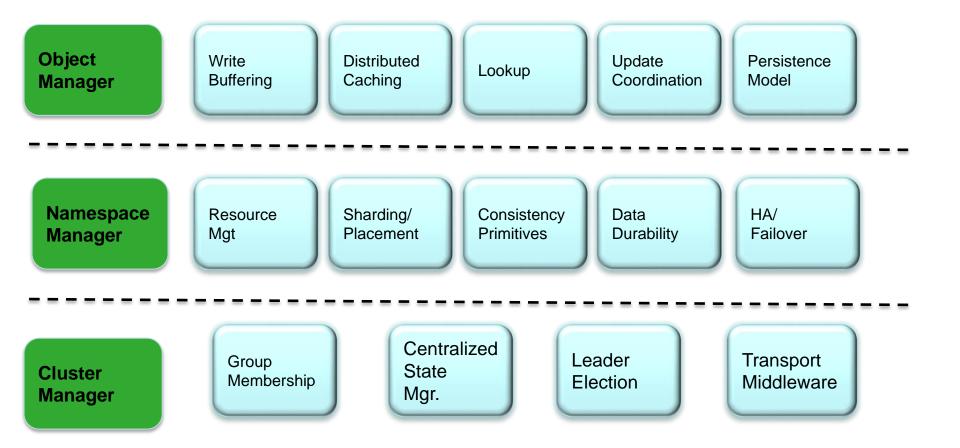


What does re-design mean?

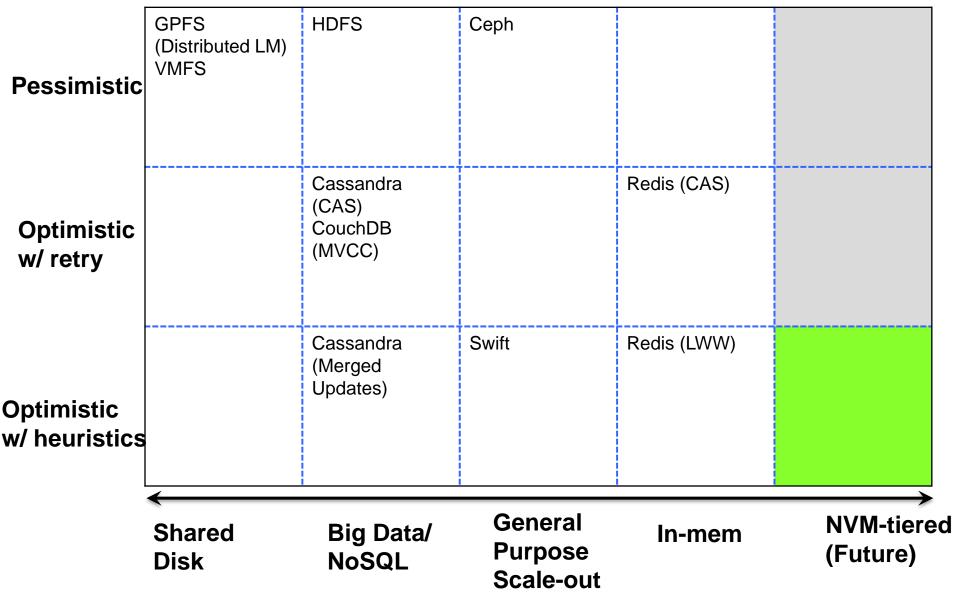


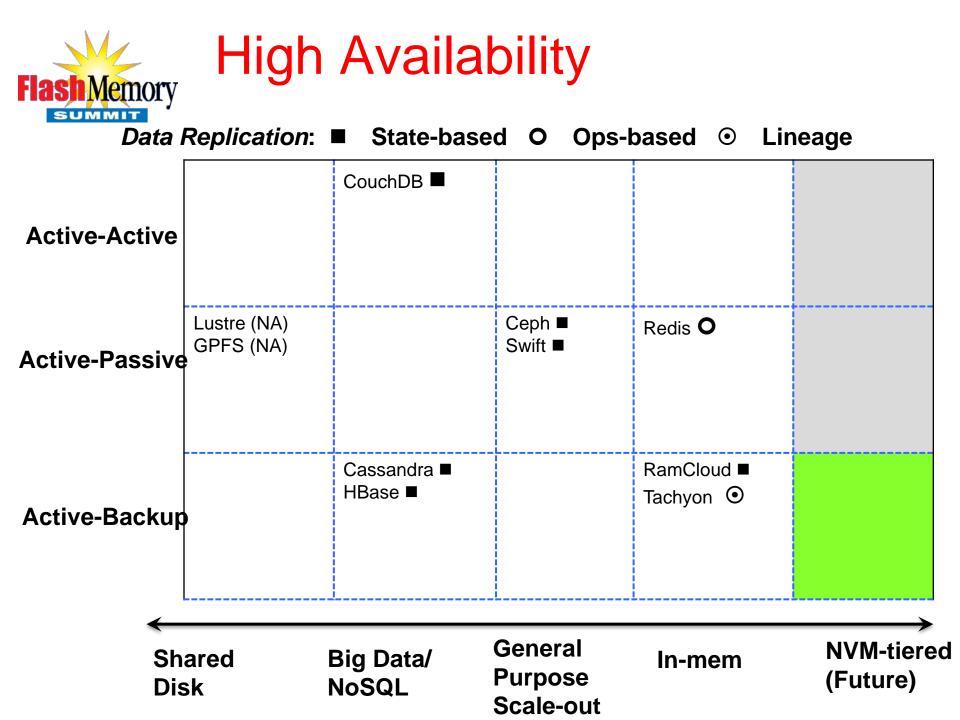


Blueprint of a Scale-out Storage Architecture



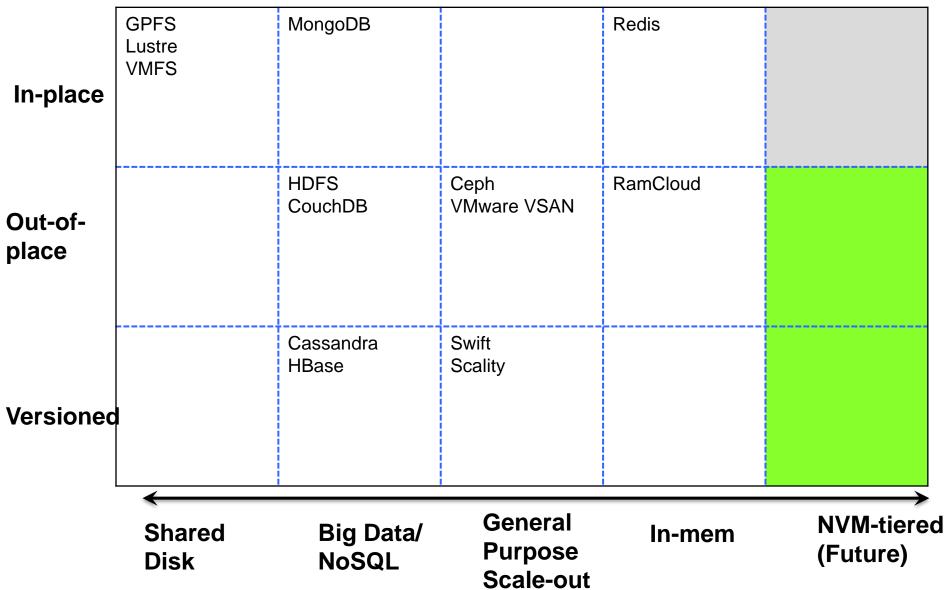
Flash Memory Concurrency Control







Persistence Model





- Why Enterprise Storage → Scale-out Architectures
 - Web 2.0 model to handle exponential data growth
- Why NVM != < Yet Another Storage Tier>
 - Holistic shifts across compute, network, memory, storage
- Why New Scale-out Design != Clean slate
 - Piecemeal evolution of micro-services within a Scale-out Architecture.







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