

Session 302-C:

Creating the Foundation of the Flash Storage Memory Industry: A Conversation with Eli Harari

Brian A. Berg, FMS Technical Chair In Conversation with Eli Harari





JFK Speech (1961) Moon Landing (1969)







- Princeton University (1969-73)
- Ph.D.: 1973





CHARGE TRAPPING EFFECTS IN THIN FILMS ${\rm OF} \ {\rm Al_2O_3} \ {\rm AND} \ {\rm SiO_2}$

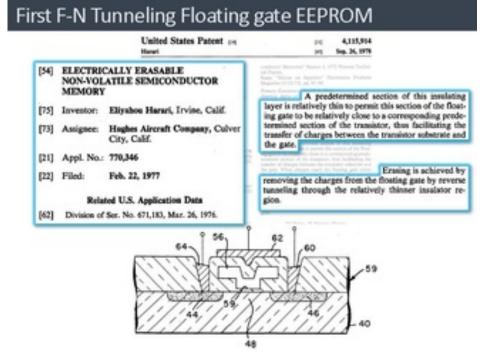
Eliyahou Harari



Hughes Microelectronics, Newport Beach, CA

(1973-79)



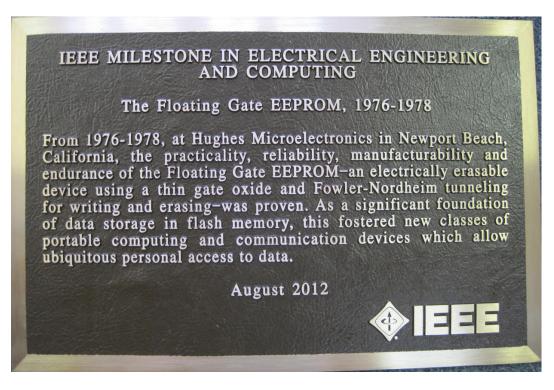




IEEE Milestone dedicated in 2012 for this 1976-1978

EEPROM work, and how it led to "System-Flash"

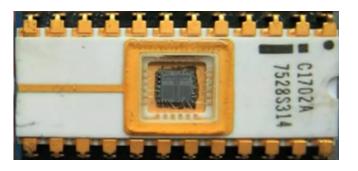
 Harari also received the 2012 FMS Lifetime Achievement Award





Dov Frohman invented EPROM at Intel (1971)





Dov interviewed Eli in 1979 for a job at Intel



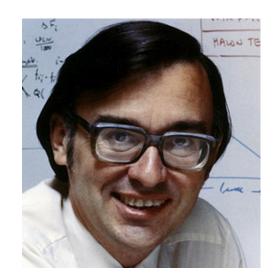
- Worked at Intel (1979-81)
- Eli proposed an SSD to

Intel President Andy Grove





"10x" Story from Ted Hoff





- Co-founded Wafer Scale Integration (1983-88)
- Business plan:
 - WaferDisc
 - then high-speed EPROM
 - then Programmable System Devices



Founded SunDisk (March 1, 1988)





SOLID-STATE MASS STORAGE SYSTEMS



• "System-Flash" was a joint effort of Eli,

Bob Norman and Sanjay Mehrotra

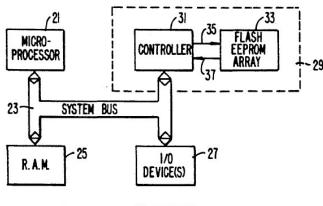


FIG._IA.

A system of Flash EEprom memory chips with controlling circuits serves as non-volatile memory such as that provided by magnetic disk drives. Improvements include selective multiple sector erase, in which any combinations of Flash sectors may be erased together. Selective sectors among the selected combination may also be de-selected during the erase operation. Another improvement is the ability to remap and replace defective cells with substitute cells. The remapping is performed automatically as soon as a defective cell is detected. When the number of defects in a Flash sector becomes large, the whole sector is remapped. Yet another improvement is the use of a write cache to reduce the number of writes to the Flash EEprom memory, thereby minimizing the stress to the device from undergoing too many write/erase cycling.

Flash Memory Summit 2015 Santa Clara, CA



- "System-Flash" included:
 - Error correction and dynamic defect mapping
 - Wear-leveling
 - Logical-to-physical mapping
 - Low stress write and erase voltages
 - Intelligent caching: speed, and write reduction
 - Garbage collection
 - Repair of disturbed cells
 - Magnetic disk drive interface



- "System-Flash" created an SSD
 - host-independent plug-compatible disk drive replacement
- Goal for SSD: 1 million read/write cycles
 - Also required a high-endurance flash transistor
 - Cost reductions through Moore's Law and MLC
 - Industry standard formats for removable storage



• IEEE talk in 1990

IEEE Electron Devices Society
Santa Clara Valley Chapter
Meeting Notice

FUTURE DIRECTIONS FOR SEMICONDUCTOR NON-VOLATILE MEMORY

Speaker: Eli

Eli Harari

SunDisk Corporation Santa Clara, CA 95054.

Place: Santa Clara University,

Daly Science Center Room No. 206

Time: Tuesday, January 16th 1990, 7:30 pm.

Semiconductor non-volatile memories have in the past been an imperfect solution looking for a problem. In the coming decade, the problem, or a major market opportunity, will present itself in the form of an emerging new class of compact, portable products, such as hand-held computers, electronic notebooks, solid-state cameras, portable copiers and Fax machines, and cellular telephones. At the same time, certain types of non-volatile memory technologies, such as Flash EEPROM are at the threshold of overcoming major technological hurdles and transforming themselves from frog to prince (or king) in the new market environment.



- Operational device in 1990/'91
- First SSD:20 MB ATA2.5" device







 First products sold to GRiD Systems for their GRiDPad pen computer (Eli is 2nd from left; Bob Norman is 3rd from left; Jeff Hawkins at far right)



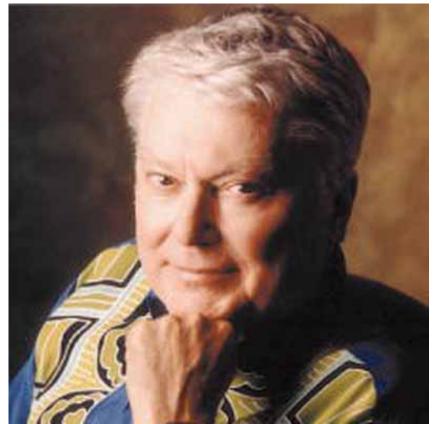


- Later in 1991:
 - PCMCIA SSD was available
 - SunDisk landed a contract to deliver 10,000 SSDs to IBM for their ThinkPad laptop





 Relationship with Western Digital, Seagate and Al Shugart





 Relationship with Kodak





- SanDisk story
 - Disruption
 - Moore's Law
 - 100,000X cost reductions over 25 years (1990-2015)















2009 IEEE Robert Noyce Award

2014 National Medal of Technology and

Innovation



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