

# Designing SSD Firmware Overlays to Reduce Performance Impact

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- Memory Constraints
- Overlay
- Overlay Manager
- SSD IO Performance
- Overlay Debugging
- Conclusion
- References





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## **Memory Constraints**

#### Program

• If an executable exceeds available program memory space the solution is overlay.

#### Memory

• Overlay space in program memory and number of overlays need to be calculated to minimize IO latency.



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## **Overlay Layout**

### **Overlay**

- Program section which is loaded at an address different from where it will run.
- Number and size of overlays depends of the code size and functions.

PROGRAM MEMORY	PROGRAM
OVERLAY SPACE	OVL 1
	 OVL 2





# **Overlay Definition**

#### Assumptions

- GCC tools are used to build the code.
- Set of **n** functions selected to create overlays.

### **Overlay definition**

 OVERLAY (f) \_\_attribute\_\_ ((section (".overlay.text"), data\_section (".overlay.data"))) f



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## Manager Overlay Table

#### Location

 External memory location where overlay code and data sections are loaded

#### Size

 Size of the overlay code and data sections.

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Code Location	Code Location
Code Size	Code Size
Data Location	Data Location
Data Size	Data Size
OVL 1	OVL n



## **Overlay Manager Function**

### Loading

 Copies overlay code and data sections from external to local memory space.

### Mapping

 Overlay manager keeps the overlay status, it is in external or local memory (unmapped or mapped)





## Handling Overlays

### **Overhead 1**

• Transactions with external memory OVH1.

#### **Overhead 2**

 Overhead incurred by the overlay manager activities in local transactions OVH2.





# **Coding Technique**

#### Least frequent

• Overlay code is the least frequently run code

#### Reentrant

Designed as a reentrant function to avoid global and static variables.

#### **Predefined**

• Text and data sections locations are predefined.



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# **Overlay Overhead and IO Latency**

#### Latency • L = OVH (OVL 1) + OVH (OVL2) + ... + OVH (OVL n)





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# **Debugger Overlay Table**

#### **Overlay table**

 Created in local memory to become part of debug inferior.

#### **Status**

 Overlay mapped or unmapped

Mapped Address	•••	Mapped Address
Overlay size		Overlay size
Load Address		Load Address
Status		Status
OVL 1		OVL n



# **Overlay Automatic Debugging**

#### **Debug overlay table**

• Debugger gets notification from overlay manager and takes overlay information from debug overlay table.

#### **Debug event**

• Notifies debugger to take proper action.



# **Overlay Manual Debugging**

#### **Overlay manual**

 Debugger does not rely on the overlay table. Overlays need to be mapped manually for the debugger to identify the mapped overlays.



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### **Decrease Latency**

### **Coding technique**

To reduce memory transactions from external to the local memory.

### Handling

• To simplify overlay code management by predefinition of the load memory location.



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#### References

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- 2. The GNU C Library Reference Manual
- 3. NVM Controller Firmware Architectural Specification, PMC



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# **Questions**?

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