Forum E-21: Controllers and Flash Technology Part 1 (Controllers Track)

Organizer/Chairperson:

Erich F. Haratsch

Seagate

Agenda

Title	Speaker	Duration (mins)
Introduction	Erich Haratsch	
Controller IC Evaluation of High Radix Parity Check Code	Oliver Hambrey	25
High-Throughput LDPC Solution for Reliable and High Performance SSD	Jiangli Zhu	25
Ultra High Throughput ECC for SSD	Yuluen Wang	25
Break		15
Compression: Making Flash (Even) Cheaper	Thomas Parnell	25
Advanced Controller Technology for 3D NAND Flash	Wei Lin	25

Thank You! Questions?

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