

# NRAM: High Performance, Highly Reliable Emerging Memory

Sheyang Ning<sup>1,2</sup>, Tomoko Ogura Iwasaki<sup>1</sup>,  
Darlene Viviani<sup>2</sup>, Henry Huang<sup>2</sup>,  
Monte Manning<sup>2</sup>, Thomas Rueckes<sup>2</sup>,  
Ken Takeuchi<sup>1</sup>

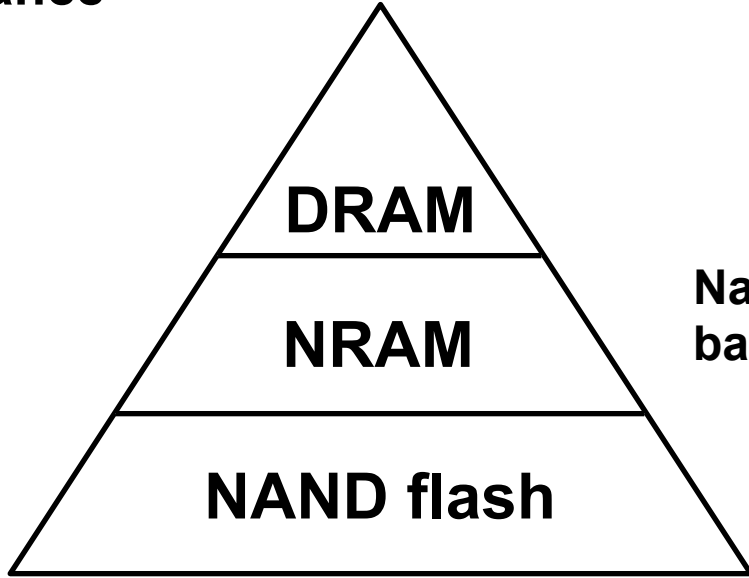
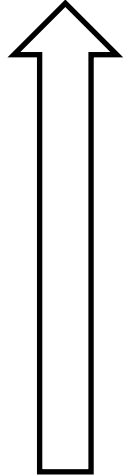
<sup>1</sup>Chuo University

<sup>2</sup>Nantero Inc.

- Introduction of NRAM
- Single NRAM cell and cell array measurement setup
- NRAM characteristics
  - DC-IV curve
  - Set and reset program characteristics
  - Large on/off ratio
  - High temperature program
  - High endurance
- Conclusion

# Introduction of NRAM

Performance



**Nano-RAM, Carbon nanotube based resistive memory**

# Compare with Conventional Memories

○ = good    ✕ = bad

	DRAM	NAND flash	NRAM	
Performance	○	✕	○	← 20 ns pulse [1]
Scalability	✕	○	○	← Single cell 15 nm [2]
Endurance	○	✕	○	← Single cell 10 <sup>12</sup> [3]
Non-volatile	✕	○	○	← 1000 years@ 85°C [2]

[1]. S. Ning et al., *IEEE Symp. on VLSI Technology*, Jun. 2014, pp. 96–97.

[2]. Nantero Presentation for ITRS ERD/ERM, *International Technology Roadmap for Semiconductors (ITRS)*, 2013.

[3]. S. Ning et al., *IEEE Trans. on Electron Devices (TED)*, vol. 62, no. 9, pp. 2837–2844, Sept. 2015.

## Compare with Emerging Memories

	ReRAM [1]	PRAM [2]	NRAM [3]
<b>Material</b>	$\text{Al}_x\text{O}_y$	$\text{Ge}_2\text{Sb}_2\text{Te}_5$	Carbon nanotube (CNT)
<b>Resistive switching on read</b>	Filament size	Phase change	Tunneling current between CNTs
<b>Endurance</b>	$10^8$	$10^9$	$10^{12}$
<b>Current</b>	High	High	Low

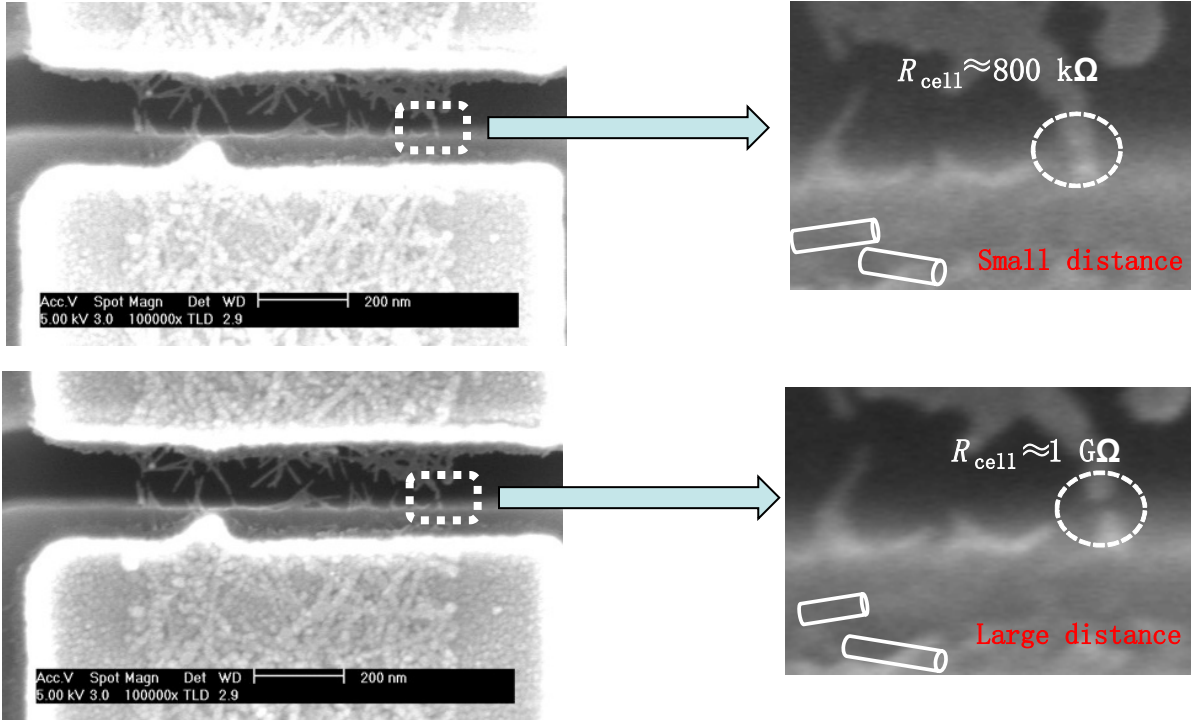
[1]. S. Ning et al., *Solid-State Electronics*, vol. 103, pp. 64–72, Jan., 2015.

[2]. H. Y. Cheng et al., *IEEE Int. Electron Devices Meeting*, 2013, pp. 30.6.1–30.6.4.

[3]. S. Ning et al., *Symp. on VLSI Tech.*, 2014, pp. 96–97.

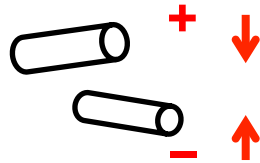
[4]. S. Ning et al., *Ext. Abstr. Solid State Devices and Materials (SSDM)*, Oct. 2015, pp. 1198-1199.

# Physical Mechanism



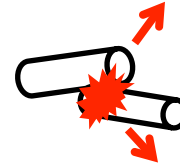
# Physical Mechanism

**Set: attraction force**



**Electrical  
induction**

**Reset: repulsive force**



**Heat caused  
phonon vibration**

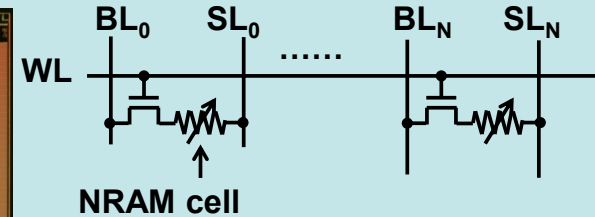
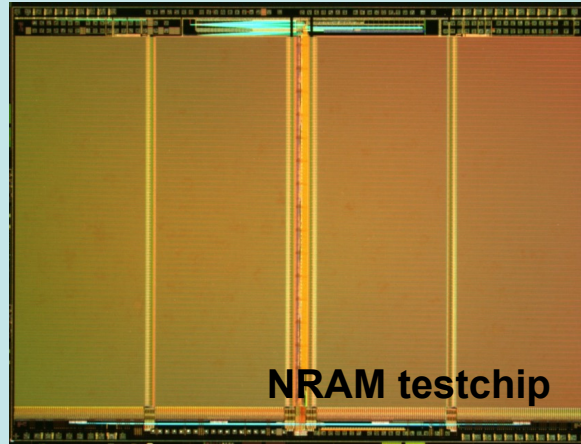
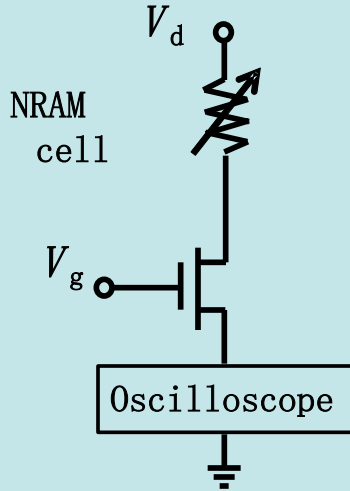
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# Single NRAM Cell and Cell Array Test

140 nm NRAM single cell

116 nm, 4 Mbits NRAM cell array

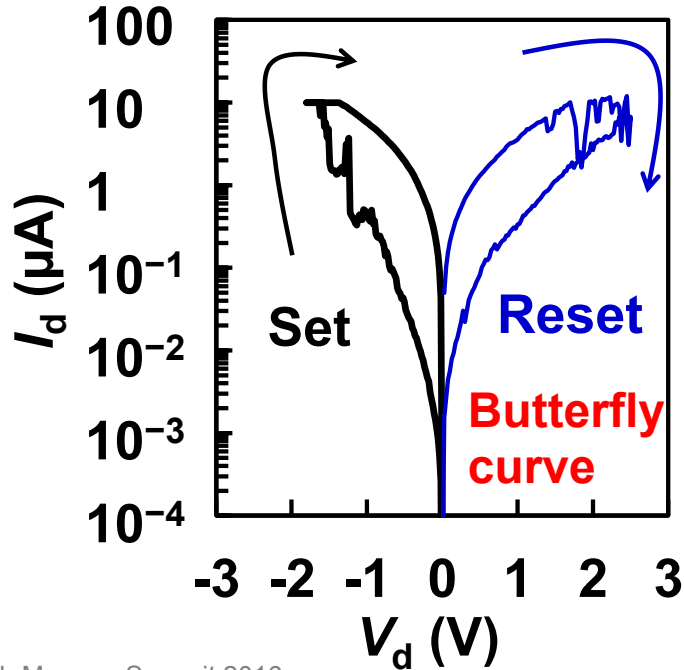


	BL	SL
Set voltage	$+V_{Set}$	0 V
Reset voltage	0 V	$+V_{Reset}$

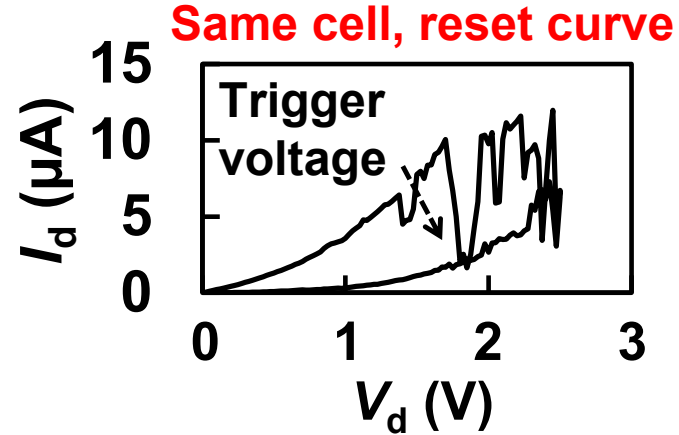
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# DC-IV Curve

## Single cell bi-polar program

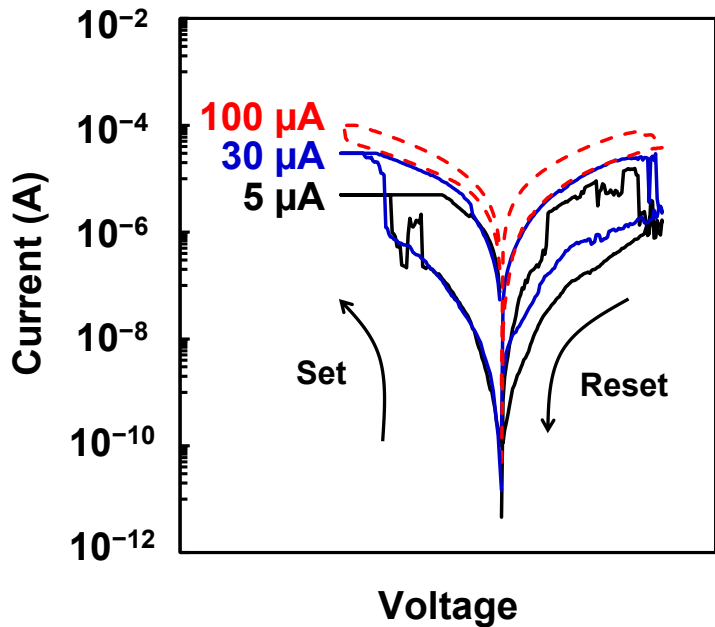


## Current vibration due to long term voltage stress on CNTs

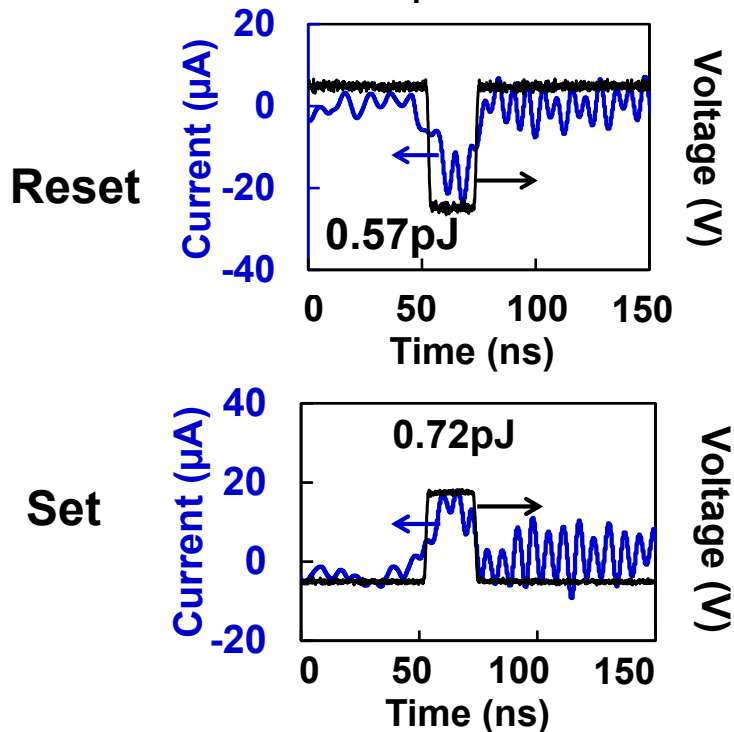


# Low Program Current

Single cell DC  $I_{\text{compliance}} = 5 \mu\text{A}$ ,  
30  $\mu\text{A}$ , and 100  $\mu\text{A}$

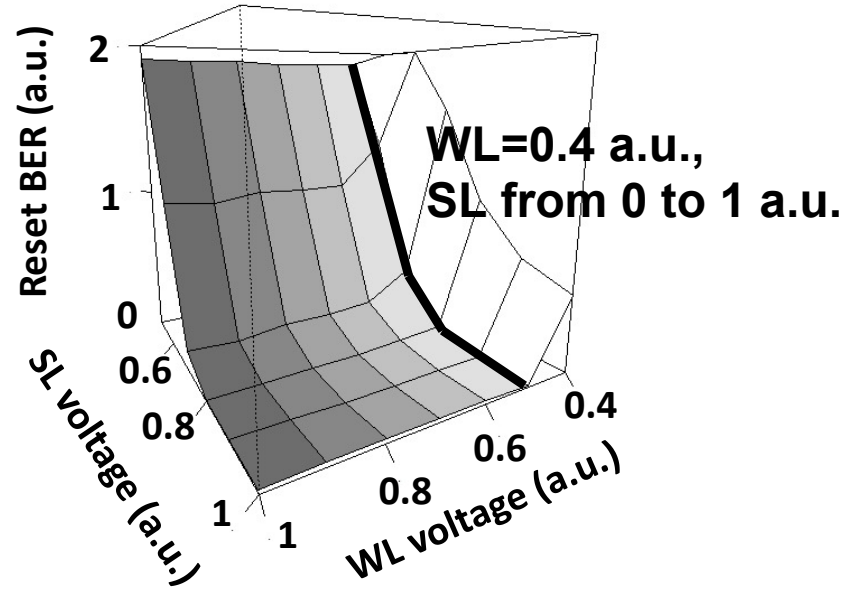
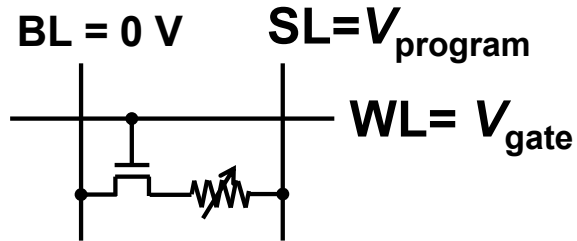


Single cell AC  $I_{\text{peak}} < 20 \mu\text{A}$



# Reset Characteristic

- Cell array measurement, Reset is driven by both voltage and current

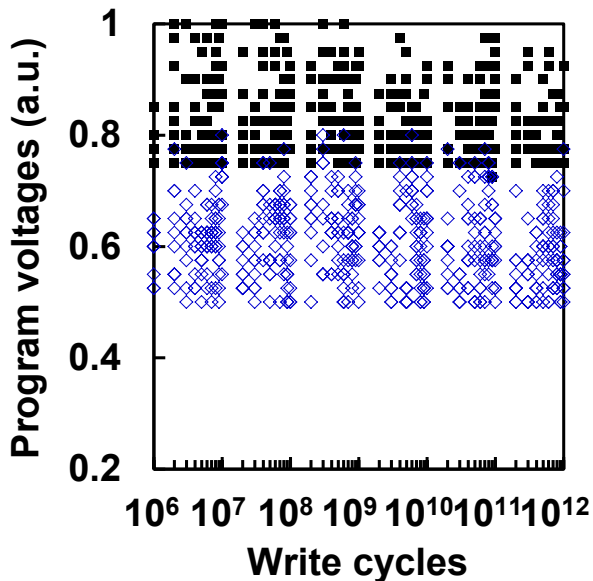


# Set and Reset Voltages

- Use incremental pulse programming on single cell

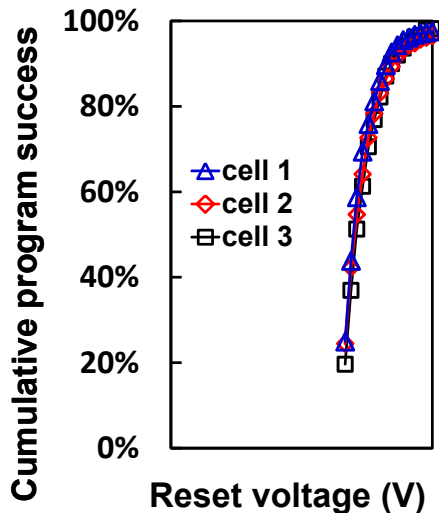
■ Reset voltage

◇ Set voltage (absolute value)

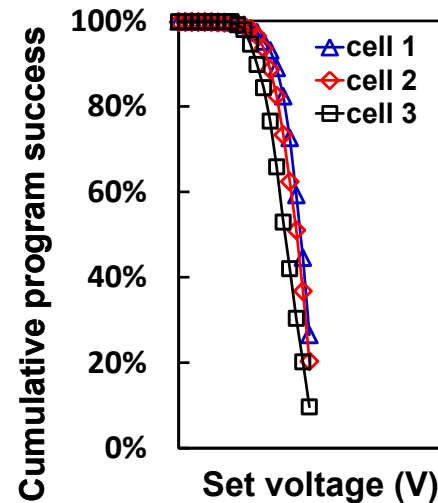


- Three randomly chosen NRAM cells

Reset



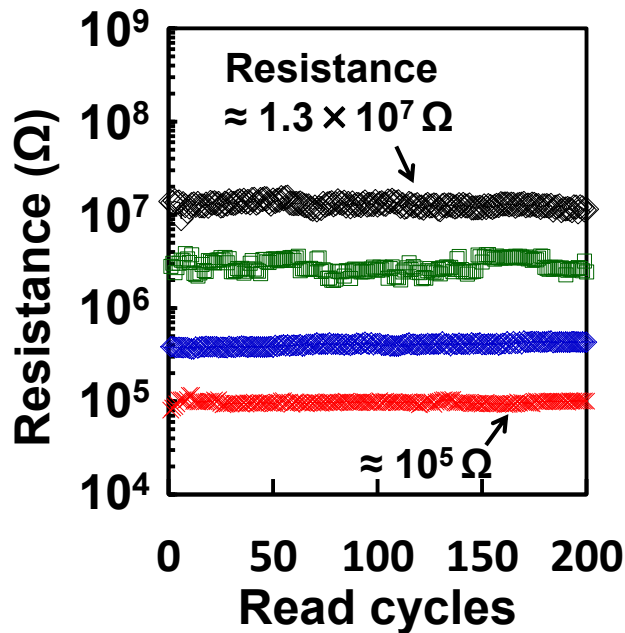
Set



# Large On/Off Ratio

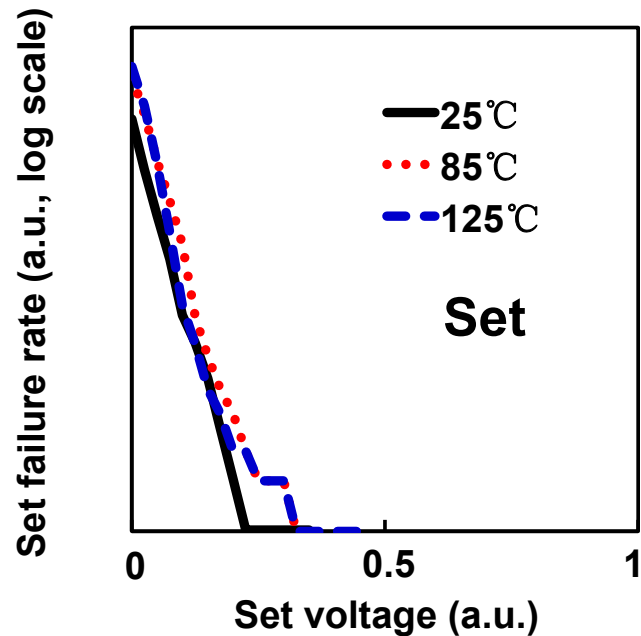
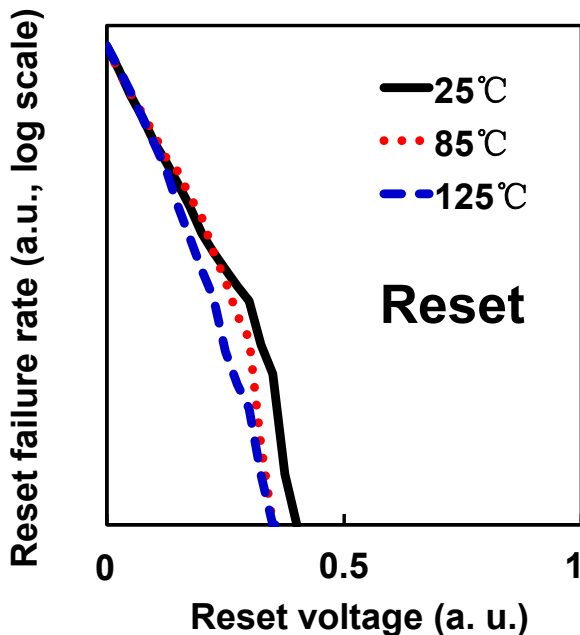
- Single cell measurement
  - > 100 times on/off ratio
  - Possible for multi-level cell (MLC)

Read at 1 V



# High Temperature Program

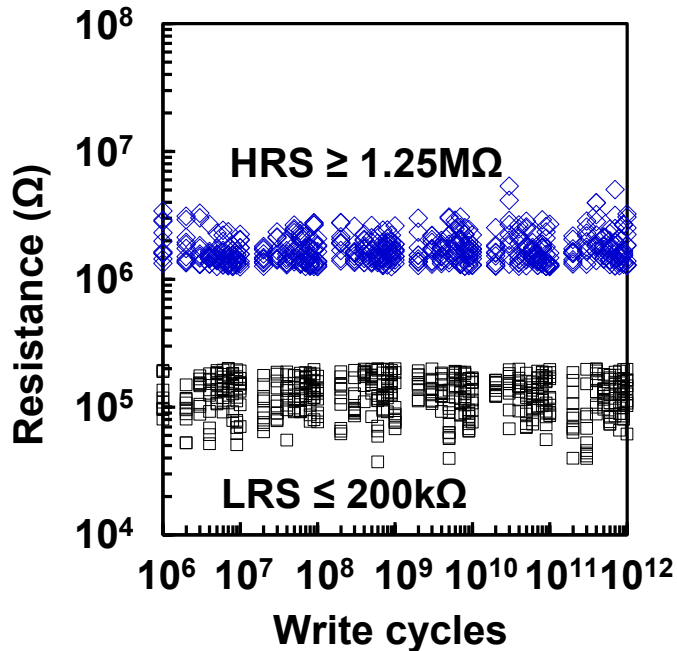
- Single cell measurement, stable program voltage at different temperatures



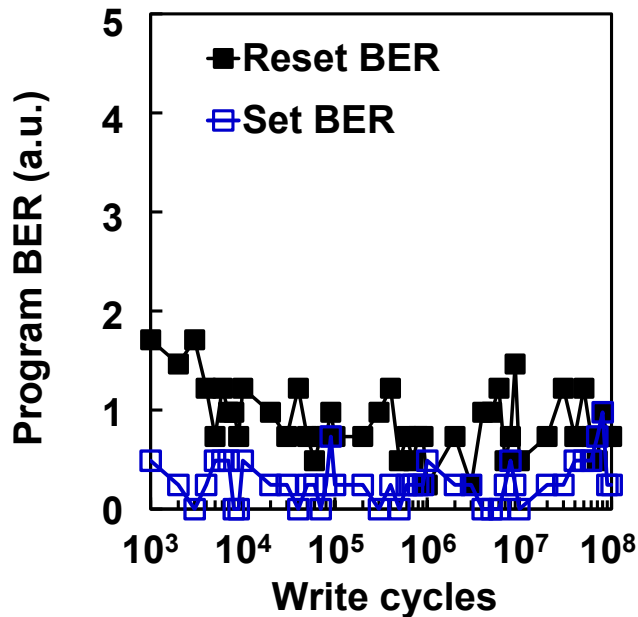


# High Endurance

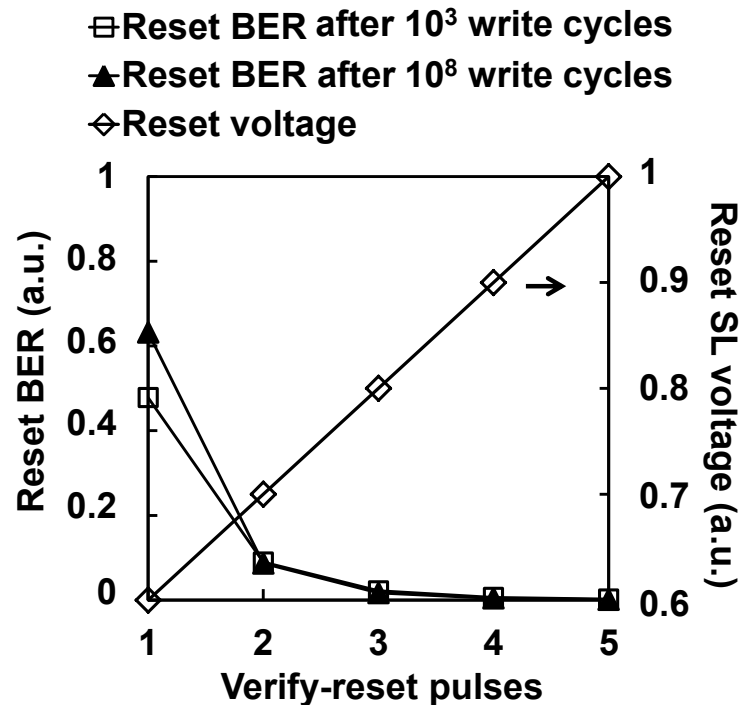
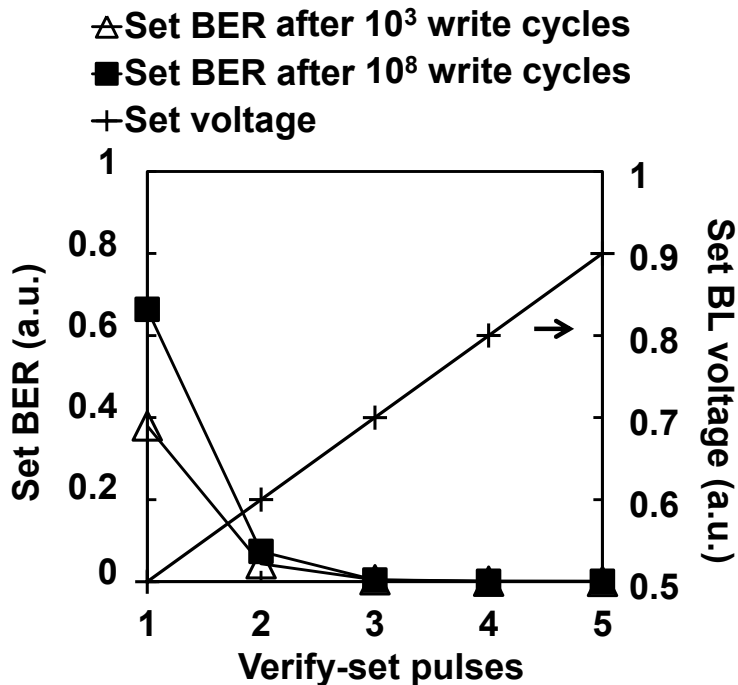
## Single cell



## Cell array



- Cell array does not wear-out after  $10^8$  write cycles



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## Conclusion

- NRAM is an emerging nonvolatile memory cell which has performance between DRAM and NAND flash.
- Compared with other emerging nonvolatile memories, NRAM has competitive characteristics, including, lower program current, large on/off ratio, large endurance, high temperature stability and long retention time.