

### NVMe Annual Update, NVMe 1.3 and NVMe Market Data Session A11 Part A 8:30 to 9:35

NVM Express Annual Update	Dave Landsman	Director Industry Standards, Western Digital
Major additions in the NVMe 1.3 standard and looking to the Future	Peter Onufryk	Fellow/NVM Solutions, Microsemi, and Member, NVMe Workgroup Board
NVMe in the Market with market data from IDC	Eric Burgener	Research Director, Storage, IDC





### NVM Express Workgroup Update

### Flash Memory Summit 2017

Dave Landsman Director Industry Standards – Western Digital NVMe BoD Member



### NVM Express, Inc. 120+ Companies defining NVMe together

#### Board of Directors

13 elected companies, stewards of the technology & driving processes *Chair: Amber Huffman* 

#### Technical Workgroup

NVMe Base and NVMe Over Fabrics *Chair: Amber Huffman* 



### Management I/F Workgroup

Out-of-band management over SMBus and PCIe® VDM *Chair: Peter Onufryk Vice Chair: Austin Bolen* 

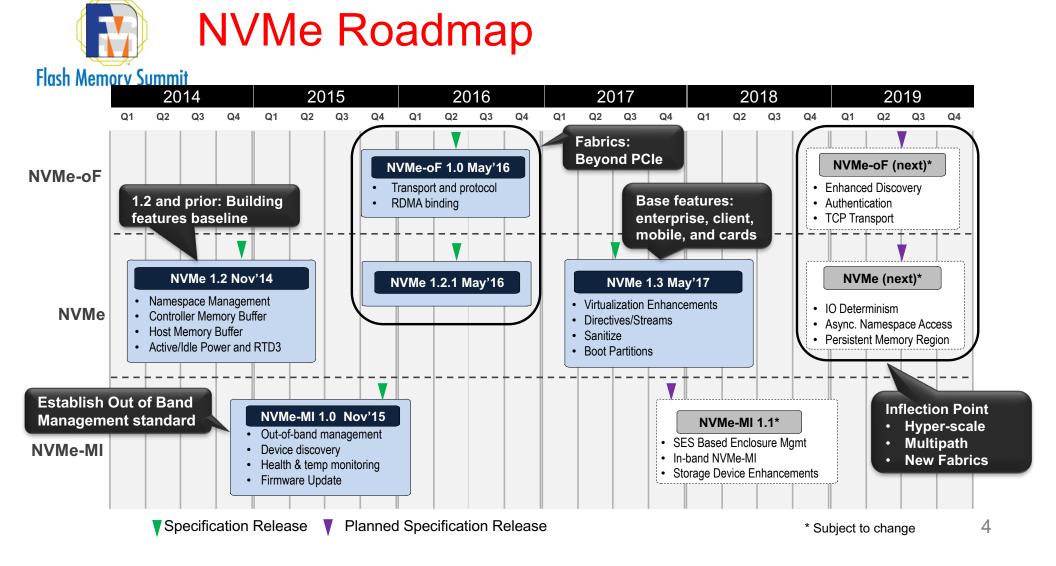
#### Marketing Workgroup

NVMexpress.org, webcasts, tradeshows, social media, and press *Co-Chairs: Janene Ellefson and Jonmichael Hands* 

### Interop (ICC) Workgroup

Interop & Conformance Testing in collaboration with UNH-IOL *Chair: Ryan Holmqvist* 







## Follow NVMe!

- www.nvmexpress.org
  - Product Information
  - Events
  - Spec Info
  - Webcasts
  - News & Blogs
  - How to be a member



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# **THANK YOU**

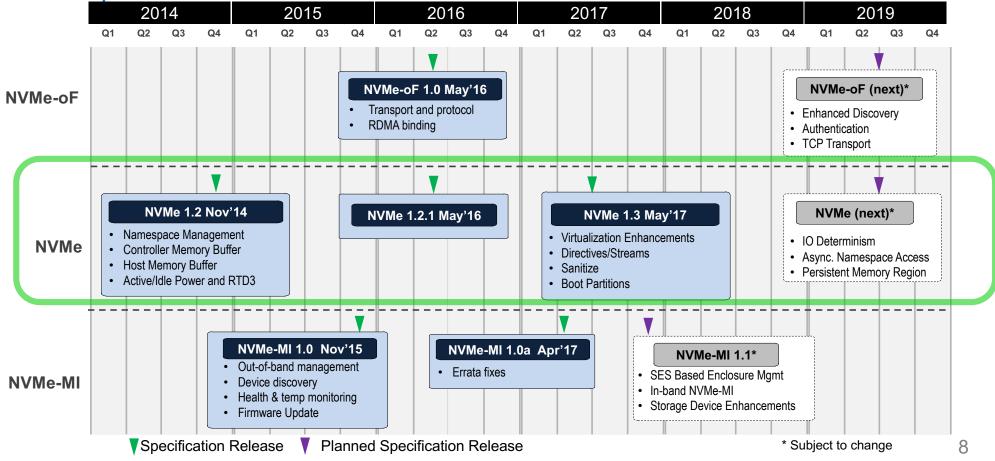


## Major New Features in NVMe 1.3 and Looking to the Future

Peter Onufryk Microsemi Corporation

### NVMe Roadmap

Flash Memory Summit





## NVMe 1.3 New Feature Summary

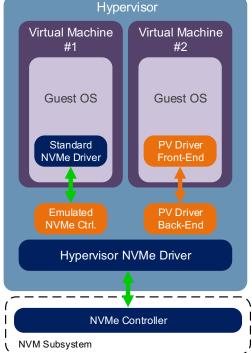
Flash Memory Summit

	Category	_Description	Benefit
C		Boot Partitions	Enables bootstrapping of an SSD in a low resource environment
	Client/Mobile	Host Controlled Thermal Management	Host control to better regulate system thermals and device throttling
	Data Center & Enterprise	Directives	Enables exchange of meta data between device and host. First use is Streams to increase SSD endurance and performance
		Virtualization	Provides more flexibility with shared storage use cases and resource assignment, enabling developers to flexibly assign SSD resources to specific virtual machines
9		Emulated Controller Optimization	Better performance for software defined NVMe controllers
6	Debug	Timestamp	Start a timer and record time from host to controller via set and get features
		Error Log Updates	Error logging and debug, root cause problems faster
		Telemetry	Standard command to drop telemetry data, logs
	Management	Device Self-Test	Internal check of SSD health, ensure devices are operating as expected
		Sanitize	Simple, fast, native way to completely erase data in an SSD, allowing more options for secure SSD reuse or decommissioning
		Management Enhancements	Allows same management commands in or out-of-band
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### Virtualization Enhancements Motivation

- Cloud hosters win with high density, oversubscription, and differentiation
  - Multi-tenancy is the norm
  - Premium differentiator offering high speed storage
- Virtual machines are inherently mobile and hosts are inherently dynamic

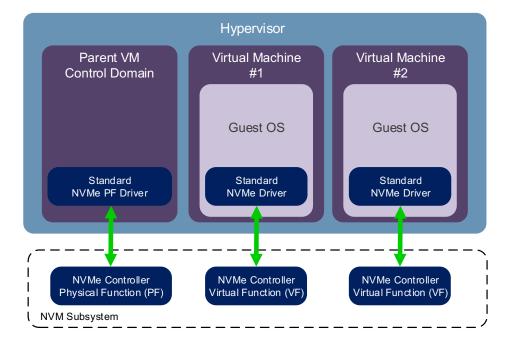


Software sharing limits NVMe throughput and increases latency



## Virtualization Enhancements Direct Assignment

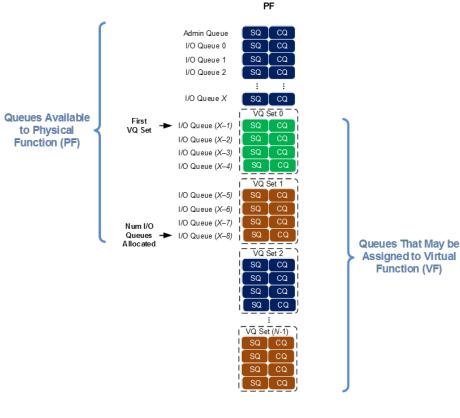
- Direct assignment enables each VM to directly access NVMe Controller
  - Eliminates software overhead
  - Allows guest OSes to use standard NVMe driver
- NVMe already supports PCI-SIG SR-IOV
- Virtualization enhancements standardize PF functionality and allow flexible dynamic mapping of resources
- Abstraction allows future mechanisms beyond SR-IOV





### Virtualization Enhancements Allocating Resources

- Resources may be dynamically moved between the PF and VF(s)
- VQ Set A set of (four) Submission Queue (SQ) and Completion Queue (CQ) pairs that may be assigned to a VF
- VI Set A set of (four) MSI-X interrupt resources that may be assigned to a VF



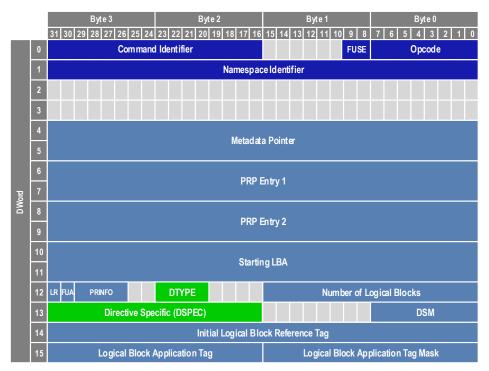


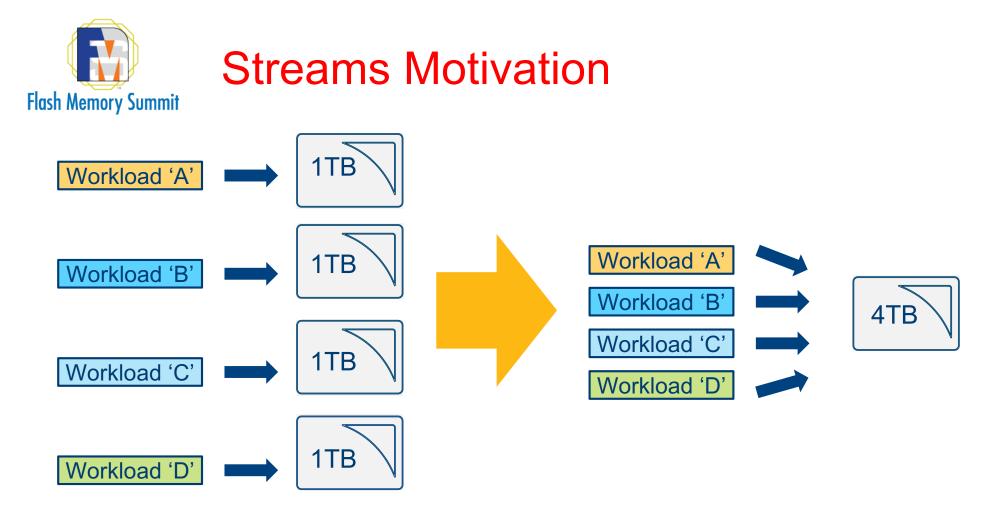
### **Directives**

- NVMe commands are always 64-bytes in size
- Remaining unused space in commands needs to be allocated with care
- Reusable Directive Specific (DSPEC) field whose contents/format is defined by the Directive Type (DTYPE) field
- DSPEC is used for Streams today and future ideas tomorrow

Flash Memory Summit 2017 Santa Clara, CA

#### Write Command

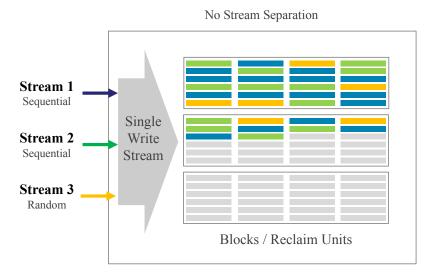






## The Benefit of Streams

#### **Standard SSD**

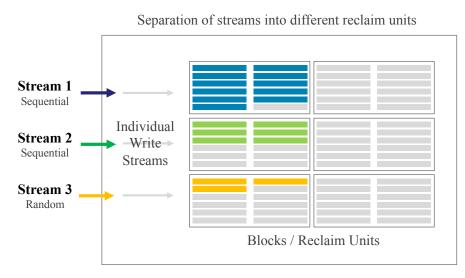


Mixed data needs garbage collection to reclaim blocks

#### **Higher Write Amplification**

Flash Memory Summit 2017 Santa Clara, CA

#### **SSD** With Streams



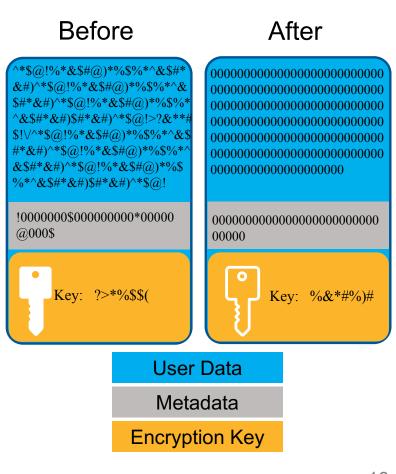
Separated data can be trimmed or self-invalidated to reclaim blocks

#### **Lower Write Amplification**



Sanitize

- Sanitize is used when retiring SSD from use, reusing • for new use case, or end of life
- Alters user data so that is unrecoverable by erasing • media, metadata, and cache
  - Block Erase low level block erase on media (physically erase NAND blocks)
  - Crypto Erase change media encryption key
  - **Overwrite** overwrite with data patterns (not good or recommended for NAND based SSDs due to endurance)
- Benefits of Sanitize vs existing Format command •
  - Keeps running after reset or pow •
  - Erases all metadata, sensitive log pages and status during operation
  - Clears all user data in caches •

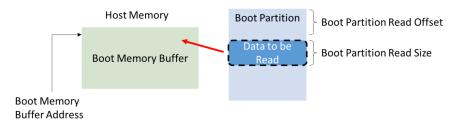




## **Boot Partitions**

- Optional storage area that can be read with "fast" initialization method (not standard NVMe queues)
  - Example: UEFI bootloader
- Saves cost and space by removing the need for another storage medium (e.g., SPI Flash or EPROM)
- Boot partitions may be updated using standard NVMe Firmware Download and Firmware Commit
- Boot partitions may be write protected

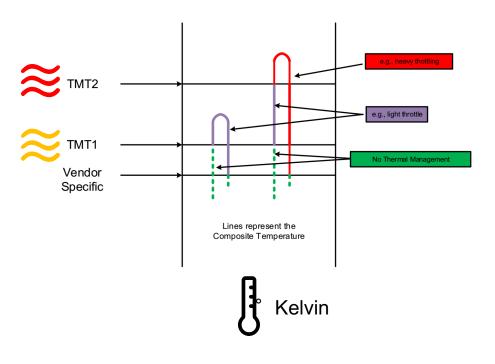
Makes NVMe more accessible for mobile and client form factors





## Host Controlled Thermal Management

- Better thermal management in client systems like laptops and desktops
- Host can set Thermal Management Temperature at which an SSD should start going into a lower power state or throttling
- TMT1 Threshold where SSD should start attempting to reduce temperature while minimizing impact on performance
- TMT2 Threshold where the SSD should start reducing temperature regardless of impact to performance





- Timestamp
  - Enables host to set a timestamp in controller via set features NVMe command, and read with get features



#### Error Log Updates

• Get Log NVMe command now returns more info on where the error occurred (queue, command, LBA, namespace, etc.) and error count



#### Telemetry

vendor unique logs that can be dumped with industry standard commands and tools



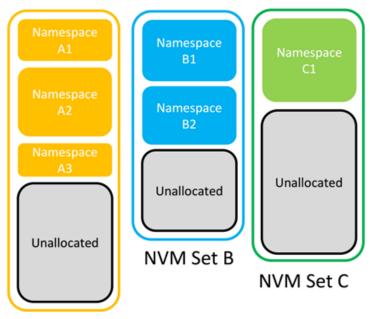
# Looking to the Future





## I/O Determinism - NVM Sets

- NVM Sets are QoS Isolated
  - Write to namespace A1 does not impact QoS associated with namespace B2
- NVM Sets have attributes
  - Endurance
- NVM Subsystem may support one or more NVM Sets
- One or more Namespaces may be allocated to an NVM Set



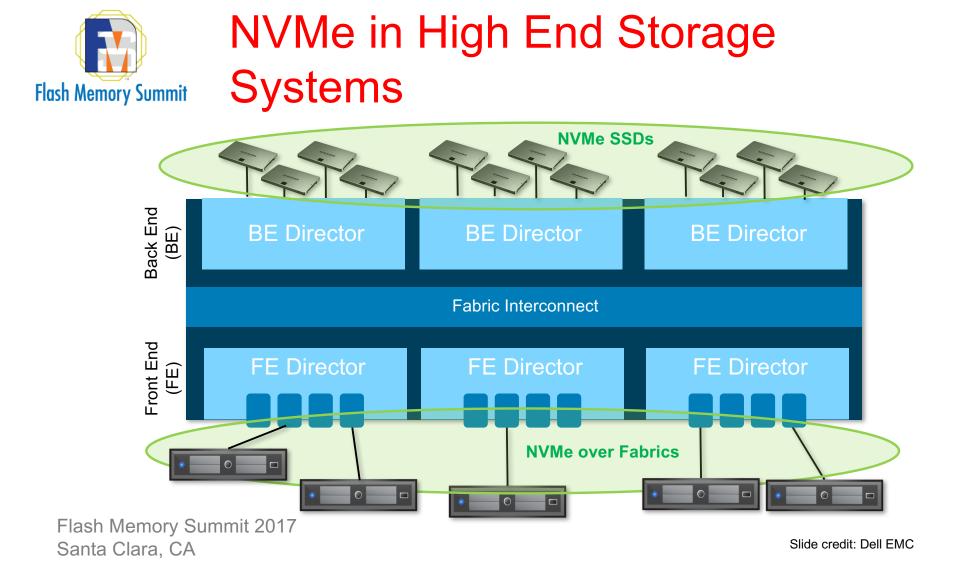
NVM Set A



### I/O Determinism - Predictable Latency Mode

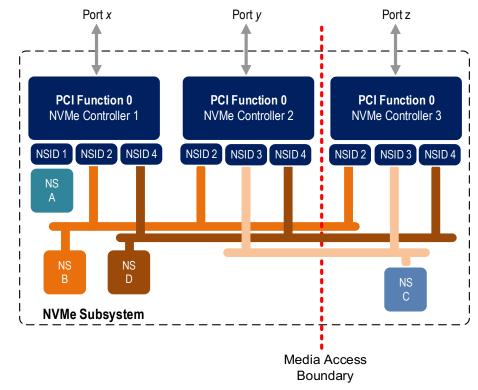
Deterministic Window	Non-Deterministic	
	Window	







# Asynchronous Namespace Access





## Persistent Memory Region (PMR)

Flash Memory Summit

- Controller Memory Buffer (CMB) ۲
  - Introduced in NVMe 1.2 ٠
  - PCI memory space exposed to host ٠
  - May be used to store commands & command • data
  - Contents **do not** persist across power cycles • and resets
- Persistent Memory Region (PMR) ullet
  - PCI memory space exposed to host ٠
  - May be used to store command data •
  - Content persist across power cycles and ٠ resets





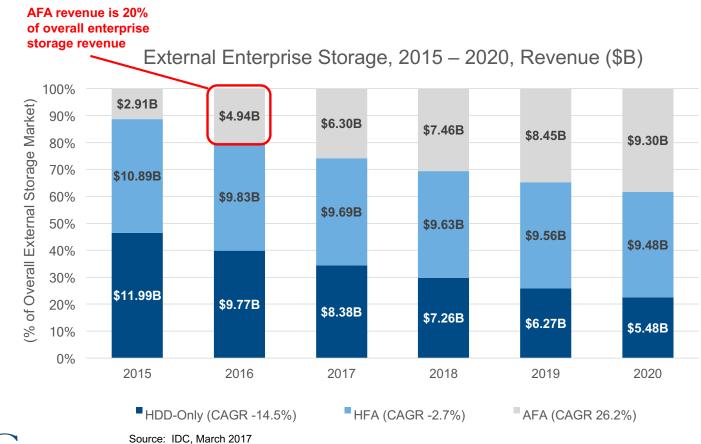
- NVMe continues to evolve with new innovative data center, enterprise, and client features
- NVMe 1.3 was ratified on May 1<sup>st</sup> and is available at <u>www.nvmexpress.org</u>
- Work is in progress on new features





### **NVMe in Enterprise Storage Systems**

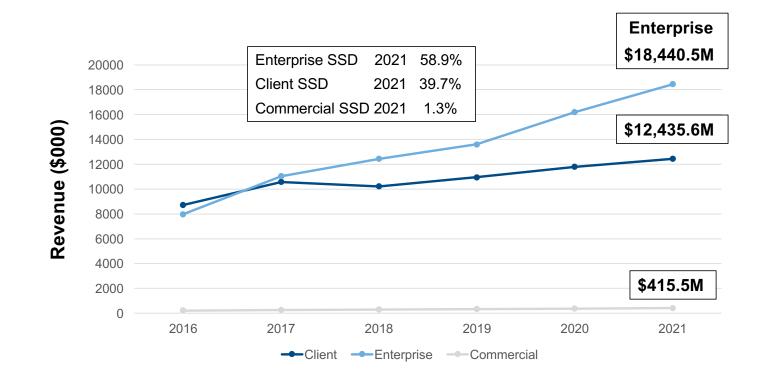
Eric Burgener Research Director, Storage IDC



### **Flash Driving Enterprise Storage**



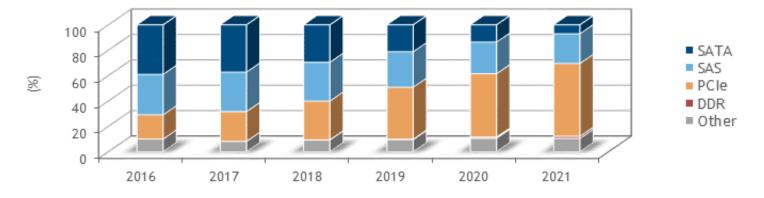
#### WW SSD Market Forecast, 2017-2021





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### **WW SSD Revenue Share by Interface**



- PCIe will grow from a 12.4% revenue share in 2016 to a 61.4% share by 2020
- In 2020 NVMe SSD revenue alone will be \$9.94B
- In 2020 rack scale flash systems revenues are still expected to be well under \$1B
- In 2020 total SSD revenues will be \$16.19B (all interface types)



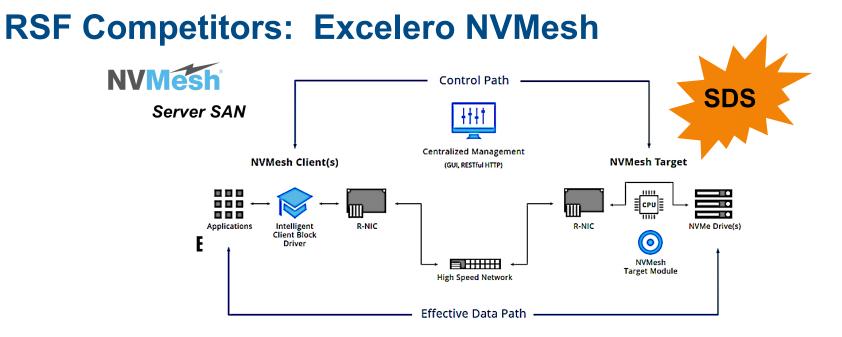
### **Rack Scale Flash Systems Emerging**



...and don't forget Pure

- Webscale infrastruc Storage FlashArray//X no SCSI) •
- Internal NVMe storage, ...... ٠
- Primary positioning is as an easily scalable "SSD" that offers the • efficiencies of shared storage
- All require custom drivers on the host and some include hardware • customizations (for "enterprise" features like RAID, snapshots, etc.)
- Primary workload targets include real-time big data analytics and • super high performance databases
- First shipments in 2016 and industry revenues under \$50M in 2017 •





- Enables NVMe deployment at scale
- RDDA protocol for remote access
- Data services run on clients
- Logical volumes, data protection, multi pathing, tiering

- Converged/disaggregated
- SAN efficiencies/local latencies
- Runs on webscale infrastructure
- RoCE v2, Infiniband-based mesh
- Open19 Foundation member



### **RSF Competitors: Micron SolidScale**



- 24 NVMe SSDs in 2U •
- Packaged with Excelero SDS •
- RAID, dedupe, snapshots, replication •
- Mellanox RoCE ٠

- Announced in May 2017 ٠
- Targeted for use with real-time big • data analytics, databases and VDI
- RESTful API supports easy • automation



#### The Importance of NVMe in Enterprise Storage



- NVMe vs SCSI advantages
- Lighter weight I/O stack
  optimized for memory
- Lower latencies and much higher throughput
- Supports much higher degrees of parallelism



- New workloads and data access patterns require much higher storage performance
- Real-time big data analytics need an ability to support high degrees of concurrency
- Big data exacerbates the data mobility problem

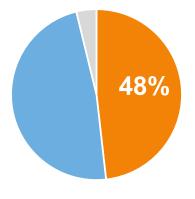
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- Improved efficiencies for "at scale" computing
- Higher infrastructure densities
- NVMe interface bandwidth needed as drive sizes increase



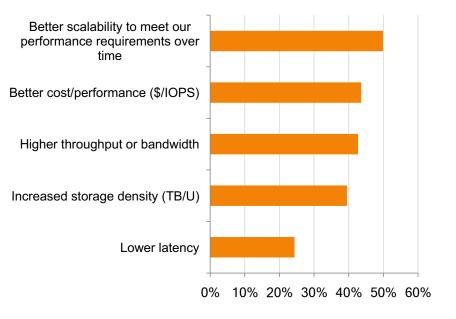
### **NVMe Adoption and Drivers**

#### PCIe or NVMe Flash



- Currently using
- Planning to use within 12 months
- Not using and no plans

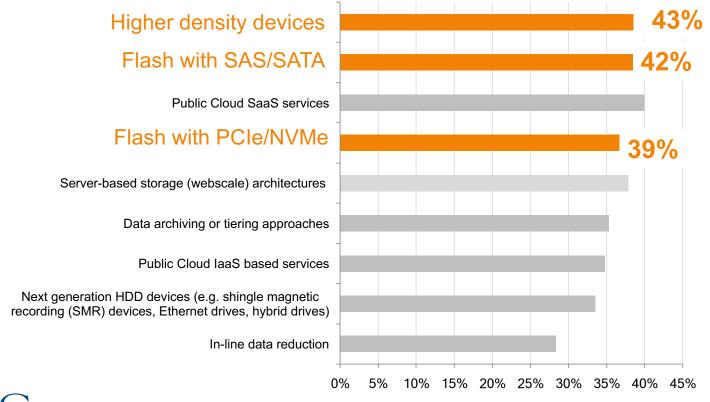
#### **Drivers to NVMe Adoption**





N=804-Source: Micron Survey: Managing High Data Growth Survey, IDC, April, 2017

### **Flash Strategies To Manage Data Growth**

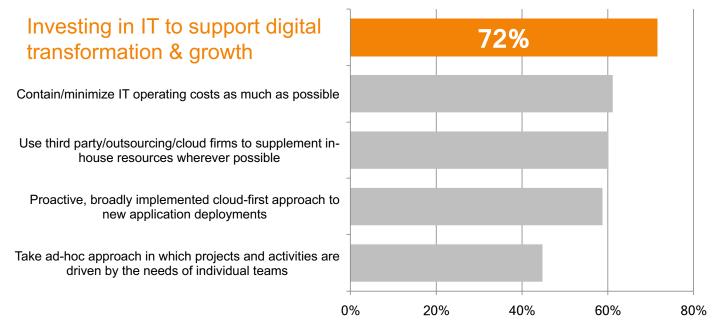




N=804-Source: Micron Survey: Managing High Data Growth Survey, IDC, April, 2017

### **DX\*** Is Driving Infrastructure Strategies

#### Rational for IT Infrastructure Decisions



\* Digital Transformation



Source: IDC Cloud Study 2017, N = 1007

### **Real-Time Big Data Analytics**





### **Essential Guidance**

- NVMe will become the mainstream foundation technology for enterprise storage by 2020
- An increasing number of select workloads will require NVMe performance (starting now)
- There are other reasons to consider NVMe now besides just low latency
  - High throughput, storage density/rebuild times
- Established vendors are taking an incremental approach to NVMe integration...
- ...but the rack scale flash architectures of the future are based on webscale designs



### **Thank You**



Eric Burgener Research Director Storage eburgener@idc.com



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