

NVMe performance optimization and stress testing

Isaac Livny Teledyne Corporation



- NVMe / NVMoF transfer overview
- PCIe perforance analysis
 - NVMoF over CNA example
- NVMe performance analysis
 - LBA distribution analysis
 - Conditional performance analysis using scripting
- Stress testing using traffic generation

Santa Clara, CA• Script examples





Each PRP data line in NVMe transaction view corresponds to pointer in a PRP list

												() () () () () () () () () ()	6
NVMe Cmd D	OPC	SQID CQID C	Data	MPTR 000000000000000000000000000000000000	PRP1	PR		SLBA	NLB PRI	NFO FUA LR	DSM No froguene	ACCF	AC N
50	Read		0033 32768 QWO				.87ATE000 0000	0000.000E3980				mormation pro-	Vided IN
414 H	001:00:0 0x0	0001 SQYTDBL 0x0	SAMSUNG M	MN ZVPV256HDGL-00000	Metrics	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	0 ns 0028 . 187 2	tamp 249 604 s					
- • NVMe H 416 H	Device ID Q 001:00:0 0x0	CID 0001 0x0033 0000	Address 0000:87A20CC0	DSQ OPC FUS Read Normal op	SE PSDT CI Deration PRP 0x00	D NSID N 033 0x00000001	/IPTR Addre		Addres 00000000:82	es 2403818 PRF	P2 Address 0000000:87A1E0	00 0000000:0	A 000E398
* NVMe 417 H	Device ID Q 001:00:0 0x0	CID OUD 0001 0x0033 0000	Address	PRP List Data Len 0x00000040	Address List	MN SAMSUNG MZVP/2	256HDGL-00000	Metrics # Link &	Split Trans 5	Time Delta 678.500 ns	Time Stamp 0028 . 187 250 868 s		
Split Tra 1451	R← 8.0 x4 Mem	MRd(32) 000:00000	RequesterID 001:00:0	CompleterID Tag 000:00:0 3	VC ID Add 0 0 87A1	E000 SC	0: 82404000 8: 02408000		405000 000 409000 000	Data 00000 8240 00000 0240	06000 00000000 8 A000 0000000 8	2407000 0000 240B000 0000	00000
Split Tra 1452	R← 8.0 x4 Mem	MRd(32) 000:00000	RequesterID 001:00:0	CompleterID Tag 000:00:0 4	IC VC ID Add 0 0 87 1	E040 Statur	Data 16 dwords	Metrics # LinkTra	ns 7.me De 688.500	lta Time ns 0028 . 18	e <u>Stamp</u> 37 251 546 s		
Split Tra 1453	R← 8.0 x4 Mem	MRd(32) 000:00000	RequesterID 001:00:0	CompleterID Tag 000:00:0 5	TC VC ID Add 0 0 87 A1	E050 Status	Oata 16 dwords	Metrics ²⁴ LinkTra 2	ns Time De 686.500	lta Time ns 0028 . 18	e <u>Stamp</u> 87 252 234 s		
Split Tra 1454	R← 8.0 x4 Mem	MRd(32) 000:00000	RequesterID 001:00:0	CompleterID Tag 000:00:0 6	TC VC ID Add 0 0 87.11	EDC0 Status	Data 12 dwords	Metrics # LinkTra	ns Time De 110.309	lta Time us 0028 . 18	e <u>Stamp</u> 37 252 920 s		
Split Tra 1455	R← 8.0 x4 Mem	MRd(32) 000:00000	RequesterID 001:00:0	CompleterID Tag 000:00:0 7	IC VC ID Add 0 87A1	res Status ECF0 SC	Data 4 dwords	etrics <mark># LinkTran</mark> 2	s Time Delt -27.309 u	a Time s 0028 . 187	<mark>Stamp</mark> 7 363 230 s		
* NVMe 418 D	Device ID Q 001:00:0 0x0	CID CID 0001 0x0033 0000	Address 00000:82403818	PP Det Cen 0x000001F	Data 506 lword S	MN SAMS UNG MZVPV25	56HDGL-00000	Metrics # Link & S	plit Trans 7 9 7	ime Delta 82.000 ns 00	Time Stamp 028 . 187 335 920 s		
* NVMe 419 D	Device ID Q 001:00:0 0x0	CID 0001 0x0033 0000	Address	DD Cata Len 0x00000400) 1024 dwords	MN SAN SUNG MZVPV2	256HD GL-00000	Metrics # Link &	Split Trans 40	Time Delta 1.649 us	Time Stamp 0028 . 187 336 702 s]	
* NVMe 420 D	Device ID Q 001:00:0 0x0	CID CID 0001 0x0033 0000	Address	RP Data Data cen 0x00000400	Data 1924 dwords	MN S MSUNG MZVE V2	256HDGL-00000	Metrics # Link &	Split Trans 40	Time Delta 1.647 us	Time Stamp 0028 . 187 338 352 s]	
* NVMe 421 D	Device ID Q 001:00:0 0x0	CID 0001 0x0033 0000	Address	DP Dute Detsizen 0x00000400	Data 1024 dwords	MN SAMSUNG MZVPV2	256HDGL-00000	Metrics # Link &	Split Trans 40	Time Delta 1.653 us	Time Stamp 0028 . 187 340 000 s]	
* NVMe 422 D	Device ID Q 001:00:0 0x0	CID CID 0001 0x0033 0000	Address	RP Data Lan) 1024 dwords	MN SAMSUNG MZVPV2	256HDGL-00000	Metrics # Link &	Split Trans 40	Time Delta 1.659 us	Time Stamp 0028 . 187 341 652 s]	
* NVMe 423 D	Device ID Q 001:00:0 0x0	CID OUD 0001 0x0033 0000	Address	Data Len 0x0000040	Data 1024 dwords	MN SAMSUNG MZVPV2	256HDGL-00000	Metrics # Link &	Split Trans 40	Time Delta 1.649 us (Time Stamp 0028 . 187 343 312 s	ļ	
• NVMe	Device ID Q		Address	uata Len	Data	MN		# Link &	Split Trans	Time Delta	Time Stamp		







SGL decoding challenges

- Only the pointed to lines should show with the complete range
- Missing ranges should show as errors with tooltip pointing to missing ranges
- Duplicates should optionally show as errors with pointers in tooltips
- Account for bit bucket descriptors





- PCIe is a split protocol
 - Allows new requests to cross old completions
 - Performance analysis to account for overlap
- PCIe FC credit
 - Accumulative credit accounting
 - Manages Bottlenecks



PCIe Performance Measurement Techniques

- performance criteria
 - Instantaneous performance metrics
 - Overall statistical analysis
 - Traffic summaries
 - Bus utilization charts
 - Conditional performance analysis using automated analysis scripting techniques



Performance metrics

- Response time complete transfer time
 - First to last packet of split transaction
- Latency time Time to data
 - End of request to start of completion
- Throughput payload over response time
 - Total payload coincident with split transaction divided by response time

Split Tra	MRd(64)	RequesterID	CompleterID	Tag	TC VC ID	Addres	SS	Status	Data
17 X8	001:00000	001:00:0	000:00:0	69		0000008:C0	000000	SC	512 dwords
		# LinkTrans	Resp. time	atency.	Pld. Bytes	Thrpt MB/s	Time Delta	Tim	e Stamp
Santa Clara, CA August 2017		5	3.484 us 1	.260 us	2048	4226.342	32.000 ns	0000.0	50 010 250 s

Flash Me	mory Sumr	nit O	vera	PCIe timing	ati s Que		S,	traffi al Input / Outpu	C	sumr	nai	ries	
				Bus Utiliz	ation		U	Ipstream		Do	wnstream		
				Li	nk Utiliz	ation		0.156 %			0.246	%	
				Tin	ne Cove	erage		0.156 %			0.246	%	
					Band	width		2.984 MB/s			4.694 M	B/s	
				Data	Throug	ghput		0.000 MB/s			3.754 M	B/s	
				Pac	kets/se	econd		387337.474			111656.3	63	
				Split Tra	nsactior	n Performan	ice						
						F irm a	Min	nimum		Average	Max	imum	
					ponse		2.	180 us	1	.04.356 us	184	.000 us	
					Throug	hput	0.0	132 US	17	91.318 US	4741.	032 us	
_							0.0.			,.			
	Resp. time	e (Min)	Resp. time (Avg)	Resp.	time (Ma	x)	Latency (Mi	n)	Latency (A	vg)	Latency (Max)	
	91.188 us		104.078 us		184.0	00 us		90.684 us		103.534 us		183.032 us	
	2.180 us		104.356 us		109.7	08 us		1.132 us		91.315 us		96.740 us	
		Requester	-> Complete	r. Reads	Δ	Total	Thrpt	MB/s (Min)		Thrpt MB/s (A	(va)	Thrpt MB/s (Max)
		000:00:0 -	> 001:00:0	Cfa TCO		18	0.026			199.078	21	1791.577	
		001:00:0 -	> 000.00.0	Mem TC	0	81601	0.437			1793 711		4741.034	
Santa	Clara, CA	001.00.0		ment re	•	01610	0.107						
Augus	st 2017					01019							11



Use case: NVMoF over CNA

- PCIe performance analysis
 - Throuput, latency leading to credit analysis
- NVMof using CNA
 - Converged network adaptor
- CNA above switch used in NT mode
 - Non transparent bridging

Company Confidentia



- Setup A Direct connection
 - No switch between root complex and endpoint.
- This setup shows 9.3 Gigb/sec
 - No performance degradation
 - Baseline to establish the troubling component







Determine the root cause for performance degradation

- Identify performance degradation source
 - Host processor?
 - RC port?
 - Switch Primary port?
 - Switch secondary port?
 - EP port?
 - CNA?
- Once identified can we tell what causes this source to limit performance?



The Analysis Process

- Is performance degradation reflected in PCIe link utilization?
- Is performance degradation observed on both primary and secondary links?
- Determine for each link if waiting for requests or stalling traffic
- If neither link is limiting performance the link is waiting for the requester, network or host
- If one of the links is limiting performance determine which port
- What stalls port's performance

Flash Memory	ash Memory Summit					nta ano	ne Ce	eou e	S	vs	}			
File Setup Record	Generate Report	Search View To	ools Window Help	ISTOMETS/IST/ACF (AMIZ/BC)	ive_porto_m	ax_9.50ig.pes	4							
🛩 🖬 🐌 🎁 🎬	• = 🍕 📤	22 🔍 🔍	E 👯 - 🔯 - B	5 🕅 🏹 🔀 🗐	88 88	🔯 🛃 🍝	ծ մահ	±11. ±111. 😣	S 📖	. 🖽 [<u>C</u>	Pkt Link	Split NVM PQ	AHCI
Split Tra	em MRd(32)	RequesterID	CompleterID Tag TO	VC ID Address	Status	Data		letrics #LinkT	ras Resp	. time	Latency T	hrpt MB/s	PId. Bytes	Time Delta
Link Tra 5.0 T		Wr(32) Length	RequesterID Tag	Address 1st BE	Last BE	Data	VCID	Explicit ACK	Motrice	# Packets	Resp. time	PId. Bytes	Thrpt MB/s	Time Delta
10 X4 32	246 Mem 010	0:00000 32	002:00:0 14	2F35A700 1111	1111 3:	2 dwords	0	Packet #37	Metrics	2	126.000 ns	128	968.812	80.000 ns
From beginning of	pono_max_sisting.pe	To beginning of:			Statús SC	32 dwor	rds M	letrics #LinkT	ras Résp 304.0	000 ns 2	Latency T 20.000 ns	401.547	128	12.000 ns 0
Packet - 0		Packet - 21	17897		ast BE	Data 2 dworde		Explicit ACK Packet #44	Metrics	# Packets	Resp. time	PId. Bytes	Thrpt MB/s	Time Delta
Marker 👻 Packet #	0 (start)	Marker 👻 Pa	acket # 217897 (end)		Status	Data		# LinkT	ras Resp	. time	Latency T	hrpt MB/s	Pld. Bytes	Time Delta
Time 🔻	0.0000010200 secs	Time 👻	0.0032283880 secs		SC	32 dwo	rds 🏴	eurics 2	448.0	00 ns 2	18.000 ns	272.478	128	12.000 ns 0
Total Time:	3.227 millicer	onds -	_		1111 32	Data 2 dwords		Packet #51	Metrics	≠ Packets 2	126.000 ns	Pld. Bytes 128	968.812	76.000 ns
Bus I Itilization	uninised				Status	Data	da M	letrics # LinkT	ras Resp	, time	Latency T	hrpt MB/s	PId. Bytes	Time Delta
Bus Otilization	Upstream	D	ownstream		ast BE	Data	VCID	ExplicitACK	1.000.0	# Packets	Resp. time	Pld. Bytes	Thrpt MB/s	Time Delta
Link Utilization	81.789 %		83.778 %		1111 3:	2 dwords	0	Packet #59	Metrics	2	128.000 ns	128	953.674	80.000 ns
Bandwidth	81.688 %		6755.62 Mb/s		Status SC	Data 32 dwor	rds M	letrics #LinkT	ras Resp 306.0	o. time 000 ns 2	Latency T 22.000 ns	hrpt MB/s F 398.923	PId. Bytes 128	Time Delta 12.000 ns 0
Data Throughput	1146.07 MB/s	$\langle \langle \rangle$	1156.98 MB/s		ast BE	Data	VC ID	ExplicitACK	4 Metrics	# Packets	s Resp. time	PId. Bytes	Thrpt MB/s	Time Delta
Packets/second	24860505.53		42654881.62		1111 32	2 dwords		Packet #66		2	126.000 ns	128	968.812	Time Dolta
Split Transaction Perform	ance				Status	32 dwoi	ds M	letrics 2	488.0	000 ns 2	80.000 ns	250.144	128	12.000 ns 0
Bernenes Time	Minimum	Average	Maximum		ast BE	Data 2 dworde	VC ID	Explicit ACK Packet #73	Metrics	# Packets	Resp. time	PId. Bytes	S Thrpt MB/s	Time Delta
Latency	176.000 ns	384.050 ns	784.000 ns		Status	Data		# LinkT	ras Resp	. time	Latency T	hrpt MB/s	Pld. Bytes	Time Delta
Throughput (MB/s)	122.000 ns 227.470 ns 626.000 ns IB/s) 27.845 325.614 540.134				SC	32 dwo1	rds M	etrics 2	366.0	00 ns 2	82.000 ns	333.525	128	12.000 ns 0
Memory Writes Performa	Memory Writes Performance				ast BE 1111 32	Data 2 dwords	VC ID	Explicit ACK Packet #81	Metrics	# Packets 2	 Resp. time 186.000 ns 	PId. Bytes 128	Thrpt MB/s 656.292	Time Delta 80.000 ns
					Status	Data		# LinkTi	ras Resp	, time	Latency T	hrpt MB/s	PId. Bytes	Time Delta
Response Time	onse Time 14.000 ns 98.630 ns 228.000 ns				SC SC	Data		2 Explicit ACK	360.0	000 ns 2	Resp time	339.084	128	12.000 ns 0
Throughput (MB/s)	70.643	1314.133		1111 32	2 dwords	0	Packet #88	Metrics	2	184.000 ns	128 128	663.426	116.000 ns	



 Consistent read and write throughput with full link utilization in both upstream and downstream directions.

Flash Me	emory S	Setu	ıp A v	viewe	d with	Link [·]	Track	er	
Split Tra 5. 76 R← 5. 1 Link Tra R← 5. 215 R← 5.	.0 4 .0 .0 4 3377	IRd(32) RequesterID 0:00000 002:00:0 m MWr(32) Length 10:00000 32	CompleterID Tag TC V0 000:00:0 6 0 0 RequesterID Tag A 002:00:0 8 2F	Address Status 0 2F51E080 SC ddress 1st BE Last BE 2A9F80 1111 1111	Data 32 dwords 4 Metrics 2 Uink Data VC ID Implicit ACK 2 dwords 0 Packet #502	Tras Resp. time Latency 258.000 ns 174.000 ns 258.000 ns 174.000 ns Metrics # Packets 1 76.0	Thrpt MB/s Pld. Bytes Time 473.141 128 16.0 Delta Time Stamp 00 ns 0000 . 000 007 764 s	i Delta Time Stamp 00 ns 0000 . 000 007 748 s	
•									<u>•</u>
Link Tracker - Packet #	481								
) 💷 Þ4 🕅 🔍 (🔲 🔤 🔤	0x 0x 10b RD Text Bin							
Time	Packet #	50	U	ostream	22	15	Down	stream	
00.000 007 616 00.000 007 618 00.000 007 620 00.000 007 622 00.000 007 624 00.000 007 624 00.000 007 628 00.000 007 632 00.000 007 632 00.000 007 636 00.000 007 640 00.000 007 644 00.000 007 644 00.000 007 644 00.000 007 644 00.000 007 644 00.000 007 644 00.000 007 655 00.000 007 655		FD FA C9 37 30 01 55 82 15 88 95 88 96 97 53 33 30 90 90	C1 48 CF 58 DB F4 76 08 76 59 88 88 88 88 88 88 65 7 64 64 64 70 10 77	8D CC 49 20 9E 33 1D 4F 7F 2 30 C0 98 59 D8 85 09 D8 85 09 D8 85 09 D8 85 4F	22 F7 C0 52 A4 D1 70 24 3F A6 8E 75 58 65 95 65 95 65 95 65 95 44 A6 EC DA A6 EC DA FD	1+ 02 2A 3E 79 E5 84 5E C6 62 58 D5 8D 9E 80 9E 88 74 74 34 74 34 67	F5 9A 3E CB 23 22 4A C9 38 66 FA 73 05 59 305 59 205 59 C1 8B 77 77 55	F3 E9 EF 36 FA C3 51 70 00 E2 FC 9F DB DF 6E EE EE 15 91 18 00 C	AU B2 F9 F7 10 A3 B6 A3 B6 A3 B6 A3 B6 C8 B7 C8 B7 C8 B7 C8 B7 C8 B7 C8 B7 C8 B7 C8 B7 C8 B7 C8 C8 B7 C8 B7 C8 C8 C8 C8 C8 C8 C8 C8 C8 C8 C8 C8 C8
0.000 007 658 00.000 007 662 00.000 007 662 00.000 007 666 00.000 007 666 00.000 007 670 00.000 007 672 00.000 007 676 00.000 007 676 00.000 007 676 00.000 007 680	481 (Upstream) 183 (Downstream 184 (Downstream	F8 90 98 98 98 98 98 98 78 78 78 78 78 70 70 60 60 50	17 90 9C 2F 47 07 A1 F5 D5 B6 B6	34 80 67 CF 98 F5 81 00 4A 8A	1A 92 87 51 90 9A A3 DD DD E4 7D	50 11 FB 70 28 6E 58 FF	2D 87 84 F0 28 6F 4C 9D	2D 44 4C D0 AB 6E F4 7E	20 F9 F9 29 AE BD A4

 Link Tracker display shows Upstream and Downstream data transfer with full link utilization across all lanes in both directions.

Setup A vs Setup B Timing Calculator Comparisons

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From beginning of:	To b	peginning of:			From beginning of:		To beginni	ing of:		
Packet 👻 2	Pa	acket 👻 17452			Packet 👻 242	24	Packet	▼ 30167		
Marker 👻	Ma	arker 👻			Marker 👻 Pac	ket # 2424 (Marker	#1) Marker	▼ Packet # 3	0167 (Marker #2)	
Time 🔻	0.0000010280 secs	me 👻 0.00	02592020 secs		Time 🔻	0.000050254) secs Time	▼ 0	.0006338780 secs	
Total Time:	258.174 microsecor	nds 🗨			Total Time:	583.624	microseconds	-		
Bus Utilization	Upstream	Downstream			Bus Utilization	Upst	ream	Downstream		
Link Utilization	81.732 %	83.928 %			Link Utilizatio	n 4	8.709 %	47.337 %		
Time Coverage	81.625 %	83.820 %			Time Coverage	e 4	8.650 %	47.282 %		
Bandwidth	16346.34 Mb/s	16785.58 Mb/s			Bandwidtl	h 9741	.89 Mb/s	9467.40 Mb/s		
Data Throughput	1144.64 MB/s	1159.60 MB/s			Data Ihroughpu Backets/secon	lt 643	.02 MB/s	635.09 MB/s	 _	
Packets/second	24944417.33	42645657.58			Factorsysecon	2092	/03/.00	2000/004.55		
Split Transaction Performa	ance Minimum Averad	ge Maximum			- Split Transaction Perl	formance Minimum	Average	Maximun	CPU I7	RC
Response Time 1	190.000 ns 383.960) ns 728.000	CDU	- 1	Response Time	502.000 ns	1.129 µs	5.636 µ:	core	
Latency j	138.000 ns 226.610) ns 502.000	RC		Latency	430.000 ns	940.310 ns	5.426 µ	x4	
Throughput (MB/s)	38.532 325.76	55 521.66	core		Throughput (MB/s)	5.537	154.453	237.491	PCIe 2.0	
Memory Writes Performan	ice		x4		Memory Writes Perfo	rmance	1		Switch	
Response Time	14.000 ns 98.140	ns 202.			Response Time	42.000 ns	157.180 ns	276.000	x4	
Throughput (MB/s)	84.771 1321.28	38 1649			Throughput (MB/s)	45.960	792.824	1649	Ň	
Note: 1 Mb= 1000 * 1000	bits and 1 MB = 1024 * 1024	4 bytes.	EP		Note: 1 Mb= 1000 *	1000 bits and 1 MB -	= 1024 * 1024 byte:	5.	CNA	EP
		Se	etup A			0 1				
Se	etun A					Set		5	Setup B	
										20

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Setup B: Cross sync between primary and secondary links

Split Tra 4	R←	5.0 x4	Mem	MRd(32) 00:00000	RequesterID 000:00:0	CompleterID 001:00:0	Tag 5	Address A1462600	Data 32 dwords	▶ Metrics	# LinkT/ s 2	Time Delta 76.000 ns	Time Stamp 000 . 000 001 550 s
* Split Tra 5	R←	5.0 x4	Mem	MRd(32) 00:00000	RequesterID 000:00:0	CompleterID 001:00:0	Tag 2	Address A1462680	Data 32 dwords	Metrics	# LinkTras 2	164.000 ns	Time Stamp 0000 . 000 001 626 s
* Split Tra 6	R←	5.0 x4	Mem	MRd(32) 00:00000	RequesterID 000:00:0	CompleterID 001:00:0	Tag 6	Address A1462700	Data 32 dwords	▶ Metrics	# LinkTras 2	Time Delta 88.000 ns	Time Stamp 0000 . 000 001 790 s
* Split Tra 7	R⊢	5.0 x4	Mem	MRd(32) 00:00000	RequesterID 000:00:0	CompleterID 001:00:0	Tag 0	Address A1462780	Data 32 dwords	► Metrics	# LinkTras 2	Time Delta 136.000 ns	Time Stamp 0000 . 000 001 878 s
* Split Tra 8	R⊢	5.0 x4	Mem	MRd(32) 00:00000	RequesterID 000:00:0	CompleterID 001:00:0	Tag 1	Address A1462800	Data 32 dwords	► Metrics	# LinkTras 2	Time Delta 84.000 ns	Time Stamp 0000 . 000 002 014 s
* Split Tra 9	R←	5.0 x4	Mem	MRd(32) 00:00000	RequesterID 000:00:0	CompleterID 001:00:0	Tag 4	Address A1462880	 Data 32 dwords 	▶ Metrics	# LinkTras 2	Time Delta 240.000 ns	Time Stamp 0000 . 000 002 098 s
* Split Tra 10	R←	5.0 x4	Mem	MRd(32) 00:00000	RequesterID 000:00:0	CompleterID 001:00:0	Tag 5	Address A1462900	 Data 32 dwords 	▶ Metrics	# LinkTras 2	Time Delta 112.000 ns	Time Stamp 0000 . 000 002 338 s
	-												
C:\isaac\c	uston	ners\ls	i\ACP\cr	oss_sync_tests\spe	ed_6gigpex.pex								
C:\isaac\c Split Tra 6	uston	ners\ls 5.0 x4	i∖ACP\cr Mem	oss_sync_tests\spe MRd(32) 00:00000	ed_6gigpex.pex RequesterID 003:00:0	CompleterID 000:00:0	Tag 5	Address 2F462600	► Data 32 dwords	▶ Metrics	# LinkTr s	Time Delta 80.000 ns	Time Stamp 000 . 000 001 750 s
C:\isaac\c Split Tra 6 Split Tra 7	uston R←	ners\ls 5.0 x4 5.0 x4	i\ACP\cr Mem Mem	oss_sync_tests\spe MRd(32) 00:00000 MRd(32) 00:00000	ed_6gig_pex.pex RequesterID 003:00:0 RequesterID 003:00:0	CompleterID 000:00:0 CompleterID 000:00:0	Tag 5 Tag 2	Address 2F462600 Address 2F462680	 Data 32 dwords Data 32 dwords 	Metrics Metrics	# LinkTr s 2 # LinkTras 2	Time Delta 80.000 ns 184.000 ns	000 . 000 001 750 s Time Stamp 0000 . 000 001 830 s
C:\isaac\c Split Tra 6 Split Tra 7 Split Tra 8	R+- R R	5.0 x4 5.0 x4 5.0 x4 5.0 x4 5.0 x4	i\ACP\cr Mem Mem Mem	oss_sync_tests\spe MRd(32) 00:00000 MRd(32) 00:00000 MRd(32) 00:00000	ed_6gig_pex.pex RequesterID 003:00:0 RequesterID 003:00:0 RequesterID 003:00:0	CompleterID 000:00:0 CompleterID 000:00:0 CompleterID 000:00:0	Tag 5 Tag 2 Tag 6	Address 2F462600 Address 2F462680 Address 2F462700	 Data 32 dwords Data 32 dwords Data 32 dwords 	Metrics Metrics Metrics	# LinkTr s 2 # LinkTras 2 # LinkTras 2	Time Delta 80.000 ns 184.000 ns Time Delta 80.000 ns	Time Stamp 000 .000 001 750 s Time Stamp 0000 . 000 001 830 s Time Stamp 0000 . 000 002 014 s
C:\isaac\c Y Split Tra 6 Y Split Tra 7 Y Split Tra 8 Y Split Tra 9	R+-	5.0 x4 5.0 x4 5.0 x4 5.0 x4 5.0 x4	i\ACP\cr Mem Mem Mem Mem	oss_sync_tests\spe MRd(32) 00:00000 MRd(32) 00:00000 MRd(32) 00:00000 MRd(32) 00:00000	ed_6gig_pex.pex RequesterID 003:00:0 RequesterID 003:00:0 RequesterID 003:00:0 RequesterID 003:00:0	CompleterID 000:00:0 CompleterID 000:00:0 CompleterID 000:00:0 CompleterID 000:00:0	Tag 5 Tag 2 Tag 6 Tag 0	Address 2F462600 Address 2F462680 Address 2F462700 Address 2F462780	 Data 32 dwords Data 32 dwords Data 32 dwords Data 32 dwords 	Metrics Metrics Metrics Metrics	# LinkTra 2 # LinkTras 2 # LinkTras 2 # LinkTras 2	Time Delta 80.000 ns 184.000 ns Time Delta 80.000 ns Time Delta 80.000 ns	Time Stamp 000 .000 001 750 s Time Stamp 0000 .000 001 830 s Time Stamp 0000 .000 002 014 s Time Stamp 0000 .000 002 014 s Time Stamp 0000 .000 002 014 s
C:\isaac\c Split Tra 6 Split Tra 7 Split Tra 8 Split Tra 9 Split Tra 10	R+- R R R	5.0 x4 5.0 x4 5.0 x4 5.0 x4 5.0 x4 5.0 x4 5.0 x4	i\ACP\cr Mem Mem Mem Mem	oss_sync_tests\spectrum MRd(32) 00:00000	ed_6gig_pex.pex RequesterID 003:00:0 RequesterID 003:00:0 RequesterID 003:00:0 RequesterID 003:00:0	CompleterID 000:00:0 CompleterID 000:00:0 CompleterID 000:00:0 CompleterID 000:00:0	Tag 5 Tag 2 Tag 6 Tag 0 Tag 1	Address 2F462600 Address 2F462680 Address 2F462700 Address 2F462780 Address 2F462800	 Data 32 dwords Data 32 dwords Data 32 dwords Data 32 dwords Data 32 dwords 	Metrics Metrics Metrics Metrics	# LinkTras 2 # LinkTras 2 # LinkTras 2 # LinkTras 2 # LinkTras 2	Time Delta 80.000 ns 184.000 ns Time Delta 80.000 ns Time Delta 80.000 ns Time Delta 96.000 ns	Time Stamp 000 .000 001 750 s Time Stamp .000 001 830 s Time Stamp .000 .000 002 014 s 0000 . 000 002 014 s .0000 .000 002 014 s Time Stamp .0000 .000 002 094 s Time Stamp .0000 .000 002 174 s
C:\isaac\c Split Tra 6 Split Tra 7 Split Tra 8 Split Tra 9 Split Tra 10 Split Tra 11	uston	5.0 x4 5.0 x4 5.0 x4 5.0 x4 5.0 x4 5.0 x4 5.0 x4 5.0 x4	i\ACP\cr Mem Mem Mem Mem Mem	oss_sync_tests\spec MRd(32) 00:00000 MRd(32) 00:00000	ed_6gig_pex.pex RequesterID 003:00:0 RequesterID 003:00:0 RequesterID 003:00:0 RequesterID 003:00:0 RequesterID 003:00:0	CompleterID 000:00:0 CompleterID 000:00:0 CompleterID 000:00:0 CompleterID 000:00:0 CompleterID 000:00:0	Tag 5 Tag 2 Tag 6 Tag 0 Tag 1 1 Tag 4	Address 2F462600 Address 2F462680 Address 2F462700 Address 2F462780 Address 2F462800 Address 2F462800 Address 2F462800 Address 2F462800	 Data 32 dwords Data 32 dwords Data 32 dwords Data 32 dwords Data 32 dwords Data 32 dwords 	Metrics Metrics Metrics Metrics Metrics Metrics	# LinkTras 2 # LinkTras 2 # LinkTras 2 # LinkTras 2 # LinkTras 2 # LinkTras 2	Time Delta 80.000 ns 184.000 ns Time Delta 80.000 ns Time Delta 80.000 ns Time Delta 96.000 ns Time Delta 94.000 ns	Time Stamp 000 .000 001 750 s Time Stamp .000 001 830 s Time Stamp .000 .000 002 014 s Time Stamp .000 .000 002 174 s Time Stamp .0000 .000 002 174 s Time Stamp .0000 .000 002 270 s



however low latencies for read completions



Setup B: Capture between switch and CNA

- What causes the long completion latencies?
- CNA in root mode
- Finite initial completion credits
- NT mode implemented in switch
- Completion credit is exhausted





Why need a tool to debug the serial links within the fabric?

- Need to see that the link width and speed come up correctly. This directly affects throughput.
- How to communicate to switch vendor the nature of the issue? An analyzer trace can prove the problem is with the switch. This is the correct way to show the root cause and communicate it to the vendor.
- The vendor may have never seen the issue since they do not use large networked storage fabrics.
- systems assembly/test engineers and system integrators need to be able to detect an interoperability problem and show evidence of the root cause of the problem and report to the silicon manufacturer



NVMe performance analysis

- As NVMe technology matures leading to a need to maximize performance
- What's special about NVMe performance vs general PCIe performance
- Differences in performance analysis techniques between SSD drives and traditional magnetic drives.



NVMe has different latency sources

- Doorbell to command submission
- Command submission to Data transfer
- Command submission to command completion
- Command completion to interrupt



NVMe performance criteria

- Response time
 - Transmission of the complete transfer from the beginning of the PCIe packet to the end of the last PCIe packet of this NVMe command
- Latency time
 - Time from the last PCIe packet of the NVMe command submission to the first PCIe packet of the NVMe command completion
- IOPS
 - # of overlapping NVMe commands from submission doorbell to completion doorbell

instantaneous performance metrics

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NVMe Cm 21	D	OPC Read	SQID 0x0001	CQID 0x0001	CID 0x0003	<mark>'Data</mark> 128 dwords	MPTR 00000000:00000000	PRP1 00000002:2DD9B000	PRP2 00000000:00000000	SLBA 00000000:00000000	NLB PF 0x0000 1	NINFO FUA LR	DSM No freque	ACCF ncy information provided
	ACCL Solution	SEQR INCOM 0 0 (EILBRT E	LBAT ELBA	ATM 000 ST	SCT Generic Comm	and Status Successful	C Device ID Completion 006:00:0	MN NVMeLeCroy000000	Explicit SQyTDBL I NVMe #120	Explicit IOSQ NVMe #121	Explicit IOCQ Ex NVMe #123	plicit CQyHDBL NVMe #125	NSID 0x00000001 Metrics
	# NVME 6	Trans Resp. time 683.652 u	e Latency F is 464.192 us	Pld. Bytes Th 512	nrpt MB/s 0.714	IOPS SDb 1462.733 682	l - CDbl SDbl - CCmd .240 us 631.476 us	SCmd - CCmd Time 466.192 us 692.5	Delta Time Stam 12 us 0039 . 509 104	p 202 s				
NVMe Cm 22	D	OPC Read	SQID 0x0001	CQID 0x0001	CID 0x0004	<mark>' Data</mark> 128 dwords	MPTR 00000000:00000000	PRP1 00000002:2DFC3CC0	PRP2 00000000:00000000	SLBA 00000000:0000000	NLB Pl 0 0x00000	RINFOFUALR0x000	DSM No freque	ACCF ency information provided
	ACCL Solution	SEQR INCOM 0 0	EILBRT E	LBAT ELBA	ATM 000 ST	SCT Generic Comm	and Status Successful	C Device ID Completion 006:00:0	MN NVMeLeCroy000000	Explicit SQyTDBL I NVMe #126	Explicit IOSQ NVMe #127	Explicit IOCQ Ex NVMe #129	plicit CQyHDBL NVMe #131	NSID 0x00000001 Metrics
	# NVME 6	Trans Resp. time 812.300 u	e Latency F Is 509.776 us	Pld. Bytes Th 512	nrpt MB/s 0.601	IOPS SDb 1231.072 810	l - CDbl SDbl - CCmd .888 us 760.884 us	SCmd - CCmd Time 1 511.840 us 13.51	Delta Time Stam 14 ms 0039 . 509 796	р 714 s				
NVMe Cm 23	D	OPC Read	SQID 0x0001	CQID 0x0001	CID 0x0005	<mark>' Data</mark> 128 dwords	MPTR 00000000:00000000	PRP1 00000002:2DFCD980	PRP2 00000000:00000000	SLBA 00000000:00000000	NLB PF 0x0000	RINFOFUALR0x000	DSM No freque	ACCF ency information provided
	ACCL Solution	SEQR INCOM 0 0	EILBRT E	LBAT ELBA	ATM 000 ST	SCT Generic Comm	and Status Successful	C Device ID Completion 006:00:0	MN NVMeLeCroy000000	Explicit SQyTDBL I NVMe #132	Explicit IOSQ NVMe #133	Explicit IOCQ Ex NVMe #135	plicit CQyHDBL NVMe #137	NSID 0x00000001 Metrics
	# NVME 6	Trans Resp. time 681.364 u	e Latency F is 470.128 us	Pld. Bytes Th 512	nrpt MB/s 0.717	IOPS SDb 1467.644 679	I - CDbl SDbl - CCmd .984 us 626.980 us	SCmd - CCmd Time 472.144 us 696.5	Delta Time Stam 84 us 0039 . 523 310	p 538 s				
NVMe Cm 24	d D	OPC Read	SQID 0x0001	CQID 0x0001	CID 0x0006	<mark>' Data</mark> 128 dwords	MPTR 00000000:00000000	PRP1 00000002:2DD9D000	PRP2 00000000:00000000	SLBA 00000000:00000000	NLB PF 0x0000	RINFOFUALR0x000	DSM No freque	ACCF ency information provided
	ACCL Solution	SEQR INCOM 0 0 (EILBRT E	LBAT ELBA	ATM 000 ST	SCT Generic Comm	and Status Successful	C Device ID Completion 006:00:0	MN NVMeLeCroy000000	Explicit SQyTDBL I NVMe #138	Explicit IOSQ NVMe #139	Explicit IOCQ Ex NVMe #141	plicit CQyHDBL NVMe #143	NSID 0x00000001 Metrics
	# NVME 6	Trans Resp. time 780.748 u	e Latency F is 475.760 us	Pld. Bytes Th 512	nrpt MB/s 0.625	IOPS SDb 1280.823 779	l - CDbl SDbl - CCmd 112 us 726.268 us	SCmd - CCmd Time I 477.808 us 804.8	Delta Time Stam 80 us 0039 . 524 007	р 122 s				
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Response time, Latency time



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SAS Definition IOPS = 1/ Latency NVMe definition IOPS = # commands / Sdbl-CDbl Example 1/ 631240 usec = 1462.733



Categorized Performance analysis

Read Requests Performance

Requester -> Completer	Total	Thrpt MB/s (Min)	Thrpt MB/s (Avg)	Thrpt MB/s (Max)	Resp. time (Min)	Resp. time (Avg)	Resp. time (Max)	Latency (Min)	Latency (Avg)	Latency (Max)
000:03:0 -> 000:00:0, Cfg TC0	416	0.995	21.175	1502.353	378.000 ns	1.172 us	3.834 us	204.000 ns	1.009 us	3.662 us
000:03:0 -> 002:00:0, Cfg TC0	5314	7.914	6189.918	11387.661	338.000 ns	376.760 ns	482.000 ns	174.000 ns	210.050 ns	304.000 ns
000:03:0 -> 003:04:0, Cfg TC0	5198	7.981	6833.574	11387.661	338.000 ns	382.470 ns	478.000 ns	174.000 ns	216.120 ns	304.000 ns
000:03:0 -> 004:00:0, Cfg TC0	1044	0.245	22.834	1526.181	2.522 us	3.152 us	15.562 us	2.360 us	2.986 us	15.396 us
000:03:0 -> 003:05:0, Cfg TC0	5194	8.048	6841.318	11387.661	338.000 ns	382.450 ns	474.000 ns	174.000 ns	215.810 ns	312.000 ns
000:03:0 -> 005:00:0, Cfg TC0	1024	0.093	23.177	1507.059	2.554 us	3.202 us	41.170 us	2.390 us	3.035 us	40.994 us
000:03:0 -> 003:06:0, Cfg TC0	5190	8.048	6844.803	11387.661	338.000 ns	382.440 ns	474.000 ns	174.000 ns	216.210 ns	312.000 ns
000:03:0 -> 006:00:0, Cfg TC0	1024	0.082	23.386	1523.765	2.526 us	3.198 us	46.490 us	2.370 us	3.032 us	46.322 us
000:03:0 -> 003:07:0, Cfg TC0	5190	7.981	6845.320	11387.661	338.000 ns	382.410 ns	478.000 ns	174.000 ns	216.160 ns	304.000 ns
000:03:0 -> 007:00:0, Cfg TC0	1024	0.081	23.116	1521.356	2.530 us	3.212 us	46.918 us	2.386 us	3.045 us	46.774 us
000:03:0 -> 004:00:0, Mem TC0	2089	0.005	0.889	16.124	3.954 us	6.335 us	807.882 us	3.796 us	6.168 us	807.708 us
004:00:0 -> 000:00:0, Mem TC0	277	155.702	186.383	468.472	274.000 ns	332.680 ns	456.000 ns	178.000 ns	230.490 ns	300.000 ns
000:03:0 -> 005:00:0, Mem TC0	2083	0.005	0.892	15.949	3.962 us	6.252 us	804.430 us	3.790 us	6.084 us	804.254 us
005:00:0 -> 000:00:0, Mem TC0	190	155.249	184.474	214.913	284.000 ns	329.720 ns	378.000 ns	168.000 ns	227.480 ns	274.000 ns
000:03:0 -> 006:00:0, Mem TC0	2078	0.005	0.873	16.188	3.962 us	6.371 us	805.082 us	3.786 us	6.203 us	804.902 us
006:00:0 -> 000:00:0, Mem TC0	541	61.527	1042.631	2765.319	288.000 ns	425.760 ns	526.000 ns	174.000 ns	248.020 ns	354.000 ns
000:03:0 -> 007:00:0, Mem TC0	2071	0.005	0.859	1.904	3.958 us	6.363 us	813.442 us	3.794 us	6.195 us	813.274 us
007:00:0 -> 000:00:0, Mem TC0	535	58.388	1051.490	2770.340	288.000 ns	428.030 ns	516.000 ns	178.000 ns	248.270 ns	338.000 ns
004:00:0 -> 000:31:6, Mem TC0	429	78.250	148.991	200.774	304.000 ns	416.230 ns	780.000 ns	192.000 ns	312.830 ns	676.000 ns
005:00:0 -> 000:31:6, Mem TC0	393	109.382	153.033	204.816	298.000 ns	402.730 ns	558.000 ns	194.000 ns	300.040 ns	454.000 ns
006:00:0 -> 000:31:6, Mem TC0	637	62.196	271.715	2601.922	302.000 ns	399.540 ns	614.000 ns	190.000 ns	283.240 ns	510.000 ns
007:00:0 -> 000:31:6, Mem TC0	872	44.703	285.667	2726.789	302.000 ns	395.230 ns	578.000 ns	198.000 ns	277.180 ns	474.000 ns
	42813									

Timing Calculator Queue View

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- View submission and completion Queues
- Compare Queues to see where overloading is occurring
- Verify if submission and completion queues are equal and that nothing was lost

	g of:		To be	ginning of:		
acket 👻	0		Pack	et 🔹 1	714903	
larker 🔻			Mark	ker 👻		
ime 🔻	1 1	10.4692413420	secs Time		187.26	42685500 secs
tal		176.795	seconds	•		
CIe timings	Queue Utiliz	ation				
Craph bypo	NV/M Cor	mandı Comma	nda nor quou		_]	
Graph type	NVM Cor	nmand: Comma	inds per queu	9	•	
1						
500						
450						
400						
350	342	34	17 3	42	347	
300	_					
250						
250						
250 200						
250 200 150	, 1	47	130	147		
250 200 150 100	, 1	47	130	147		
250 200 150 100		47	130	147		
250 200 150 100 50		47	130	147		
250 200 150 130 50 0		47	130	147	1	
250 200 150 130 50 50 50 50		47 1 3 4			1 CQ CQ 4 5	
250 200 150 130 50 0 50 0	1 50 50 5	47 1 3Q SQ S 3 4 5	130 Q CQ 5 0	147 147 CQ CQ 1 3	1 CQ CQ 4 5	
250 200 150 130 50 50 50 50 0 \$0 0	1 5Q 5 1	47 1 3Q SQ S 3 4	130 Q CQ 5 0	147 200 100 100 100 100 100 100 100	1 CQ CQ 4 5	4





Long Recordings Memory Utilization Model Assumptions

- 16GB memory dedicated per direction
- Capture Duration Doubles when using expanded mode
- Recording stops as soon as either side fills up
- SSD rate for read is 2 GB / sec or 16 Gb/sec
 - This implies a Gen3 x4 link with 60% utilization
- Assume 16 pages / command
- Assume 2 doorbells
- Assume no interrupt aggregation
 - Dropped idles, SKPs, EDSs, DLLPs
- Each TLP occupies between 1.09-1.43 memory blocks on average



NVMe Enhanced mode – long

recordings

- 3 Columns of filter in items
 - 1. Entities that form NVMe Commands
 - 2. NVMe Control Registers
 - 3. PCIe entities related to NVMe traffic





Conditional performance analysis using Verification scripting

- Extract metrics within a defined range
- LBA drive access pattern
- Queue access distribution
- Low power states entry / exist
- Multiple NVMe commands per TLP referenced by time stamps



{

VSE script example

OnStartScript() ReportText("OnStartScript called..."); ReportText("\n\nRunning...\n"); EventCount = 0; SendAllChannels(); SendAllTraceEvents(); SendLevelOnly(_NVMC); #SendLevelOnly(_SPLIT); filePtr = OpenFile("C:\\Documents\\test.csv"); WriteString(filePtr,"Start time, Response time, latencyTime, LBA, Length, QID, FUA"); }

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VSE script example

ProcessEvent()

respTime= in.Metric ResponseTime; latencyTime=in.Metric LatencyTime; time=in.Time; throughput=in.Metric Throughput; CMD = in.nvmcCommandOpCode; NLB = in.Read NLB; SLBA0 = in.Read_SLBA_DW0; SLBA1 = in.Read SLBA DW1; SLBA = in.Read SLBA; SQID = in.nvmcSubmissionQueueID; if(CMD!= 1) FUA = 0: FUA = in.Write FUA; else if (SQID!= 0) {ReportText(FormatEx("%s,%s,%d,%s,%d,%d,%d", CSV_Val_TimeStamp_Seconds(in.Time), CSV Val TimeStamp Seconds(respTime), CMD, (SLBA), (NLB+1), SQID, FUA));} if (SQID!= 0) {WriteString(filePtr,FormatEx("%s,%s,%s,%s,%d,%d,%d", CSV Val TimeStamp Seconds(in.Time), CSV Val TimeStamp Seconds(respTime), CSV_Val_TimeStamp_Seconds(latencyTime), (SLBA), (NLB+1), SQID, FUA));} if(EventCount == MAX NUMBER OF EVENTS) ScriptPassed(); EventCount++; return Complete();}

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SSD traffic statistics

Start time	Response time	Command	LBA	Length	QID	FUA
97.44338	0.000819348	2	0x800000000000000	1	3	0
97.44427	0.00087518	2	0x800000000000000	1	3	0
97.44522	0.001432268	2	0xC00000000000000	4	3	0
97.4476	0.0008339	2	0x800000000000000	1	5	0
97.44904	0.00085046	2	0x800000000000000	1	1	0
97.45029	0.000832564	2	0x800000000000000	1	3	0
97.45125	0.000837508	2	0x800000000000000	1	3	0
97.4522	0.00144462	2	0xC00000000000000	4	3	0
97.45464	0.000901324	2	0x800000000000000	1	5	0
97.4576	0.000903012	2	0x800000000000000	1	3	0
97.4586	0.001566428	2	0xC00000000000000	4	3	0
97.46027	0.003569364	2	0x800000000000000	13	3	0
97.46389	0.00089634	2	0x7FE807000000000	1	3	0
97.46483	0.00089858	2	0x80F403000000000	1	3	0
97.46662	0.001507236	2	0x800000000000000	1	5	0
97.47032	0.00072846	2	0x800000000000000	1	1	0
97.47108	0.001501092	2	0xC00000000000000	4	3	0
97.47268	0.003540652	2	0x800000000000000	13	3	0
97.47632	0.000855788	2	0x7FE807000000000	1	3	O
97.47727	0.0008609	2	0x80F403000000000	1	3	O
97.47907	0.000801716	2	0x800000000000000	1	3	O
97.48059	0.000653244	2	0x800000000000000	1	5	0
97.48137	0.000685132	2	0x800000000000000	1	5	0
97.48209	0.001389612	2	0xC00000000000000	4	5	0
97.48421	0.00069338	2	0x800000000000000	1	5	0
97.48543	0.000734724	2	0x800000000000000	1	5	0





Traffic generation

 The use of traffic generators to stress test an NVMe device and characterize its performance independent of a specific platform.



High \bullet doorbell entry Fast •

Ge	neration script for stress testing
Memory Summit	Address Space Write Location Mem64 Offset Size Data 64 bytes Packet 2.5 TLP 0 Mem MWr(32) 010:00000 Length 064:02:0 Tag Address EC031008 1st BE Last BE Data LCRC 0xA1C03DFF Wait TLP Header Timeout Infinite Fmt Type FFFFFFFFEE20000 Address 1st BE Last BE Data LCRC Packet 2.5 TLP MWr(32) Length RequesterID Tag Address 1st BE Last BE Data LCRC
	3 x1 0 Mem 010:00000 1 064:02:0 1 E 0 01000000 0xAA9510A1 Image: S → E QuickTiming markers not set QuickTiming markers not set Image: S - S - S - S - S - S - S - S - S - S
High	Generation Script Editor 1 🖬 ▶4 1))); ↓ 🖄 📾 🕮 ↦ ▾ ⇔ ▾ 🗮 🌾 🎓 🌾 🤮 👪 🥵 🦢
doorbell	15 Structure=NVMe 16 ↓ 17 Offset = 0x10000 18 NVMeStructType=NVMCommand
entry	$\begin{array}{llllllllllllllllllllllllllllllllllll$
Fast	23 NumLBlocks = 1} 25 ; Write Queue 1 dorbell 26 □ packet="Temp_OneDwordWrite"
completions	<pre>27 { Tag = 0 Address = (CONTROLLER_REGISTERS_BASE + 0x1008) 29 Payload = (55000000) } 30 ; Wait for the Controller to process the command. The last thing would be the MSI-X interrupt at vector 1 31 = Wait=TLP{ 32 TLPType = MWr32 33 Address = CQ1_INT_VECTOR_ADDRESS} 34 ; Write Queue 1 Completion Queue Head 35 = packet="Temp_OneDwordWrite" { 36 Address = (CONTROLLER_REGISTERS_BASE + 0x100C)</pre>
anta Clara, CA	37 L Payload = (55000000) }



NVMe write with queueing

Link Tra 34 R← 2.5 x8	TLP 2852	Mem	MRd(64) 001:00000	Length 512	RequesterID 001:00:0	Tag 69	Address 00000008:C0000000	1st BE La 1111	ast BE 1111	VC ID 0	Explicit ACK Packet #22966	Metrics	# Packets 2	Time Delta 32.000 ns
[*] Link Tra 35 R← 2.5 x8	TLP 2853	Mem	MRd(64) 001:00000	Length 512	RequesterID 001:00:0	Tag 70	Address 00000008:C0000800	1st BE La 1111	ast BE 1111	VC ID 0	Implicit ACK Packet #22976	Metrics	# Packets 1	Time Delta 32.000 ns
[*] Link Tra 36 R← 2.5 x8	TLP 2854	Mem	MRd(64) 001:00000	Length 512	RequesterID 001:00:0	Tag 71	Address 00000008:C0001000	1st BE La 1111	ast BE 1111	VC ID 0	Implicit ACK Packet #22976	Metrics	# Packets 1	Time Delta 32.000 ns
Link Tra 37 R← 2.5 x8	TLP 2855) Mem	MRd(64) 001:00000	Length 512	RequesterID 001:00:0	Tag 72	Address 00000008:C0001800	1st BE La 1111	ast BE 1111	VC ID 0	Implicit ACK Packet #22976	Metrics	# Packets 1	Time Delta 80.000 ns
[*] Link Tra 38 R← 2.5 x8	TLP 2856	Mem	MRd(64) 001:00000	Length 512	RequesterID 001:00:0	Tag 73	Address 00000008:C0002000	1st BE La 1111	ast BE 1111	VC ID 0	Implicit ACK Packet #22976	∙ Metrics	# Packets 1	Time Delta 32.000 ns
Link Tra 39 R← 2.5 x8	TLP 2857	Mem	MRd(64) 001:00000	Length 512	RequesterID 001:00:0	Tag 74	Address 00000008:C0002800	1st BE La 1111	ast BE 1111	VC ID 0	Implicit ACK Packet #22976	∙ Metrics	# Packets 1	Time Delta 32.000 ns
Link Tra 40 R← 2.5 x8	TLP 2858	Mem	MRd(64) 001:00000	Length 512	RequesterID 001:00:0	Tag 75	Address 00000008:C0003000	1st BE La 1111	ast BE 1111	VC ID 0	Implicit ACK Packet #22976	Metrics	# Packets 1	Time Delta 32.000 ns
[×] Link Tra 41 R← 2.5 x8	TLP 2859	Mem	MRd(64) 001:00000	Length 512	RequesterID 001:00:0	Tag 76	Address 00000008:C0003800	1st BE La 1111	ast BE 1111	VC ID 0	Implicit ACK Packet #22976	Metrics	# Packets 1	Time Delta 80.000 ns
Link Tra 42 R← 2.5 x8	TLP 2860	Mem	MRd(64) 001:00000	Length 512	RequesterID 001:00:0	Tag 77	Address 00000008:C0004000	1st BE La 1111	ast BE 1111	VC ID 0	Explicit ACK Packet #22976	Metrics	# Packets 2	Time Delta 32.000 ns
Link Tra 43 R← 2.5 x8	TLP 2861	Mem	MRd(64) 001:00000	Length 512	RequesterID 001:00:0	Tag 78	Address 00000008:C0004800	1st BE La 1111	ast BE 1111	VC ID 0	Implicit ACK Packet #22986	Metrics	# Packets 1	Time Delta 32.000 ns
Link Tra 44 R← 2.5 x8	TLP 2862	Mem	MRd(64) 001:00000	Length 512	RequesterID 001:00:0	Tag 79	Address 00000008:C0005000	1st BE La 1111	ast BE 1111	VC ID 0	Implicit ACK Packet #22986	Metrics	# Packets 1	Time Delta 32.000 ns
Link Tra 45 R+ 2.5 x8	TLP 2863	Mem	MRd(64) 001:00000	Length 512	RequesterID 001:00:0	Tag 80	Address 00000008:C0005800	1st BE La 1111	ast BE 1111	VC ID 0	Implicit ACK Packet #22986	Metrics	# Packets 1	Time Delta 80.000 ns
Link Tra 46 R← 2.5 x8	TLP 2864	Mem	MRd(64) 001:00000	Length 512	RequesterID 001:00:0	Tag 81	Address 00000008:C0006000	1st BE La 1111	ast BE 1111	VC ID 0	Implicit ACK Packet #22986	Metrics	# Packets 1	Time Delta 32.000 ns
Link Tra 47 R← 2.5 x8	TLP 2865	Mem	MRd(64) 001:00000	Length 512	RequesterID 001:00:0	Tag 82	Address 00000008:C0006800	1st BE La 1111	ast BE 1111	VC ID 0	Implicit ACK Packet #22986	Metrics	# Packets 1	Time Delta 32.000 ns



NVMe write with no queuing

Link Tra 2014	R←	2.5 x8	TLP 3319	Mem	MRd(64) 001:00000	Length 512	RequesterID 001:00:0	Tag 24	Address 00000008:C0009800	1st BE 1111	Last BE 1111	VC ID 0	Explici Packet #	t ACK #31575	# Packe	ets Time Delta 96.000 ns	Time Stam 0000 . 050 423	p 234 s
[*] Link Tra 2015	R→	2.5 x8	TLP 365	Cpl	CpID 010:01010	Length 128	RequesterID 001:00:0	Tag 196	CompleterID 5000:00:0	Status SC	BCM E	lyte Cnt 1536	Lwr Addr 0x00	Data 128 dwords	VC ID 0	Explicit ACK Packet #31589	Metrics # Packe	ts Time Delta 268.000 ns
[*] Link Tra 2016	R→	2.5 x8	TLP 366	Cpl	CpID 010:01010	Length 128	RequesterID 001:00:0	Tag 186	CompleterID 5000:00:0	<mark>Status</mark> SC	BCM E	lyte Cnt 1024	Lwr Addr 0x00	Data 128 dwords	VC ID 0	Explicit ACK Packet #31595	Metrics # Packe	ts Time Delta 308.000 ns
Link Tra 2017	R→	2.5 x8	TLP 367	Cpl	CpID 010:01010	Length 128	RequesterID 001:00:0	Tag 186	CompleterID 5000:00:0	<mark>Status</mark> SC	BCM E	lyte Cnt 512	Lwr Addr 0x00	Data 128 dwords	VC ID 0	Explicit ACK Packet #31600	Metrics # Packe	ts Time Delta 272.000 ns
[*] Link Tra 2018	R→	2.5 x8	TLP 368	Cpl	CpID 010:01010	Length 128	RequesterID 001:00:0	Tag 197	CompleterID 5000:00:0	Status SC	BCM E	lyte Cnt 2048	Lwr Addr 0x00	Data 128 dwords	VC ID 0	Explicit ACK Packet #31606	Metrics # Packe	ts Time Delta 8.000 ns
[*] Link Tra 2019	R←	2.5 x8	TLP 3320	Mem	MRd(64) 001:00000	Length 512	RequesterID 001:00:0	Tag 25	Address 00000008:C000A000	1st BE 1111	Last BE	VC ID	Explici Packet i	t ACK #31590 Metrics	# Packe	ets Time Delta 272.000 ns	Time Stam 0000 . 050 424	р 186 s
Link Tra 2020	R→	2.5 x8	TLP 369	Cpl	CpID 010:01010	Length 128	RequesterID 001:00:0	Tag 197	CompleterID S 000:00:0	Status SC	BCM E	lyte Cnt 1536	Lwr Addr 0x00	Data 128 dwords	VC ID 0	Explicit ACK Packet #31612	Metrics # Packe	ts Time Delta 272.000 ns
[*] Link Tra 2021	R→	2.5 x8	TLP 370	Cpl	CpID 010:01010	Length 128	RequesterID 001:00:0	Tag 187	CompleterID 5000:00:0	Status SC	BCM E	lyte Cnt 1024	Lwr Addr 0x00	Data 128 dwords	VC ID 0	Explicit ACK Packet #31614	Metrics # Packe	ts Time Delta 264.000 ns
Link Tra 2022	R→	2.5 x8	TLP 371	Cpl	CpID 010:01010	Length 128	RequesterID 001:00:0	Tag 187	CompleterID 5000:00:0	Status SC	BCM E	lyte Cnt 512	Lwr Addr 0x00	Data 128 dwords	VC ID 0	Explicit ACK Packet #31620	Metrics # Packe	ts Time Delta 228.000 ns
Link Tra 2023	R→	2.5 x8	TLP 372	Cpl	CpID 010:01010	Length 128	RequesterID 001:00:0	Tag 198	CompleterID 5000:00:0	Status SC	BCM E	lyte Cnt 2048	Lwr Addr 0x00	Data 128 dwords	VC ID 0	Explicit ACK Packet #31626	Metrics # Packe	ts Time Delta 188.000 ns
Link Tra 2024	R←	2.5 x8	TLP 3321	Mem	MRd(64) 001:00000	Length 512	RequesterID 001:00:0	Tag 26	Address 00000008:C000A800	1st BE 1111	Last BE	VC ID	Explici Packet f	t ACK #31615 Metrics	# Packe 2	ets Time Delta 80.000 ns	Time Stam 0000 . 050 425	p 410 s



NVMe performance optimization and stress testing **Teledyne LeCroy (Protocol Solutions Group)** 3385 Scott Boulevard Santa Clara, CA 95054 **Phone:** 800-909-7211 or 408-727-6600 **Fax:** 408-727-0800 Email Sales: contact.corp@teledynelecroy.com **Email Support:** psgsupport@teledynelecroy.com (Protocol Analyzers) Web Site: http://teledynelecroy.com/ **Phone Support:** 1-800-909-7112 or 408-653-1260



- SGL decoding challenges
- PCIe throughput analysis
- Long recordings analysis



SGL decoding challenges

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If (1) was a segment descriptor (2) can contain SGL segment descriptors, data descriptors, bit bucket descriptors or last segment descriptor.

- For each segment descriptor or data block, keyed data block or last segment descriptor there will be only ONE corresponding NVMe transaction line matching the corresponding address in the descriptor. This is how we implement a PRP transaction level line, based on PRP list
- For a bit bucket descriptor there will be NO corresponding NVMe transaction line
- No case will produce an NVMe transaction line that will not correspond to an address specified in a descriptor from a preceding segment
- Duplications, missing data or incorrect addresses should not create new transaction layer lines.
- Addresses outside the descriptor address range definition (address and length) will be classified unassociated traffic
- Missing address should be marked as error with a tooltip listing the missing address range
- Duplications can be optionally shown as errors, pointing to the duplicated packets.
- See examples in the bottom for cases where multiple NVMe transaction lines are shown for same descriptor

SGL decoding challenges

Flash Memory Summit

- Only the pointed to lines should show as NVMe transaction lines containing the complete range.
- If there are duplications they should collapse into that line.
- If addresses are missing they should be indicated as errors with tooltips showing the missing addresses.

	NVMe	Э н		evice ID	QID	CID	Address	SGL	0.01	Type	Address	Length	Zero	SGL	Typ	pe	Address	Length 2
	529			57:00:0	0x0001	0x0008	0000000:84247A80		SGL	Data BIOCK 0	0000020.7E0098	0010x00002080			ISGE Dat	авюск	000000207E00B880	0x0001E780
		3.9	Delt 59 us	a Tir s 0047.	ne Stamp 532 993 :	218 s												
	[™] NVMe	Эн	D	evice ID	QID	CID	Addres	SGL	Data	Data Len	* Data		MN			Metrics	# Link & Split Trans	Time Delta
	538			57:00:0	0x0001	0×0008	00000020:7E009800	UGE	Data	0x0000088	136 dwords	SAMSUNG MZ	WLL1T	бнен	P-000	Wrethes	2	383.750 ns
	NVMe 539	н		evice ID 57:00:0	QID 0x0001	CID 0x0008	Address 00000020:7E009A00	SGL	Data	Data Len 0x00000088	Data 136 dwords	SAMSUNG MZ	MN WLL1T	бнг	P-00003	Metrics	# Link & Split Trans 2	Time Delta 412.500 ns
	▼ NVMe 540	в		evice ID 57:00:0	QID 0x0001	CID 0x0008	Address 00000020:7E009C00	SGL	Data	Data Len 0x00000088	Data 136 dwords	SAMSUNG MZ	MN WU T	бнен	P-00003	Metrics	# Link & Split Trans 2	Time Delta 406.250 ns
	[™] NVMe 541	₽ н		evice ID 57:00:0	QID 0x0001	CID 0x0008	Address 00000020:7E009E00	SGL	Data	Data Len 0x00000110	Data 272 dwords	SAMSUNC	MN WLL1T	бнен	P-00003	▶ Metrics	# Link & Split Trans 4	Time Delta 804.500 ns
	NVM€ 543	е н		evice ID 57:00:0	QID 0x0001	CID 0x0008	Address 00000020:7E00A220	SGL	Data	Data Len 0x00000088	Data 136 dwords	SAL SUNG MZ	MN WLL1T	бнен	P-00003	Metrics	# Link & Split Trans	Time Delta 402.000 ns
	[™] NVMe 544	э н		evice ID 57:00:0	QID 0x0001	CID 0x0008	Address 00000020:7E00A420	SGL	Data	Data Len 0x00000088	Data 136 dwor	SAMSUNG MZ	MN WLL1T	бнен	P-00003	Metrics	# Link & Split Trans	Time Delta 416.500 ns
	[™] NVMe 546	е н		evice ID 57:00:0	QID 0x0001		Address	SGL	Data	Data Len 0x00000110	12 dwords	SAMSUNG MZ	MN WLL1T	бнен	P-00003	Metrics	# Link & Split Trans 4	Time Delta
	▼ NVMe 548	э н		evice ID		CID	Address	SGL	Dat	Ox00000088	Data	SAMSUNG MZ	MN WLL1T	бнен	P-0003	Metrics	# Link & Split Trans	Time Delta
	* NVMe	• н		evice ID		CID	Address	SGL	Data	Data Len	Data	SAMSUNC MZ	MN	BUEU	B 00003	Metrics	# Link & Split Trans	Time Delta
		э н		evice ID	QID	CID	Address	SGL	Data	Data Len	Data		MN	GHEN		Metrics	# Link & Split Trans	Time Delta
	550 * NVMe	э н		evice ID		CID	A dress	SGL	Data	Data Len	Data	SAMSUNG MZ	MN	бнен	P-00003	Metrics	# Link & Split Trans	Time Delta
	551			57:00:0	0x0001	0×0008	00000020:7E00B260			0x0000088	136 dwords	SAMSUNG MZ	WLL1T	бнен	P-00003		2	430.250 ns
	552	н		evice ID 57:00:0	QID 0x0001	0x0008	0000 020:7E00B460	SGL	Data	Data Len 0x00000088	Data 136 dwords	SAMSUNG MZ	WLL1T	бнен	P-00003	Metrics	# Link & Split Trans 2	Time Delta 408.500 ns
	[▼] NVMe 553	∍ н		evice ID 57:00:0	QID 0x0001	CID 0x0008	Address 000 0020:7E00B660	SGL	Data	Data Len 0x00000088	Data 136 dwords	SAMSUNG MZ	MN WLL1T	бнен	P-00003	Metrics	# Link & Split Trans 2	Time Delta 416.250 ns
Remove	[™] NVMe 554	н		evice ID 57:00:0	QID 0x0001	CID 0x0008	00000020:7E00B880	SGL	Data	Data Len 0x00000088	 Data 136 dwords 	SAMSUNG MZ	MN WLL1T	бнен	P-00003	Metrics	# Link & Split Trans	Time Delta 392.000 ns
	NVM€ 555	в		evice ID 57:00:0	QID 0x0001	CID 0x0008	Address 00000020:7E00BA80	SGL	Data	Data Len 0x00000088	Data 136 dwords	SAMSUNG MZ	MN WLL1T	бнен	P-00003	Metrics	# Link & Split Trans	Time Delta 406.250 ns
	[▼] NVMe 556	∍ н	P	evice ID 57:00:0	QID 0x0001	CID 0x0008	Address 00000020:7E00BC80	SGL	Data	Data Len 0x00000088	Data 136 dwords	SAMSUNG MZ	MN WLL1T	бнен	P-00003	Metrics	# Link & Split Trans	Time Delta 410.500 ns
	NVM€ 557	н		evice ID 57:00:0	QID 0x0001	CID 0x0008	Address 00000020:7E00BE80	SGL	Data	Data Len 0x00000110	Data 272 dwords	SAMSUNG MZ	MN WLL1T	бнен	P-00003	Metrics	# Link & Split Trans 4	Time Delta 798.250 ns

SGL – multiple data block descriptors

Flash Memory Summit

NVMe H Device ID QID SOYTDBL IO SOT MN Time Detta Time Stamp 0 158:00:0 0x0003 SOYTDBL IO SOT MM 1 268.000 ns 0005 . 202 880 068 s
NVMe Device ID QID CID Address OPC FUSE PSDT CID NSID MPTR Address Scl Type Address Length Zero SLBA NLB PRINEO 1 158:00:0 0x00000 0x000000 843C4D80 IOSQ OPC FUSE PSDT CID NSID MPTR 00000000:800400000 SCL SGL SGL
PRACT FUA LR AF AL SR I ILBRT LBAT LBAT MM Metrics # Link & Split Trans Time Delta Time Stamp 0 1 0 No frequency info None 0 0x00000 0xFFFF HUSMR7638BDP3Y1 1 6.380 us 0005 . 202 880 336 s
NVMe H Device ID QID CID Address SGL Type Address Length Zero SGL Type Address Length Zero SGL S
Type Address Length Zero ScL ScL<
Type Address Length Zero ScL
Type Address Length Zero ScL
Address Length Zero MN * # Link & Split Trans Time Delta Time Stamp 00000000:8404BEF0 0x00000100 0 HUSMR7638BDP3Y1 * 2 5.612 us 0005.202 886 716 s
NVMe H Device ID QID CID Address SGL Data Len Data MN Metrics # Link & Split Trans Time Delta Time Stamp 3 158:00:0 0x00006 0x000005:39E0E500 SGL Data Data Len Data MN Metrics 2 1.956 us 0005.202 892 328 s
NVMe H Device ID QID CID Address Data Len Data MN #Link & Split Trans Time Delta Time Stamp 4 158:00:0 0x00006 0x000005:39E0E800 SGL Data Data Len Data HUSMR7638BDP3Y1 #Link & Split Trans Time Delta Time Stamp 31.342 us 0005 . 202 894 284 s 31.342 us 0005 . 202 894 284 s 31.342 us 0005 . 202 894 284 s



 What is preventing the full duplex bus utilization of the read and write requests from maximizing data throughput in Setup B as opposed to Setup A configuration that shows maximum link utilization in both directions?





Setup B – instantaneous vs **Overall performance**

1st BE Last BE

		5.0	TIP	•
34	R⊷	x4	774	Mem
Link Tra 35	R←	5.0 x4	TLP 775	Mem
Link Tra 36	R←	5.0 x4	TLP 776	Mem
* Link Tra 37	R←	5.0 x4	TLP 777	Mem
Link Tra 38	R←	5.0 x4	TLP 778	Mem
Link Tra 39	R←	5.0 x4	TLP 779	Mem
Link Tra 40	R←	5.0 x4	TLP 780	Mem
Link Tra 41	R←	5.0 x4	TLP 781	Mem
-				
[°] Split Tra 0	R←	5.0 x4	Mem	MRd(000:00
* Split Tra 0 * Split Tra 1	R← R←	5.0 x4 5.0 x4	Mem Mem	MRd(000:0(MRd(000:0(
Split Tra 0 Split Tra 1 Split Tra 2	R← R←	5.0 x4 5.0 x4 5.0 x4 x4	Mem Mem Mem	MRd(000:0(MRd(000:0(MRd(000:0(
Split Tra 0 Split Tra 1 Split Tra 2 Split Tra 3	R← R←	5.0 x4 5.0 x4 5.0 x4 5.0 x4	Mem Mem Mem	MRd(000:00 MRd(000:00 MRd(000:00 MRd(000:00
Split Tra 0 Split Tra 1 Split Tra 2 Split Tra 3 Split Tra 4	R← R← R←	5.0 x4 5.0 x4 5.0 x4 5.0 x4 5.0 x4 5.0 x4	Mem Mem Mem Mem	MRd(000:00 MRd(000:00 MRd(000:00 MRd(000:00
* Split Tra 0 * Split Tra 1 * Split Tra 2 * Split Tra 3 * Split Tra 4 * Split Tra 5	R← R← R← R←	5.0 x4 5.0 x4 5.0 x4 5.0 x4 5.0 x4 5.0 x4 5.0 x4	Mem Mem Mem Mem	MRd(000:0(MRd(000:0(MRd(000:0(MRd(000:0(MRd(000:0(

010:00000	32	003:00:0	6	2F5	33980		1111	1111	32	dwords	0	Packet
Timing Calculate	or - [read_	write_traffic_bet	ween_	x86_and_	plx.pex]							×
From beginning o	f:			To begin	ning of:							
Packet 👻	0			Packet	-	91	227					
Marker 🔻				Marker	•							
Time 🔻		0.0000010020 \$	ecs	Time	-			0.0021786	5420 se	ecs		
Total Time:		2.178	millise	conds	•	•]						
Bus Utilization	1	Unstrea	m			D	ownstre	am				
Link Util	ization	41.4	 376 %				40.9	971 %		-		
Time Cov	verage	41.	826 %				40.9	925 %		-		
Ban	dwidth	8375.2	0 Mb/s			4	8194.1	7 Mb/s				
Data Throu	ughput	544.3	3 MB/s	>		<	550.34	4 MB/s	>			
Packets/s	second	190430	92.52				228495	606.81				
(Split Transact	tion Perfor	mance										
		Minimum		Ave	erage			Maximum		_		
(Response	Time	184.000 n	s	363	.540 ns		5	774.000 ns	3			
Latency		118.000 n	s	196	.070 ns		5	598.000 ns	3			
Throughput	(MB/s)	40.799		32	8.028			554.865				
Memory Write	es Perform	ance		1								
Respons	e Time	36.000 ns	;	121	. 160 ns		:	230.000 ns	5			
Throughput	(MB/s)	76.294		101	18.831			1649.599				

AL	JR.	Mot	trice	# Pack	ets	Resp. u	me	Plu. Byt	es	Intpl	VIB/S	Time De	ana
#1	10	wei	uncs	2		128.000	ns	128		953.	674	80.000	ns
	CK	1		#Pack	ets	Resp. ti	me	PId. Byt	es	Thrpt	MB/s	Time De	elta
1	13	Met	trics	2		128.000	ns	128		953.	674	80.000	ns
Ę			_	# Dook	ata	Deen ti				Throt		Time D	lto
	16	Met	trics	# Fack	ets	126 000	ne	129	es	207	576	28 000	ne
E	10		_	~		130.000	115	120		037.	510	00.000	113
.(CK	1 Me	trics	# Pack	ets	Resp. ti	me	PId. Byt	tes	Thrpt	MB/s	Time D	elta
1	20			2		128.000) ns	128		953.	674	84.000	ns
	CK	4	trian	# Pack	tets	Resp. ti	me	PId. Byt	tes	Thrpt	MB/s	Time D	elta
1	23	me	uncs	2		128.000) ns	128		953.	674	76.000	ns
	СК	1	-	# Pack	ets	Resp ti	me	PId Byt	es	Throt	MB/s	Time D	elta
1	26	Me	trics	2		130.000	ns	128		939.	002	80.000	ns
	21/2		_	# Deel		Deep di				Theat	UD/a	Time D	
	28 29	Me	trics	# Pack	ets	Resp. ti	me	PIG. Byt 112	es	200	MB/S	11me Do	ne
E	20		_	-		120.000	113	112		030.	030		113
1	< 1	Metri	ics 7	# Packe	ts F	Resp. tim	ie F	Id. Bytes	s Ti	hrpt M	B/s	Time Delt	a
P	0			2		68.000 n	s	8		112.19	97	270.000 n	S
j	nkTr	ras	Res	p. time	L	atency	Thr	pt MB/s	Pld	I. Bytes	в Т	'ime Delta	
	2		304.	000 ns	164	4.000 ns	20	00.774		64	1	60.000 ns	0
li	nkTr	ras	Res	p. time	L	atencv	Thr	pt MB/s	Pld	I. Bytes	5 T	ïme Delta	
Г	2		344.	000 ns	166	6.000 ns	35	54.856		128		32.000 ns	0
	nkTr	-	Pac	n timo		atoney	Thr	nt MR/c	DId	Dutor		imo Dolto	
ľ	2	a.ə	406	000 ns	216	6 000 ns	30	00 666		128		48 000 ns	0
	-	_								_			-
4	nkTr	ras	Res	p. time	L	atency	Thr	pt MB/s	Pld	I. Bytes		ime Delta	
Ь	2		428.	000 ns	242	2.000 hs	20	35.211		128		80.000 hs	10
i	nkTr	ras	Res	p. time	L	atency	Thr	pt MB/s	Pld	I. Bytes	в Т	'ime Delta	
Ŀ	2		432.	000 ns	244	4.000 ns	28	32.570		128		30.000 ns	0
i	nkTr	ras	Res	p. tim <u>e</u>	L	atency	Thr	pt MB/s	Pld	I. Bytes	5 Т	ime Del <u>ta</u>	
Г	2		440.	000 ns	246	6.000 ns	27	7.433		128	1	04.000 ns	0
	nkTr	rae	Res	n time		atency	Thr	nt MB/s	Pld	Byte		ime Delta	
ľ	2		408.	000 ns	220	0.000 ns	29	99.192		128		30.000 ns	0
11-													_

Flash Memory Sur	nmit Se	etup I	3 vie	wed i	n Lin	k Trac	ker	
Split Tra 17537 R← 5.0 Mem	MRd(32) RequesterID 00:00000 000:00:0	CompleterID Tag TC VC II	Address Status	Data Metrics # Lin	KTras Resp. time Latency	Thrpt MB/s PId. Bytes Time Delta	Time Stamp	
Link Tra 5.0 TLP	MWr(32) Length	RequesterID Tag Addr	ess 1st BE Last BE	Data VC ID Explicit A	CK #Packets F	Resp. time Pld. Bytes Thrpt MB/s	Time Delta Time St	amp
52376 x4 1163	10:00000 32	000:00:0 22 A151:	2380 1111 1111 3	32 dwords 0 Packet#15	53706 Metrics 2 1	68.000 ns 128 726.609	76.000 ns 0000 . 003 21	14 050 s
Link Tra 52377 R← 5.0 TLP 52377 X4 1164	Mem MWr(32) Length 10:00000 32	RequesterID Tag Addr 000:00:0 23 A151:	ess 1st BE Last BE 2400 1111 1111 3	Data VCID Explicit A 32 dwords 0 Packet #15	Metrics #Packets F 2 1	Resp. time PId. Bytes Thrpt MB/s 68.000 ns 128 726.609	Time Delta Time Sta 76.000 ns 0000 . 003 2	amp :14 126 s
	· ·							
Link Tracker - Packet # 153688								>
💷 🜬 🏥 🔍 🔍 📃 🔤	0x 10b RD Text Bir	n 1						
Time Packet #		Upstr	ream			Downstrea	im	
00.003 213 952	A3	A3 37	83	A3	57	4E	84	FD A
00.003 213 354		6B	48	DD				
00.003 213 958	F8	F2	9F	6B				
00.003 213 960	45 BE	E3 97	29 C5	DF F1				
00.003 213 964	E7	ČÉ	87	35				
00.003 213 966	48	DB	5C	61				
00.003 213 968	40	81	88	B6				
00.003 213 970	5C	A2	10	22 C6				
00.003 213 974	65	B5	8D	5A				
00.003 213 976	3B	7F	3B	F6				
00.003.213.978	CF	59 62	A4 AB	F9 95				
00.003 213 982	8D	50	AG	82				
00.003 213 984	17	72	0A	1F				
00.003 213 986	76 A6	83 BE	CD B8	80				
00.003 213 990	85	46	32	25				
00.003 213 992	9A	77	8F	C3				
00.003 213 994	F7 F5	14	A4 35	97 B8				
00.003 213 998	30	46	21	5B				
00.003 214 000	60	70	1F	81				
00.003 214 002	D6 C2	EE 09	5C 6E	E4 A7				
00.003 214 006	84	B9	78	29				
00.003 214 008	72	A4	44	AG				
00.003 214 010	57	AE 72	EF B1	BA 41				
00.003 214 012	77	úČ	FB	9B				
00.003 214 016	CD	FB	39	A0				-

- One direction Memory writes
- No downstream completions



FC Credits updating properly early in trace

P	acket		5.0	EC-Cr	L VC	ID HdrF	C DataF	TLP	Cnl	CpID	Length	RequesterID	Tag	CompleterID	Sta	tus E	всм	Byte Ont L	wr Addr	۲ (Data		LCI	RC	EC-Ch	VC
4	4283	TX 1	×4	10-01	0	126	2036	1824		10:01010	32	003:00:0	1	000:00:0	S		0	128	0x00	32 (dword		x4842	2C3D7	1.0-0.01	0
					_																					
I P	acket	D.	5.0	EO NE	VC	HdrF(D ataF(TLP	Marra	MRd(32)	Length	RequesterID	Tag	Address	1 st BE	E Last E	9E	LCRC		N N ID 1	V (ID)	HdrFC	🛯 🗋 Data	aFC	Time Delta	a j
	4284	7.6	×4	P C-INP		71	🚽 finite	1687	wern	00:00000	32	003:00:0	3	2F32A500	1111	1111	1	0xBC41270	0C	2-INP		70	Infir	nite	16.000 ns	3 0
P	acket		5.0	EO B	VC /	Harro	DataFC	TLP	More	MWr(32)	Length	RequesterID	Tag	Address	1st BE	Last BE	•	Data	L	CRC		о в V	9 🖉	HdrFC	DataFC	Tir
-	4286	75-	×4	FC-F	Ċ.	113	300	1688	Wern	10:00000	32	003:00:0	15	2F627200	1111	1111	32	dwords	0x43	19980	5	C-F	6	112	292	6.

FC Credits updating properly later in trace

Packet 5.0 29939 R⊷ 5.0 x4 DLLP ACK	AckNak_Seq_Num 583	CRC 16 0xA117	Idle Time S 0.000 ns 0000 . 000 .	tamp 720 128 s							
Packet R← 5.0 FC-NP VC F	drFC ataFC TLF	Mem	MRd(32) Length	RequesterID Tag	Address 2E411D00	1st BE Last BE	LCRC 0xA11AD34B	FC-NP	HdrFC	DataFC T	ime Delta
Packet 5.0 VC ID			MVVr(32) Length	RequesterID Tag	Address	1stBE LastBE	Data	LCRC			DataFC Tim
29942 R- x4 FC-P 0 1	13 300 3265	Mem	10:00000 32	003:00:0 17	2F58DC00	1111 1111 3	2 dwords	0x59DE7FFB	FC-P	112	292 14

FC updating properly for posted and non posted transactions



Setup B:Capture between switch and EP

Here we observe some overlap, but read throughput goes significantly down during write transfers.



Flast	(n Me	mor	v Si	ummit	Ç	Set	:up	o E	3: [,]	vie	W	ed	in	P	ac	ke	t N	Лc	de)		
E	Packet Packet	- P.	5.0		* Morro	MRd(3	32) I	Length	Request	erID Tag		dress Tess	1st BE	Last BE			4.5	i mi	cros	sec	late	ncy
	Packet 28575 Packet 28763		5.0 x4 5.0 x4	1482 TLP 1485 TLP 3785	Mem Mem	MRd(3 000:000 CpIE 010:01	32) I 000 000 010	22 Length 32 Length 32	Request 000:00 Request 000:00	erID Tag :0 6 erID Tag erID Tag :0 4	Add A13 Com 00	1B600 dress 1B680 pleterID 1:00:0	1111 1111 Status SC	1111 Last BE 1111 BCI 0	0×55550 LCR 0x8315 1 Byte Cnt 128	461E Uwr Add	4.484		Time Sta 00 . 000 60 LCR 0x9E8E	0 478 c 00 346 s C A285	Idle 6.000 ns	3
	28764 Packet 28765	R-	x4 5.0 x4	3786 TLP 3787	Срі Срі	010:01 CpIE 010:01	010 010 010	32 Length 32	000:00 Request 000:00	:0 1 erID Tag :0 0	00 ⁻	1:00:0 pleterID 1:00:0	SC Status		128 1 Byte Cnt 128	0x00 Lwr Addr 0x00	32 du 2003 32 du	words ata words	0x4F65 LCR 0xD4CA	E31A C 2770	Idle 6.000 ns Idle 30.000 n	3 IS
	Packet 28767 Packet 28770		5.0 x4 5.0 x4 x4	3788 TLP 3789	Cpl Cpl	010:01 010:01 010:01	010 010 010 010 010 010 010 010 010 010	32 Jength 32	Request 000:00	erito 1ag :0 3 erito Tag :0 2	Com 00 ⁻ 00 ⁻	pleterID pleterID 1:00:0	Status SC Status SC		A Byte Cht 128 A Byte Cht 128	Lwr Addr 0x00 Lwr Addr 0x00	32 du 32 du 32 du	ata words ata words	LCR 0xFE89 LCR 0x799D	7B15 2C E04E	6.000 n 6.000 n Idle 6.000 n	5
	Jtilizati	on 😂 I: Later	💷 🛙	▶<) [Ŭ]	. € . <u></u> ⊖	L 1⊕ 1⊖			×7													
Iransaction lime (µs	1 2 568 ∢ Time	570 ,590, s	D S	572 5 action Tin	74 57 ne:5µs, Sp	76 578 lit Transact	580 ion:3165	582 (All)	584	586	588	590	592	594	596 5	98 600	0 602	2 60	4 606	608	610	6:
nroughput (MB/s) 0 100 200	SPLI	570	ughpu	t Per Tran	saction	16 578	580	582	584		599	590	<u> </u>	594	596 5		603	2 60		608		

- We now display read transactions with their corresponding completions.
- Read requests tend to accumulate, varying their tags, with no completions until read requests stop.



The Analysis process

Flash Memory Summit

- The root cause for the stalled requests is the long completion latency from the switch side.
- We now question why does the endpoint Network Processor stop issuing requests?
- Option 1: The endpoint Network Processor has to allocate resources for the gueued up completions. This continues until the endpoint NP runs out of resources and then stops issuing read requests, note the 4.5 usec stall of read requests once all resources/tags have been exhausted.





Read Completion Credit analysis

Cpl	CpID 010:01010	Length 32	RequesterID 000:00:0	Tag 2	CompleterID 001:00:0	Status SC	BCM 0	Byte Cnt 128	Lwr Addr 0x00	×	Data dworda	LCRC 0x6A40BEE2	Time Delta 52 000 ns
Update	FC-Cpl VC ID	HdrFC D	ataFC CRC 16 2199 0x4B92		ime Delta 1 8.000 ns 0000	Fime Stamp . 000 598 89	8 s						<u> </u>
Cpl	CpID	Length	RequesterID	Tag	CompleterID	Status	BCM	Byte Cnt	Lwr Addr	>	Data	LCRC	Time Delta
	010:01010	32	000:00:0	5	001:00:0	SC	0	128	0x00	32	dwords	0xD5D48F20	52.000 ns
Update	FC-CpI VC ID	HdrFC D	ataFC CRC 16 2207 0x77FC		ime Delta 1 4.000 ns 0000	Time Stamp . 000 598 97	8 s						·
Cpl	CpID	Length	RequesterID	Tag	CompleterID	Status	BCM	Byte Cnt	Lwr Addr	>	Data	LCRC	Idle
	010:01010	32	000:00:0	1	001:00:0	SC	0	128	0x00	32	dwords	0x4F451C69	12.000 ns
Cpl	CpID	Length	RequesterID	Tag	CompleterID	Status	BCM	Byte Cnt	Lwr Addr)	Data	LCRC	Time Delta
	010:01010	32	000:00:0	7	001:00:0	SC	0	128	0x00	32	dwords	0x9942B7F7	4.000 ns
Update	FC-CpI VC ID	HdrFC D	ataFC CRC 16 2215 0x9061	7	ime Delta 6.000 ns 0000	Fime Stamp . 000 599 08	2 s						
Cpl	CpID 010:01010	Length 32	RequesterID 000:00:0	Tag 4	CompleterID 001:00:0	Status SC	BCM 0	Byte Cnt 128	Lwr Addr 0x00	32	Data dwords	LCRC 0xF8658EEB	Time Delta 176.000 ns
Cpl	CpID	Length	RequesterID	Tag	CompleterID	Status	BCM	Byte Cnt	Lwr Addr	•	Data	LCRC	Time Delta
	010:01010	32	000:00:0	0	001:00:0	SC	0	128	0x00	32	dwords	0xDB75DD52	240.000 ns
Cpl	CpID	Length	RequesterID	Tag	CompleterID	Status	BCM	Byte Cnt	Lwr Addr	•	Data	LCRC	Time Delta
	010:01010	32	000:00:0	2	001:00:0	SC	0	128	0x00	32	dwords	0xB09014F1	82.000 ns
Cpl	CpID	Length	RequesterID	Tag	CompleterID	Status	BCM	Byte Cnt	Lwr Addr	►	Data	LCRC	Time Delta
	010:01010	32	000:00:0	5	001:00:0	SC	0	128	0x00	32	dwords	0x5CCAD1C4	222.000 ns
Cpl	CpID	Length	RequesterID	Tag	CompleterID	Status	BCM	Byte Cnt	Lwr Addr	►	Data	LCRC	Fime Delta
	010:01010	32	000:00:0	7	001:00:0	SC	0	128	0x00	32	dwords	0x75CE9B7E	4.828 us
Update	FC-CpI VC ID	HdrFC D	ataFC CRC 16 2279 0x9FEF		ime Delta 24.000 ns 0000	Fime Stamp . 000 604 70	6 s						
Cpl	CpID	Length	RequesterID	Tag	CompleterID	Status	BCM	Byte Cnt	Lwr Addr	•	Data	LCRC	Idle
	010:01010	32	000:00:0	4	001:00:0	SC	0	128	0x00	32	dwords	0x9E8EA285	6.000 ns



Do we need to upgrade our hardware?

Flash Memory Summit

- Do we need more speed, higher link width, faster processor or more hardware acceleration?
 - Upgrading will create more heat issues so new mechanics and cooling mechanisms may be needed.
 - Upgrading means higher cost
 - Upgrading means more complexity
 - Upgrading means longer time to market and extended project completion
- With the correct tool one will be able to tell if his complex fabric has low performance because of low bandwidth and needs to be upgraded, or because of an unutilized link, pointing to the root cause of the low performance and exact component / issue to be addressed



NVMe Performance bottlenecks

- Performance bottlenecks in NVMe based platforms
 - PCI express flow control credit analysis.
 - Tradeoffs between memory resource allocation
 and data throughput
 - Virtual channels manage traffic in fabric
 - Allocate traffic classes to Virtual channels

Flash Memory Summit Analysis

get more

reports

Drill down to

Trace Expert- Performance

Performance Analysis

- Link Transaction Performance
- <u>Split Transactions Performance</u>
- <u>NVMe Performance</u>

Link Transaction Performance

Performance

Transaction Type	Total	# Packets (Min)	# Packets (Avg)	# Packets (Max)	Resp. time (Min)	Resp. time (Avg)	Resp. time (Max)	Pld. Bytes (Min)	Pld. Bytes (Avg)	Pld. Bytes (Max)
MsgD	1	2	2.00	2	1.412 us	1.412 us	1.412 us	4	4.00	4
CfgRd0	1072	2	2.00	2	1.236 us	1.375 us	1.620 us	0	0.00	0
CpID	565132	2	2.00	2	84.000 ns	1.636 us	2.396 us	1	63.87	64
CfgWr0	86	2	2.00	2	1.316 us	1.401 us	1.604 us	1	2.47	4
Cpl	163	2	2.00	2	68.000 ns	188.340 ns	308.000 ns	0	0.00	0
MWr(32)	2939	2	2.00	2	76.000 ns	1.011 us	1.692 us	4	4.00	4
MRd(32)	3	2	2.00	2	1.404 us	1.625 us	1.740 us	0	0.00	0
MRd(64)	71725	2	2.00	2	76.000 ns	202.820 ns	468.000 ns	0	0.00	0
MWr(64)	29700	2	2.00	2	148.000 ns	696.740 ns	988.000 ns	16	123.37	128
	670821									

Memory Writes Performance

Requester, TC	Total	Resp. time (Min)	Resp. time (Avg)	Resp. time (Max)	Pld. Bytes (Min)	Pld. Bytes (Avg)	Pld. Bytes (Max)	Thrpt MB/s (Min)	Thrpt MB/s (Avg)	Thrpt MB/s (Max)
000:00:0, TC0	1973	1.268 us	1.407 us	1.692 us	4	4.00	4	2.255	2.715	3.008
006:00:0, TC0	30667	76.000 ns	681.170 ns	988.000 ns	4	119.60	128	10.039	162.247	207.603
	32640									

Split Transactions Performance

Overall Performance	Ma		norto					
Requester -> Completer	т		epons		ime)	Resp. time (Avg)	Resp. time (Max)	
000:00:0 -> 000:00:0	6	2		2	102.880 us	123.771 us	179.312 us	
000:00:0 -> 006:00:0	1155	2		2	99.648 us	118.388 us	647.712 us	
006:00:0 -> 000:00:0	71718	2		9	1.848 us	6.673 ms	9.859 ms	
	72879							

8/16/17



Recording duration using 32K DW NVMe Transfers

Best Case Gen 1 x1 Lane Width Worst Case Gen 3 x4 Lane Width

	Link Utilization (%)							Link Utilization (%)					
TLP size (dw)	100	90	80	70	60		TLP size (dw)	100	90	80	70	60	
32	139.8164	155.3516	174.77 <mark>05</mark>	199.737 <mark>8</mark>	233.0274		32	8.875 <mark>066</mark>	9.861185	11.093 <mark>83</mark>	12.67867	14.79178	
64	128.817	143.13	161.0213	184.0243	214.695		64	8.176861	9.085 <mark>402</mark>	10.22108	11.68123	13.6281	
128	123. <mark>3173</mark>	137. <mark>0</mark> 192	154.1466	176.16 <mark>76</mark>	205.528 <mark>9</mark>		128	7.82 <mark>775</mark> 9	8.69751	9.784 <mark>699</mark>	11.182 <mark>51</mark>	13.04627	
256	120.5675	133.9638	150.7093	172.23 <mark>92</mark>	200.945 <mark>8</mark>		256	7.653208	8.503564	9.56 <mark>651</mark>	10.933 <mark>15</mark>	12.7553 <mark>5</mark>	
512	119.1925	132.4361	148.9 <mark>907</mark>	170.275	198.654 <mark>2</mark>		512	7.56 <mark>5932</mark>	8.406591	9.457 <mark>415</mark>	10.80847	12.6098 <mark>9</mark>	
1024	118.5051	131.6723	148.1313	169.2 <mark>93</mark>	197.508 <mark>4</mark>		1024	7.522294	8.358105	9.402 <mark>868</mark>	10.74613	12.5371 <mark>6</mark>	
2048	118.1613	131. 290 4	147.7017	168.80 <mark>19</mark>	196.935 <mark>6</mark>		2048	7.500475	8.333862	9.375 <mark>594</mark>	10.714 <mark>96</mark>	12.5007 <mark>9</mark>	
4096	117.9895	131.0994	147.4868	168.5564	196.6491		4096	7.489566	8.32174	9.361 <mark>958</mark>	10.69938	12.48261	

**Note: Capture Duration Doubles when using T34 expanded mode