Forum E-21: Controllers and Flash Technology Part 1 (Controllers Track)

Organizer/Chairperson:

Erich F. Haratsch

Seagate

Agenda

Title	Speaker	Duration (mins)
Introduction	Erich Haratsch	5
Optimizing a SSD Controller Platform for Reusability and Reliability	Andrei Vityaev	25
Using Machine Learning to Enhance Flash Endurance & Latency	Cloud Zeng	25
ECC Approach for Correcting Errors Not Handled by RAID Recovery	Jeff Yang	25
Break		10
Performance Advantages of Spatially Coupled ECC in Flash Memories	Homa Esfahanizadeh	25
SSD Lifetime Extension using Multi-Code-Rate LDPC with Multi-Dimensional LLR	Rino Micheloni	25

Thank You! Questions?

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